		Technology	Module Speed (MT/s)	Clock Cycle Time (ns)	CAS Latency (CL)	Input "what if"		Clock Cycle Time * # of Cycles = Total Completion Time	
						Clock Cycle Here			
Examples--->									
		SDR	*100*	*8.00*	*3*	*3*		24	ns
		SDR	*133*	*7.50*	*3*	*3*		22.5	ns
		DDR	*335*	*6.00*	*2.5*	*2.5*		15	ns
		DDR	*400*	*5.00*	*3*	*3*		15	ns
		DDR2	*667*	*3.00*	*5*	*5*		15	ns
		DDR2	*800*	*2.50*	*6*	*6*		15	ns
		DDR3	*1333*	*1.50*	*9*	*9*		13.5	ns
		DDR3	*160B*	*1.25*	*11*	*11*		13.75	ns
		DDR4	*1866*	*1.07*	*13*	*13*		13.91	ns
		DDR4	*2133*	*0.94*	*15*	*15*		14.1	ns
		DDR4	*2400*	*0.83*	*17*	*17*		14.11	ns
		DDR4	*2666*	*0.75*	*18*	*18*		13.5	ns
			Just save-as this spreadsheet as your own; can't have nice things.						
		Your Memory (input here)							
	Speed (e.g. 2400, 2666, etc):		2666	15	15	15	35		
	Desired Speed		2933	16.50225056	16.50225056	16.50225056	38.50525131		
			3200	18.00450113	18.00450113	18.00450113	42.01050263		
				You might want to protect these cells too,,,					
				(*^-^*)					