

IXPUG2016 Agenda

Thursday, September 22, 2016

Programming and Visualization (Room 1416)

Start	End	Title	Author(s)
8:15 AM	9:00 AM	Breakfast	
9:00 AM	9:30 AM	Heterogeneous Programming of Intel Xeon and Intel Xeon Phi with Intel TBB Flow Graph	Evgeny Fiksmen, Michael Voss and Sergey Vinogradov
9:30 AM	10:00 AM	LLVM and Clang on the KNL	Hal Finkel
10:00 AM	10:10 AM	MPI + hStreams in NEMO5: Partitioning Xeon Phi	Xinchen Guo, Daniel Lemus, Daniel Mejia, Jim Fonseca, Gerhard Klimeck and Tillmann Kubis
10:10 AM	10:20 AM	Process and Thread Affinity with MPI/OpenMP on KNL	Helen He
10:20 AM	10:30 AM	Effective OpenMP Implementations on Intel's Knights Landing	Yuliana Zamora
10:30 AM	11:00 AM	Break	
11:00 AM	11:30 AM	Visualization with OSPRay: Research and Production	Aaron Knoll and Ingo Wald
11:30 AM	11:45 PM	Visualization with vI3. Now on the CPU	Silvio Rizzi, Joseph Insley and Aaron Knoll
11:45 PM	1:00 PM	Lunch	
1:00 PM	2:00 PM	Performance Tuning on KNL with Intel® Parallel Studio XE 2017 Cluster Edition	James Tullos

Working Group Meeting (1404)

Start	End	Title	Author(s)
2:00 PM	3:30 PM	Joint Working Group Meetings	John Pennycook and WG members

Key: Tutorial Keynote Presentation Lightning Talk