

IXPUG2016 Agenda

Monday, September 19, 2016

Programming (Room 1416)

Start	End	Title	Author(s)
8:15 AM	9:00 AM	Breakfast	
9:00 AM	12:00 PM	Advanced MPI Programming	Pavan Balaji
12:00 PM	1:00 PM	Lunch	
1:00 PM	3:00 PM	Advanced OpenMP	Lars Koesterke and Kent Milfeld
3:00 PM	3:30 PM	Break	
3:30 PM	5:30 PM	Ensuring Program Correctness and Optimal Performance on Intel Phi Using Alinea Forge	Ryan Hulguin and Beau Paisley

Techniques (Room 1407)

Start	End	Title	Author(s)
8:15 AM	9:00 AM	Breakfast	
9:00 AM	12:00 PM	Vectorization Strategies for Intel's 2nd Generation Intel Xeon® Phi™ Architecture Codenamed Knights Landing	Ashish Jha, Vitali Morozov and Jack Deslippe
12:00 PM	1:00 PM	Lunch	
1:00 PM	5:00 PM	Top Ten Issues for Effectively Utilizing a Large KNL System	John Levesque

Key: Tutorial Keynote Presentation Lightning Talk