

# ASIC subWG

Alexandre Camsonne  
11/3/2022

# Introduction

Organize subWG for easier interaction with project ( Fernando ), detector WG and eRD WG

Proposed starting members : Alexandre Camsonne, Chris Cuevas, Fernando Barbosa, Irakli Mandjavic, Pietro and detector ASICs

# Tasks of working group

- Gather information about potential ASICs  
Keep updated list
- Ensure ASICs candidates are compatible with a streaming DAQ
- Keep close contact with detector WG
- Keep close contact with project and
- Help with testing and development of new ASICs
- Propose ASICs for detector WG who are still in development phase or could benefit
- Provide information to ASICs developer on how to interface with foreseen ePIC DAQ
-