



RISE

RISC-V Software Ecosystem

Kernel and Virtualization Working Group
26/08/2024



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Agenda

- Kernel and Virtualization TSC deepdive was done on 22-08-2024
 - [slides](#)
- RISC-V Summit NA 2024
- Open floor (remaining time)

RISC-V Summit NA 2024



- Sessions accepted for RISC-V summit NA 2024
 - Schedule: <https://events.linuxfoundation.org/riscv-summit/program/schedule/>
 - RISC-V ACPI is ready for server platforms (Ventana, Sunil & Himanshu)
 - RISC-V RAS error record interface (Ventana, Greg)
 - RISC-V server SoC standardization (Rivos, Ved)
 - RISC-V control flow integrity (Rivos, Ved & George)
 - The benefits of building a new AI accelerators with RISC-V (Google, Martin Maas)
 - Berberis: Dynamic binary translation from RISC-V to x86 on Android (Google, Lev)
 - An adaptive interrupt arch. for extremely timing critical applications (Samsung)
 - Enhance the performance of QEMU RVV load/store (SiFive)
 - SiFive Event Trace (SiFive)
 - ... any other sessions from RISE members ??? ...
- RISE lounge at RISC-V summit NA 2024

Open floor

- Any other topics or issues ??