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</div><div>[KEMBALI KE MENU SEBELUMNYA]</div>
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<div style="text-align: left;">1. Jurnal</div><div style="text-align: left;">2. Alat dan Bahan</div><div style="text-align: left;">3. Rangkaian Simulasi</div><div style="text-align: left;">4. Prinsip Kerja Rangkaian
<div>5. Video Rangkaian</div><div>6. Analisa </div><div>7. Link Download</div></div></div><div style="text-align: left;">
</div><div style="text-align: left;">1. Jurnal[Back]</div><div style="text-align: left;"><div class="separator" style="clear: both; text-align: center;"><div class="separator" style="clear: both; text-align: center;">
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3.2&nbsp;</b>Percobaan dengan mengganti B1 dengan Dclock&nbsp;</span></div><div
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gelombang output dari masing-masing logika</span></div></span></div><div style="text-align:
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roman"; serif;">Gerbang logikaNOT, AND, NAND, OR, NOR, XOR, dan XNOR disusun sejajar secara vertikal dan setiap inputnya dihubungkan pada input yang sama berupa logicstate dan DC Clock. Perubahan pada output terhadap input dapat dilihat pada rangkaian. Ketika input hanya berupa 2 logicstate maka hasil keluaran akan bersifat stabil dan tidak berubah-ubah. Namun pada input yang menggunakan 1 logicstate dan 1 DC Clock akan menghasilkan output yang berubah-ubah atau tidak stabiil, tergantung pada prinsip dari masing-masing gerbang logika .</div><div style="text-align: left;">
</div><div style="text-align: left;">5. Video Rangkaian[Back]</div><div style="text-align: left;">
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</div><div style="text-align: left;"><div style="background-color: white; box-sizing: border-box; font-family: times, "times new roman"; serif;">Pada percobaan 1 ini, kami menguji prinsip kerja gerbang logika dasar seperti NOT, AND, NAND, OR, NOR, XOR, dan XNOR yang ditambah atau dihubungkan dengan input yang sama, yaitu dengan logicstate dan juga DC Clock. Kemudian, tiap gerbang logika kami uji dan perhatikan perubahan outputnya terhadap inputnya. Hasil dari output terhadap perubahan input kami catat dalam bentuk tabel pada jurnal. Analisa prinsip kerja gerbang logika yang dapat kami simpulkan adalah sebagai berikut:</div><div style="background-color: white; box-sizing: border-box; font-family: times, "times new roman"; serif;"><ul style="box-sizing: border-box;"><li style="box-sizing: border-box;">NOT adalah gerbang logika yang outputnya kebalikan dari inputnya.<li style="box-sizing: border-box;">AND adalah gerbang logika yang membutuhkan kedua atau semua input bernilai 1 agar output dapat bernilai 1. Ketika tidak semua input bernilai 1 maka mustahil output akan bernilai 1.<li style="box-sizing: border-box;">NAND merupakan kebalikan dari AND, dimana ketika semua input bernilai 1, maka output akan bernilai 0. Sementara ketika salah satu input bernilai 1 atau semua input bernilai 0 maka output menghasilkan nilai 1.<li style="box-sizing: border-box;">OR hanya membutuhkan salah satu input atau semua input bernilai 1 untuk menghasilkan output bernilai 1. Sementara, jika semua input di-nol-kan, maka hasil output berupa nol.<li style="box-sizing: border-box;">NOR adalah kebalikan dari OR. Dimana ketika salah satu input dan semua input bernilai 1 maka menghasilkan output bernilai 0. Sementara, jika semua input di-nol-kan, maka hasil output berupa 1.<li style="box-sizing: border-box;">XOR adalah exclusive OR, dimana XOR ini menggunakan prinsip ganjil-genap. Ketika input ganjil, maka output berupa 1. Sementara jika genap, output berupa 0.<li style="box-sizing: border-box;">XNOR adalah kebalikan dari XOR, dimana ketika input genap maka output 1. Lalu, jika input ganjil maka output 0.<li style="box-sizing: border-box;">DC Clock membuat output dari gerbang logika berubah-ubah atau switching. Jika digambarkan dalam bentuk sinyal pulsa maka akan menghasilkan gelombang yang naik (1) dan turun (0). Namun ada juga yang menghasilkan sinyal lurus yang stabil, bisa stabil di 0 (LOW) maupun stabil pada 1 (HIGH).</div></div><div style="text-align: left;">
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font-size: medium;">7. Link Download[Back]</div><div style="text-align: left;">
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w_" style="color: #1a8e57; cursor: url("https://ani.cursors-4u.net/games/gam-16/gam1571.ani"), url("https://ani.cursors-4u.net/games/gam-16/gam1571.png"), auto; text-decoration-line: none;">[klik disini]<li style="background-color: white; cursor: url("https://ani.cursors-4u.net/games/gam-16/gam1571.ani"), url("https://ani.cursors-4u.net/games/gam-16/gam1571.png"), auto; font-family: Georgia, Utopia, "Palatino Linotype", Palatino, serif; font-size: 15.4px; margin: 0px 0px 0.25em; padding: 0px;">Download Datasheet Gerbang OR (2 input) [klik disini]<li style="background-color: white; cursor: url("https://ani.cursors-4u.net/games/gam-16/gam1571.ani"), url("https://ani.cursors-4u.net/games/gam-16/gam1571.png"), auto; font-family: Georgia, Utopia, "Palatino Linotype", Palatino, serif; font-size: 15.4px; margin: 0px 0px 0.25em; padding: 0px;">Download Datasheet Gerbang NAND (2 input) [klik disini]<li style="background-color: white; cursor: url("https://ani.cursors-4u.net/games/gam-16/gam1571.ani"), url("https://ani.cursors-4u.net/games/gam-16/gam1571.png"), auto; font-family: Georgia, Utopia, "Palatino Linotype", Palatino, serif; font-size: 15.4px; margin: 0px 0px 0.25em; padding: 0px;">Download Datasheet Gerbang NOR (2 input) [klik disini]<li style="background-color: white; cursor: url("https://ani.cursors-4u.net/games/gam-16/gam1571.ani"), url("https://ani.cursors-4u.net/games/gam-16/gam1571.png"), auto; font-family: Georgia, Utopia, "Palatino Linotype", Palatino, serif; font-size: 15.4px; margin: 0px 0px 0.25em; padding: 0px;">Download Datasheet Gerbang XOR (2 input) [<a href="https://drive.google.com/uc?export=download&id=1R3nZMAI0yJznkcitOll-3PTEbwmkO_t

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