Join DesignNex as a DFT Intern!

Are you passionate about Design for Test (DFT) and eager to enhance your skills in scan insertion, ATPG, and coverage analysis? Ready to elevate your career with a team that values innovation and collaboration? DesignNex is looking for talented DFT Interns like you for the Noida location!

What We're Looking For:

+1 year of DFT experience (internships included)

+ Expertise in scan insertion, ATPG, MBIST and coverage analysis

+ Proficiency in scripting (Perl, Shell, etc.)

+Knowledge of Synopsys TetraMax/DFTMax and Cadence Encounter Test is a plus

+ Excellent problem-solving and communication skills

Ready to Take the Next Step?

If you're driven, passionate, and ready to make an impact, we want to hear from you! Apply now and let's build something incredible together at DesignNex in Noida.

DE How to Apply:

Submit your resume and cover letter to akshay.soni@designnex.com with the subject line "DFT Intern Application - [Your Name]".