## K.S. RANGASAMY COLLEGE OF TECHNOLOGY, TIRUCHENGODE - 637215

## (Autonomous)

# DEPARTMENT OF ELECTRONICS ENGINEERING (VLSIDT) MODES OF CONTENT DELIVERY ROLE PLAY

Date: 17.09.2025

Batch : 2024-2028 Academic Year : 2024-25

Course code/ Name : 60 EV 303/Digital Logic Design

Year/Semester/Section : II/III

Course Instructor : Mr.D.Poornakumar

## **Topic: Functions of Logic Gates**

To better understand the concept of logic gates and their functions, a role play activity was conducted in the classroom. The role play involved students and the teacher acting out the different logic gates and explaining their operations. This interactive method helped in grasping the basic concepts of digital electronics in a fun and engaging way.

## **Objective**

- ❖ To explain the basic functions of common logic gates (AND, OR, NOT, NAND, NOR, XOR, XNOR).
- ❖ To illustrate how logic gates process binary inputs to produce outputs.
- To encourage active participation and understanding of digital system design fundamentals.

## **Roles and Responsibilities**

Teacher: Facilitated the role play by asking questions, guiding explanations, and summarizing concepts.

Students: Took turns explaining and demonstrating different logic gates, their truth tables, and real-world applications.

## **Observations**

- ✓ The role play helped students visualize and remember the functions of different logic gates better.
- ✓ Active participation increased student engagement and interest in digital electronics.
- ✓ Clarification of doubts was easier through interactive questioning and demonstration.



Course Instructor HoD