Organizations Attending:

- Eclypsium
- Intel
- Google
- Insyde
- TCSL
- Healthcare

Erik to draft Contribution Guidelines on wiki.

- Time for reviews
- 2 Approvals
- Shouldn't break (Travis CI, etc)

Current Focus Areas and Future Roadmap

- Eclypsium
    - New Vulnerabilities & Visibility
        - Eg. Devices
    - Validate Decode Functionality
- Intel
    - Release
    - Cleanup
        - Eg. XML / Logging
        - Common ASM
    - Reorganize config files and drivers to allow support for other architectures/devices
    - Evangelization
        - Within Intel - working with the Open Source Technology Center and attending a customer-focused security event in December (Stephano)
        - FOSDEM - Specifically the hardware dev room and the distribution room. (Stephano)
        - TianoCore - Mentioning or getting Intro-Level talks to CHIPSEC at various events in 2019. Specifically LinuxFest Northwest, OSCON, etc. (Stephano)
        - BSides PDX - working on posting content
        - CanSec
        - Open Idea: Video Series
        - Slides Posted from OSFC
    - Python3 Support
- Google

    - Mostly a User (and some patching) - not looking at implementing a major feature right now
    - Pain Points - installation/execution process, stability, unit testing
- Insyde
    - User - maybe some debug
    - Pain Points - will come back with things. Please open github issues for what you find.
- TCSL
    - Trying to rebuild whitelist from ATR repo. It hasn't been updated in 2 years. Looking for help.
        - Want to make the original EFI files (as well as the hash) available so that we can do more.
    - **Discuss with Eclypsium offline. Will come back to this meeting with more plans.**
        - Insyde could get some interest in providing EFIs.
        - **John/Arun can create a wiki page for this project.**
- Healthcare
    - **Request - optional flag to generate a more verbose report**
        - **Please open a github issue for this**
    - Pain Points - wrapping chipsec into a customer Ubuntu image to boot live. We have to hand that off to engineering to audit. They have to:
        - Turn off secure boot
        - Boot/run on device
        - SSH results
    - We would really like to reduce the time they spend on this.
    - **We should point (readme.md) at the LUV project as a bootable live image with CHIPSEC - Erik**
        - **We could have a CHIPSEC yocto meta-layer. Stephano will start this.**
        - LUV also includes FWTS.
    - Pain point around turning off secure boot
        - Maybe work with LUV and leverage Grub/shim so you don't have to turn off secure boot.

Intel Open - trying to check that we have tests for publicly known issues. If you find something we missed, email list or chipsec@intel.com

Chipsec-dev mailing list - epic fail by john. Erik will save us.

John - Schedule this meeting Quarterly.