



Geethanjali College of Engineering and Technology

Department of Electronics and Communication Engineering

Center of Excellence in VLSI

Events Conducted

S.No	Title	Category	Date and Time	Place
1	Satvara Think-Quick	Online Quiz	6th June 2021. From 07:00 PM To 07:30 PM	Online
2	BOOLEAN MASTER	Online Quiz	29th June 2021. From 07:00 PM To 08:00 PM	Online

Ms.G.SreeLakshmi
Co-cordinator CoE-VLSI Design

Prof.O.V.P.R.Siva Kumar
Cordinator CoE-VLSI Design

Dr.S.Suryanarayana
Prof & HoD-ECE



Report of

“Satvara Think-Quick 4 .0 On-line Quiz Competition”

Organized by IEEE CAS Student Branch Chapter-GCET

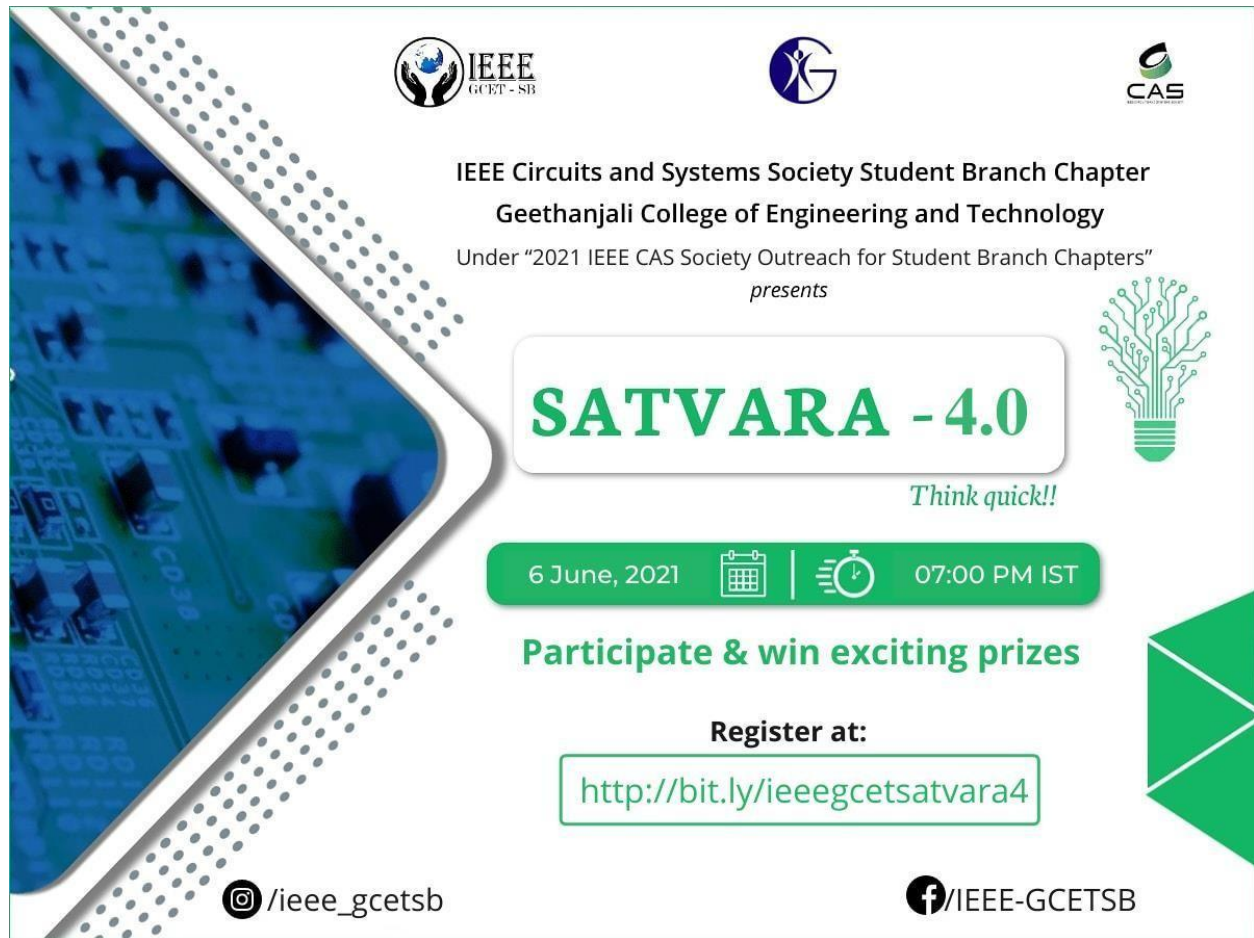
Dates: 6th June 2021. From 07:00 PM To 07:30 PM

- No. of Student Registrations: 170
- No. of Student participants: 155
- Target Students: B.Tech II and III Yr Students
- No. of IEEE Student Member participants: 34
- No. of Non-IEEE Student Member participants: 121
- Registration Fee: Nil

Agenda: To promote a thorough conceptual understanding of Digital Electronics and its related areas

About Event:

IEEE Circuits and Systems Society Student Branch Chapter GCET organized "SATVARA-Think quick!! 4.0" an online quiz competition on digital electronics and its related areas. This was the online quiz competition conducted by IEEE CAS SBC GCET and the motto behind conducting this event is to promote a thorough conceptual understanding of digital electronics. It consisted of 25 questions related to digital electronics.



IEEE Circuits and Systems Society Student Branch Chapter
Geethanjali College of Engineering and Technology
Under "2021 IEEE CAS Society Outreach for Student Branch Chapters"
presents

SATVARA - 4.0

Think quick!!

6 June, 2021 | 07:00 PM IST

Participate & win exciting prizes

Register at:
<http://bit.ly/ieeegcetsatvara4>

@ieeee_gcetsb | /IEEE-GCETSB

The winners of SATVARA-Think quick!! 4.0 Online Quiz competition are:

s.no	Name	Percentage	College	Cash prize
1	Errabothu Mamatha	92%	G Narayanamma institute of technology	1500
2	Harshini yamana	72%	Stanley college of engineering and technology	1000

3	Simram Kumari	72%	Ramgarh engineering college	500
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Event Coordinators:

1. M. Keerthana (18R11A0432 : ECE-3A)
2. Y. Saketh (18R11A0446 : ECE-3A)
3. G.Akhila (18R11A0463 : ECE-3B)
4. G.Haasya (18R11A04A8 : ECE-3C)

Report prepared and drafted by:

Saketh Kumar Yama

Chair, IEEE SB GCET



Report of

“BOOLEAN MASTER On-line Quiz Competition”

Organized by IEEE CAS Student Branch Chapter-GCET

Dates: 29th June 2021. From 07:00 PM To 08:00 PM

- No. of Student Registrations: 321
- No. of Student participants: 321
- Target Students: B.Tech II and III Yr Students
- No. of IEEE Student Member participants: 85
- No. of Non-IEEE Student Member participants: 236
- Registration Fee: Nil

Agenda: To promote a thorough conceptual understanding of Boolean Algebra and Logic Gates and its related areas

About Event:

IEEE Circuits and Systems Society Student Branch Chapter GCET organized "BOOLEAN MASTER" an online quiz competition on Boolean Algebra and Logic Gates and its related areas. This was the online quiz competition conducted by IEEE CAS SBC GCET and the motto behind conducting this event is to promote a thorough conceptual understanding of Boolean Algebra and Logic Gates. It consisted of 25 questions related to digital electronics.





**IEEE Circuits and Systems Society Student Branch Chapter
Geethanjali College of Engineering and Technology
presents**



BOOLEAN MASTER

Participate and win exciting prizes

Register at :
<https://bit.ly/booleanmaster>



 **7.00 PM**

 **29th JUNE 2021**

 **/ieee_gcetsb**

 **/IEEE-GCETSB**

The winners of “BOOLEAN MASTER” Online Quiz competition are:

s.no	Name	Percentage	College	Cash prize
1	K. Prerana	92%	Geethanjali College of Engineering and Technology	1500
2	Hariharan R S	88%	Vellore Institute of Technology, Vellore	1000

3	Redyam Bhargavi	88%	G Narayanamma Institute of technology and science	500
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Event Coordinators:

1. M. Keerthana (18R11A0432 : ECE-3A)
2. Y. Saketh (18R11A0446 : ECE-3A)
3. G.Akhila (18R11A0463 : ECE-3B)
4. G.Haasya (18R11A04A8 : ECE-3C)

Report prepared and drafted by:

Saketh Kumar Yama

Chair, IEEE SB GCET