

Architecture des ordinateurs :

Exercice :

```
list p=18f4550, r= dec
include <p18f4550.inc>
org 0x00
comp1 equ 0x77
comp2 equ 0x78
comp3 equ 0x47
// configurer les sorties
Bcf TRISB,0
Bcf TRISB,1
Bcf TRISB,2
Movlw 0x0E    // numeriques
Movwf ADICON1
//Configurer les entrés
Bsf TRISE,0
Bsf TRISE,1
Bsf TRISE,2
Movlw 0x0A    // numeriques
Movwf ADICON1
Movlw 0x07
Movwf CMCON
Bcf PORTB,0
Bcf PORTB,1
Bcf PORTB,2
```

//Allumer les diodes

Boucle

BTFSC PORTE,0

Goto First

Goto Both

Goto boucle

First

Bsf PORTB,0

Bcf PORTB,1

Bcf PORTB,2

Call time

End

Both

Btfsc PORTE,1

Goto second

Goto third

End

Second

Bcf PORTB,0

Bsf PORTB,1

Bcf PORTB,2

Call time

End

Third

Bcf PORTB,0

Bcf PORTB,1

Bsf PORTB,2

Time //50ms

Movlw 0x250

Movwf compt1

Movlw250

Movwf comp2

Boucle1

Nop

Decfsz compt1,f

Goto boucle1

Goto boucle2

Boucle2

Decfsz compt2,f

Goto boucle2

End

Call time

End