

Date: 01/10/20

Attendees: Reza, Tawfik, Kevin, Ali, Christian, Jaideep, Rita, DJ

Agenda:

1. Slides up at the wiki
2. Agenda for the global summit set - three talks, two panels
 - a. ODSA - 3 hours on March 5th at 9 AM
 - b. Three talks: Case for chiplets, Software architecture, ODSA year in review
 - c. Two panels:
 - i. D2D workflow panel,
 - ii. Open PHY panel (Cadence, Marvell, Synopsys, Xilinx) - still looking for a moderator
3. D2D workflow panel moderated by Rita
 - a. Looking for panelists
 - b. Have an AMD architect - can discuss latency, power, clock distribution - also HBM memory.
 - c. Sam met with Intel person who led Kaby Lake - can talk about business workflow or system architect
 - d. Looking for panelists experienced in multi-chiplet workflow
 - i. Packaging - Tawfik can suggest some names from Intel
 - ii. Test
 - iii. System architect
4. DJ - need a new workstream discussion
5. Next week - brief year in review discussion.