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1. Addition of two BCD digits requires two Parallel Adder Circuits.
a) 2-bit
b) 8-bit
c) 16-bit
d) 4-bit
Answer: d) 4-bit
2. In CMOS 5 Volt series, the input voltage of Logic high signal (VIH) with a range from to volts.
a) 4.5, 5
b) 0, 3.5
c) 3.5, 5
d) 0, 5
Answer: c) 3.5, 5
3. Subtractors also have an output to check if 1 has been
a) Complemented
b) Primed
c) Shifted
d) Borrowed
Answer: d) Borrowed
4. The output of an AND gate is one when
a) All of the inputs are one
b) Any of the input is one
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c) All the inputs are zero	
d) Any of the input is zero	
Answer: a) All of the inputs are one	
5. A 3-variable Karnaugh map has cells.	19
a) Three	0/0
b) Sixteen	VO.
c) Four	
d) Eight	18 N
Answer: d) Eight	2
	2)
6 single digit BCD Adders can be cascaded together to form	a 2-digit BCD Adder.
a) 1	
b) 2	
c) 3	
d) 4	
Answer: b) 2	
7. A.(B + C) = A.B + A.C is the expression of	
a) Demorgan's Law	
b) Commutative Law	
c) Distributive Law	
d) Associative Law	
Answer: c) Distributive Law	
8. Which one is true?	

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Power consumption of CMOS is higher than TTL. Power consumption of TTL is higher than CMOS. Power consumption of both CMOS and TTL depends on the number of gates in the circuit. Both TTL and CMOS have the same power consumption. (Choose the correct option.) 9. The digital circuits operate with which combination of values given below?

-5 volts, 0 volts

-5 volts, +5 volts

+5 volts, 0 volts

0 volts, 0 volts

(Choose the correct option.)

Answer: c) +5 volts, 0 volts 10. The series of TTL chips are characterized by their a) Switching Speed Only b) Power Dissipation Only c) Both d) None of the above Answer: a) Switching Speed Only

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