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He completed his dual major of Bachelor of Physics and Bachelor of Electrical Engineering from University of Science and Technology of China in 2019. After that, he joined Prof. Aaton Thean's group in August 2019, where he is focusing on system technology co-optimization of computing architectures with electrical components based on novel materials. He is working on error correction method for non-volatile memory applications, spiking neural network based on bi-direction switching mem-transistor and monolithic 3D stackable compute-in-memory architecture infused with BEoL transistors.

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