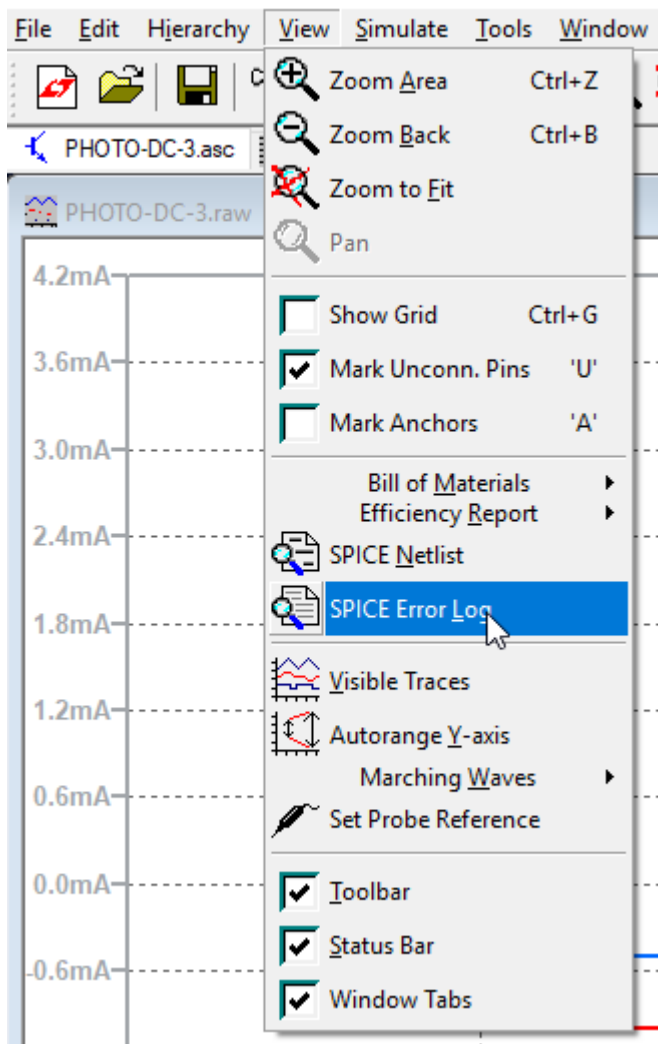


Симулация с LTSpice и измерване ток на късо съединение и напрежение на празен ход

1. Симулирайте схемата
2. Отворете Spice Error Log



- 3.

```

Ignoring empty pin current: Ix(x1:in)
.step vlight=0.001
.step vlight=0.002
.step vlight=0.003

```

Measurement: isc I_{кc}

step	i(v1)	at
1	-0.000495025	0
2	-0.000728177	0
3	-0.000784511	0

Measurement: uoc U_{пх}

step	v(b) - v(a)	at
1	0.4582	-0.4582
2	0.482405	-0.482405
3	0.496563	-0.496563

Date: Wed Dec 05 12:37:01 2018
Total elapsed time: 0.527 seconds.

