Report on Introduction to HDL by IEEE

CAS Society (CASS) (SBC60981AG)

Date: 25.04.24   **Time: 12.30 to 01.30 pm**

Venue:IOT Classroom, Sri Sai Ram Engineering College

Ms.S.Usha, Associate Professor of ECE department gave a **talk on Introduction to HDL for IOT** students on 25.04.24. She started the session by giving an introduction about IEEE CAS Society and its 75th-year celebration. Then the session on HDL was taken by her.

Hardware Description Language (HDL) is a specialized computer language used to describe the structure and behavior of digital circuits. HDLs enable designers to model and simulate complex electronic systems before actual hardware implementation.Verilog and VHDL are two

widely-used HDLs, each with its own syntax and features.HDLs allow for the creation of high-level abstractions, making it easier to design and verify digital circuits.

They facilitate the design process by providing a systematic approach to describing hardware components and their interactions.

Understanding HDL is essential for digital circuit designers and engineers involved in FPGA (Field-Programmable Gate Array) and ASIC (Application-Specific Integrated Circuit) design. Proficiency in HDLs enables efficient design iteration and reduces time-to-market for electronic products. Overall, HDLs play a crucial role in the design and development of modern digital systems, offering a powerful means of abstraction and simulation.

She also thanked the CEO, Principal, Dean Academics and HOD-ECE, HOD-CSE(IOT) for giving this wonderful opportunity.