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<a name="home">

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<a href="https://mdafa201044.blogspot.com/p/daftar-isi-1.html"><span style="font-family:
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<span style="font-family: arial;"><b>DAFTAR ISI</b>

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<a href="#kondisi"><span style="font-family: arial;">1. Kondisi</span></a></div>

<div style="text-align: left;">

<a href="#hardware"><span style="font-family: arial;">2. Gambar Rangkaian Simulasi</span></a></div>

<div style="text-align: left;">

<a href="#rangkaian"><span style="font-family: arial;">3. Video Simulasi</span></a></div>

<div style="text-align: left;">

<a href="#flowchart"><span style="font-family: arial;">4. Prinsip Kerja</span></a></div>

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<a href="#listing"><span style="font-family: arial;">5. Link Download</span></a></div>

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<div>
</div>1. Kondisi

[Kembali]<div>
<div><div>Percobaan 2 - Kondisi 6</div><div>
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!important;">Rangkaian sederhana 1 : B=1, D=0, A=1, C'=1, D=1<li style="cursor: url("https://lh3.googleusercontent.com/blogger_img_proxy/ABLy4EzctJfkQYKsBsYGiqFGKX8JI5v4ci5fcG1qpCxs97-1Vt3KwX_nmctyey8PYT6GE-Rsx3UMvOkSF92q18_yO9rxhKwepcUfz1qO1TC-8km3Qw3I5B GVIw=s0-d");

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!important;">Rangkaian sederhana 2 : B=1, D=0, A=1, B=1, C'=0</div></div></div><div>
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2. Gambar Rangkaian Simulasi

[Kembali]</div><div>
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menghasilkan logika 1 kecuali pada SW 7 yang bagian D berlogika 0 dan kemudian diteruskan ke gerbang logika sebagai inputnya. Pada gerbang logika XOR inputnya ialah logika 1 dan 0
 outputnya bernilai 1, karena pada prinsipnya gerbang logika XOR akan menghasilkan output
 1 ketika input berjumlah ganjil, sebaliknya jika jumlah input
 berjumlah genap maka outputnya berlogika 0.
 Selanjutnya pada gerbang AND, Pada kaki 1 yakni di A nilai input logikanya ialah logika 1, kaki 2 yakni di B nilai output logikanya logika 1, dan pada kaki 3 yakni di C nya disambung dengan gerbang NOT yang menyebabkan input pada kaki C' yang awalnya berlogika 1 karena ada gerbang NOT menjadi logika 0. Karena pada kaki input C dipasang gerbang NOT, maka output yang dikeluarkan oleh gerbang AND adalah logika 0. Hal ini karena prinsip kerja gerbang AND ialah output yang dikeluarkan berlogika 1 jika semua nilai input berlogika 1 dan sebaliknya, output yang dikeluarkan berlogika 0 jika terdapat salah satu input memiliki masukan berlogika 0.
 Dengan output XOR adalah logika 1 dan AND berlogika 0, maka input yang masuk ke gerbang OR adalah logika 1 dan 0 yang menyebabkan LED menyala. Karena prinsip gerbang OR yaitu penjumlahan dimana akan bernilai 1 jika input salah satunya bernilai 1.

Pada percobaan 1 dan 2 terdapat perbedaan posisi gerbang NOT nya, yang mana pada percobaan 1 gerbang NOT terletak di kaki 2 gerbang AND. Sedangkan pada percobaan 2 gerbang NOT terletak di kaki 3 gerbang AND

5. Link Download

[Listing](#)

[Kembali](#)

- Download HTML [\[klik disini\]](#)
- Download Rangkaian Simulasi [\[klik disini\]](https://drive.google.com/drive/folders/1zPvH1sKOM6tbSIqKlvTdYNB0FZ-0tQXh?usp=sharing)
- Download Video Simulasi [\[klik disini\]](https://drive.google.com/file/d/1z_6t6yaP6RS0SXNi1LjWfGbCkgyT7zhM/view?usp=sharing)
- Download Datasheet Gerbang AND (3 input) [\[klik disini\]](https://drive.google.com/uc?export=download&id=1RBQHVJOJgcKKcx3xusXO30vsCRD9WL3D)
- Download Datasheet Gerbang XOR (2 input) [\[klik disini\]](https://drive.google.com/uc?export=download&id=1R3nZMAI0yJznkcitOII-3PTEbwmkO_tK)
- Download Datasheet Gerbang NOT [\[klik disini\]](https://drive.google.com/uc?export=download&id=1R8JcqStNlvZxOzbHS5zriuxCFFcSePP3)
- Download Datasheet Gerbang OR (2 input) [\[klik disini\]](https://drive.google.com/uc?export=download&id=1RW7q7Bj7ch_SAVYofK6qwYA3Mc53PWSt)

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