

Please review bolded items in minutes, denoting somewhat important / insightful information

1. **Call to order**
 - 1.1. The **Executive Board Meeting** of the **Circuit Bent Baby** was called to order at **12:00PM** on **12/07/2022** in a Virtual Zoom Meeting by **Nixon Puertollano**.
2. **Roll Call (7)**
 - 2.1. Present (7): Chris Kruger, Liamjohn Velazquez, Matthew Tricomi, Trent Reichenbach Nixon Puertollano, Walter Wargacki, Darian Muresan
 - 2.2. **Late ():**
 - 2.3. Absence ():
 - 2.4. Excused Absence ():
 - 2.5. Guests/Shadows ():
3. **Officer Reports**
 - 3.1. Darian (Advisor) & General Information
 - 3.1.1. Met with Erike Mayo, apparently hes into DJing :D.
 - 3.1.2. Checking up how are things
 - 3.1.2.1. Geometry last week
 - 3.1.2.1.1. Circle
 - 3.1.2.1.2. Rectangle
 - 3.1.2.2. Animations this week
 - 3.1.2.2.1. Need to incorporate potentiometer + triangles?**
 - 3.1.2.2.2. Need to think of it in terms of hardware instead of programming - Darian
 - 3.1.2.2.3. Adding some animation, changing size, shape changing???
 - 3.1.2.2.4. Need to figure out why rectangle disappears
 - 3.1.2.2.5. No labs with audio recording, only playback
 - 3.1.2.2.6. Could incorporate with voice using ADC converter (microphone)
 - 3.1.2.2.7. Have music synchronized, threshold based to make output
 - 3.1.2.3. We have until the 22nd to finish up project**
 - 3.1.2.3.1. Ideal to get most done by tuesday, but 22nd is 2nd opportunity**
 - 3.1.2.4. Documentation**
 - 3.1.2.4.1. Jira**
 - 3.1.2.4.2. Confluence** - need to be more active using
 - 3.1.2.4.3. Need to start block diagramming + have an architecture
 - 3.1.2.4.4. Using LaTeX manual

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3.1.2.4.5. Think of documenting and project design into little components (modules for different functions, architectural level)

3.1.2.4.6. Where to keep diagramming?

3.1.2.4.7. Darian wants us to use LaTeX

3.1.2.4.8. Helps for reviewing and understanding thought process when you revisit a project after a few weeks, or if someone on team has to leave work/meeting early

3.1.3. Latex Demo

3.1.3.1. Perforce (similar to GitHub)

3.1.3.2. Architecture Documents / Directory

3.1.3.3. Visio file - diagram creation

3.1.3.4. .tex file - text / programming language with packages

3.1.3.4.1. Chapters

3.1.3.4.2. Main file with different chapters/modules files

3.1.3.4.3. Compiling main file → formats modules into viewable technical paper / documentation

3.1.3.5. Use diagrams to explain implemented code

3.1.3.5.1. Changing of color

3.1.3.5.2. Different geometry

3.1.3.5.3. Animation logic, screen wipes, etc.

3.1.3.6. Definitely learn to use after we finish this semester/DSD

3.1.4. Wrapping up

3.1.4.1. Need as much diagramming as possible

3.1.4.2. 1:1 between coding and diagrams

3.1.4.3. Diagrams should capture logic

3.1.4.4. Focus on:

3.1.4.4.1. Presentation + Demo (or have recording)

3.1.4.4.2. Diagramming

3.2. Matt Tricomi

3.2.1. Sticking to our road map CIR board

3.2.2. Presentation is tuesday the 13th (DSD)

3.3. Liamjohn Velazquez

3.3.1. Do we have resources for VHDL code?

3.3.1.1. Darian - course textbook + textbook he has

3.3.1.2. Darian can bring those textbooks

3.4. Chris Kruger

3.5. Trent Reichenbach

3.5.1. Could offer potentiometer once finished with DSD project

3.6. Walter Wargacki

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3.7. Nixon Puertollano

4. Action Items / Follow-up Next Time

4.1. This weeks tasks

4.1.1. Geometry (Triangle) + Animation

4.1.2. Use of Potentiometer

4.2. For later

4.2.1. Use of audio input?

4.3. Documentation

4.3.1. Jira, Confluence, LaTeX, Overleaf

4.3.2. Darian pushing for LaTeX documentation / diagramming

4.4. Need some things:

4.4.1. Potentiometer: Get from Trent? After DSD + Design tomorrow

4.4.2. VHDL textbook: get from Darian

5. Adjournment

5.1. Meeting was adjourned at **12:30PM** by **Nixon Puertollano**.

6. Minutes by: Nixon Puertollano