



CIP Technical Steering Committee Meeting

Date: 25 June 2019

Roll Call

TSC members

- Laurence Urhegyi (Codethink) (Representative)
- Masashi Kudo (Cybertrust) (Representative)
- Pavel Machek (Denx) (Kernel Maintainer)
- SZ Lin (Moxa) (Representative)
- Hiroshi Mine (Hitachi)
- Masato Minda (Plat'Home) (Representative)
- Chris Paterson (Renesas)
- Takehisa Katayama (Renesas) (Representative) (Voting)
- Wolfgang Mauerer (Siemens) (Representative)(Voting)
- Akihiro Suzuki (Toshiba)
- Dinesh Kumar (Toshiba)
- Daniel Sangorrin (Toshiba)
- Kazuhiro Hayashi (Toshiba)
- Nobuhiro Iwamatsu (Toshiba) (Kernel Maintainer)
- Yoshi Kobayashi (Toshiba) (Representative) (Voting) - Chair
- Neal Caidin (The Linux Foundation) (leaving early)

Discussions

Kernel team

- Stable kernel release
 - Kernel 4.4
 - 4.4.181-cip33
 - Many CVE fixes including MDS (Microarchitectural Data Sampling: CVE-2018-12130, CVE-2018-12126, CVE-2018-12127, CVE-2019-11091).
 - CIP updates include the update of RZ/G1C by Renesas.
 - 4.4.182-cip34
 - Fix SACK panic

- Kernel 4.19
 - 4.19.50-cip3
 - Many CVE fixes including MDS (Microarchitectural Data Sampling: CVE-2018-12130, CVE-2018-12126, CVE-2018-12127, CVE-2019-11091).
 - CIP updates include device-tree fixes by Renesas.
 - 4.19.52-cip4
 - Fix SACK panic
 - IRC weekly engineering
 - <https://irclogs.baserock.org/meetings/cip/2019/06/cip.2019-06-20-09.00.log.html>
 - <https://irclogs.baserock.org/meetings/cip/2019/05/cip.2019-06-13-09.00.log.html>
 - Kernel release policy - releasing 4.19 twice a month and 4.4 once a month (Effective June, 2019)
 - Kernel 4.19
 - second and fourth Fridays of the month
 - Kernel 4.4
 - second Friday of the month
 - kernel 4.4 released cycle is in slow-moving stage
 - (Difficult to estimate actual release date because of number of patches depends on each stable release)
 - Failed patch tracker
 - <https://gitlab.com/cip-project/cip-kernel/classify-failed-patches>
 - ===== Release process =====
 - 1. Review stable kernel patch once released via gitlab [1].
 - -> Mark the review and the name of the worker under the commit.
 - **Start to review stable kernel patches in rc stage**
 - 2. Review patch from CIP members via cip-dev [2]
 - -> Update the status of the commit in patchwork. I suggest these
 - -> patches should be reviewed by voluntary movement instead of assignment
 - 3. Start smoke testing
 - 4. Tag candidate kernel in a newer version
 - 5. Ack by other maintainers
 - 6. Release and send the news to cip-dev
- [1] <https://gitlab.com/cip-project/cip-kernel/lts-commit-list>
- [2] <https://patchwork.kernel.org/project/cip-dev/list/>
- Stable patches review
 - <https://gitlab.com/cip-project/cip-kernel/lts-commit-list>

CIP Core

- Items need to be approved by TSC
 - None
- add isar-cip-core support for iwg20m [99%]
 - waiting for upgrade to latest ISAR

- gitlab-ci.yml:
 - working but we need to investigate the costs and ask for approval
- Package decision process
 - Held the second CIP-Core meeting (June 18th)
 - [Minute](#)
 - [Action items](#) (Kazu) remaining
 - Examples of package dependency resolution (at proposal-time)
 - Improvement of the maintenance level
 - Voting process details
 - Future plan of the package decision process (Could be extended)
 - June 27: The second proposal (Documentation)
 - June 27-July 2or3: Review in ML
 - July 2or3: The third CIP-Core meeting, Get approval of the WG members
 - July 9: (TSC meeting) Get approval of TSC

CIP Testing Workgroup

Working group meets in the weekly IRC meeting (#cip) on Thursdays @0900 UTC.

Latest Status (25/06/19)

- lab-cip-mentor almost set up. Only remaining issue is worker<->master connectivity. This is probably due to blocked ports.
- Kernel CI loop
 - Containers/scripts for Kernel CI testing have been pushed to [CIP Playground](#).
 - Arm/Arm64 architectures currently supported.
 - Next step is to add gitlab-ci.yml files to the relevant linux-cip branches (e.g. [gitlab-ci.yml](#)).
 - Chris will send an email to the mailing list with more details soon.
- Planning to do a simple demo of the CIP CI/testing 'loop' at OSS-J, as part of the Renesas RZ/G2 HTML5 demo.

Discussion Points

1. Is there any interest in CIP attending/sponsoring (\$2K) the [Automated Testing Summit](#) in October? (Day after ELC-E)
 - Chris is planning to attend.
 - PROPOSAL: To recommend to the CIP board that CIP sponsors ATS 2019 at Bronze level for \$2K.
 - Proposer: Renesas.
 - Seconded: Toshiba.
 - Against: None.
 - Approved.

Software update workgroup

- OSSJ Prototype status
 - Contents
 - Demo showing how SWUpdate can be integrated into CIP Core (Generic). We will use a Beaglebone Black (anyone can try at home)
 - Done
 - Created branch on isar-cip-core with swupdate recipe
 - Watchdog settings
 - Integration with Hawkbit
 - Scripts to setup the demo
 - TODO
 - Integrate boot.scr script
 - Polish up some parts
- Next meeting: on Thursday

Next meeting

- 9th July