Renesas

Digital IC Design Engineer

Responsibilities:

- Participate in feasibility studies and sub-block/chip-level architecture definition
- Design of new digital circuits meeting performance and area
- Support DFT strategy and implementation
- Work closely with back-end designers to correctly implement digital designs to layout
- Simulation, verification and lab evaluation of digital circuits
- Support production test and HW/SW development for evaluation, characterization and testing
- Tapeout database and document preparation
- Attendance at and participation in all relevant project meetings, keeping project manager aware of achievements, issues and potential risks to timescales
- Demonstrates an ability to learn new tool features and procedures
- Attend training courses or conferences
- Build up your knowledge and improve your skills using your own research

Requirements:

- Master's or Ph.D. in Electrical Engineering, Computer Engineering, Computer Science or related disciplines
- Preferably some experience in design of digital circuits (0-1 for junior designers, 2-6 years for middle/senior designers)
- Knowledge of electrical properties, digital circuits, behavioral modeling and simulation and Unix
- Understanding of IC design workflow
- Ability to prioritize work, set goals and meet deadlines
- Self-motivated and take full responsibility for solutions
- Good written, communication and teamwork skills
- Pre-Intermediate English

Will be a plus:

- Experience with Cadence Tools (ex.: Incisive, vManager, etc.)
- Knowledge of transistor-level integrated circuits and semiconductor physics
- Experience with protocols (UART, I2C, SPI, JTAG, etc.)
- Programming/scripting (Verilog/VHDL, SystemC/Verilog, TCL, Python, Bash, Perl)
- Familiarity with RTL logic design, design constraints, synthesis, P&R, static timing analysis and formal verification
- Understanding of advanced verification methodologies (e.g. UVM, ABV)

What we offer:

Full time and from the office job

Contacts:

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