



Geethanjali College of Engineering and Technology
Department of Electronics and Communication Engineering

Center of Excellence in VLSI Design

Details of Events

For the Academic Year 2022-2023 II semester

S.No	Title	Category	No. of students attended	Date and Time	Place
1.	Implementation of Utility Application Modules with VEGA Processor Ecosystem	Internship	2	15 th May 2023 to 15 th July 2023	IIT Guwahati
2.	Internship on “Design and Verification using Verilog”	Internship	97	10 th May to June 4 th 2023	online Mode(Bangalore)
3.	Value added Course on Front end Design using Verilog HDL	VAC	21	7 th Feb to April 2023	Online mode (NIT surathkal)
4.	3-day Workshop on “Front –end and Back-end VLSI Circuits using Cadence Tools”	Workshop	36	31 st March 2023 to 2 nd April 2023	GCET
5.	Project Based Learning EXPO (Analog and Digital VLSI Projects)	Project EXPO	74	16 th February 2023	GCET
6.	Workshop on VLSI Design flow using Cadence Tools	Workshop	36	23 rd January 2023 to 4 th February 2023	GCET
7	3-day Workshop on “Front –end and Back-end VLSI Circuits using Cadence Tools”	Workshop	30	23 rd January 2023 to 25 th January 2023	GCET

8	Industry oriented session on Career opportunities in VLSI design Industry	Guest Lecture	139	18 th Aug 2022	GCET
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