

List of Publications

Sl. No	Publication Details
1	Kotresh Marali, Irshadahmed Preerjade, Shreevatsa Tilgul, Shrikrishna K, Sourab M Kulkarni “Performance Analysis of Fourier Transform Algorithms with and without using OpenMP”, International Journal of Engineering Research & Technology (IJERT) ISSN: 2278-0181, ICRTT - 2018 Conference Proceedings
2	S. S. Navalgund, S. S. Kerur, Kotresh Marali “IoT based Intelligent Autonomous Vehicle on Multicore Processors” International Journal of Advance Research and Innovation,ISSN 2347 3258, 2017
3	Ms. Sahyadri J. V. ,Mr.Kotresh E. Marali “Performance Analysis and Hardware Implementation of Digital Circuit Design Using Reversible Logic” IOSR Journal of Electronics and Communication Engineering (IOSR-JECE), ISSN: 2278-8735, Mar-Apr2016
4	Pooja Kharade, Laxmi Mandalollu, A.S. Pooja , Pooja Savadatti and Kotresh Marali, Prototype implementation of IoT based autonomous vehicle on Raspberry Pi, Bonfring International Journal of Research in Communication Engineering, ISSN: 2277-5080 , Nov-2016
5.	Umesh, Kotresh E. Marali and Shrikanth K. Shirakol, “Design, Implementation and Performance Analysis of 1-Bit CMOS Full Adder using GDITL”, Emerging research in Computing, Information, Communication and Applications: ERCICA-2014, ISBN:9789351072621,
6	Umesh, Kotresh E. Marali , “Analysis and Detection of various Faults in Combinational Circuits using D-Algorithm and BIST”, International Journal of Engineering Research & Technology (IJERT), NCRTS`2014 Conference Proceedings ISSN: 2278-0181
7.	Kumara Swamy H. L., Kotresh E. Marali, Siddalingesh S. Navalgund “Novel Techniques For Circumventing The Glitch Effects On Digital Circuits For Low Power VLSI Design”, International Journal of Engineering Research & Technology (IJERT) Vol. 2 Issue 3, March - 2013 ISSN: 2278-0181
8.	Kotresh Marali, et., al., “Design space exploration of mamdani and sugeno inference systems for fuzzy logic based illumination controller” International journal of VLSI and Embedded system-IJVES, 2012

9	Kotresh Marali, S. S. Navalgund , “Design and Implementation of Multipliers for a Complex Numbers”, International Conference on “Information, Communication & Embedded systems”, held on 24 th & 25 th of February 2012 at S. A. College of Engineering, Chennai, Tamil Nadu.
10	Kotresh Marali, S. S. Navalgund “Design Space Exploration for Optimal Implementation of a Binary Multiplier Using VHDL”, International Conference on Emerging Trends in Engineering, held on 4 th & 5 th of May 2011 at N.M.A.M Institute of Technology, Nitte, Mangalore, Karnatak. ISBN: 978-93-81195-07-9c
11	Kotresh Marali, S. S. Navalgund , “Hardware Implementation and Modeling of Glitch Effects on Arithmetic circuits With Trivial Operand”, National Conference on Innovations & Trends in Computer & Communication Engg., ITCCE- 2011 held at MET’s Institute Of Engineering, Nashik, Maharashtra held during Feb 25-27, 2011.
12	Kotresh Marali, S. S. Navalgund , “Design and Implementation of Low power, High Performance Trivial Operands Arithmetic Unit”, National Conference on Recent Trends in Computational Science & Engg., NCRTCSE- 2011 at KVG College Of Engineering, Sullia, Karnataka held on 12 th Feb, 2011.