



**K.S. RANGASAMY COLLEGE OF TECHNOLOGY,
TIRUCHENGODE - 637 215**

Department of Electronics Engineering (VLSI Design and Technology)

**EE
(VLSIDT)**

**Centre For Silicon System Research (CSSR)
Activities of 2025-26**

S.No.	Date	Type of the Event	Title of the Event
1.	22.07.2025 to 23.07.2025	Workshop	FPGA Made easy with Python using PYNQ & Kria boards
2.	31.01.2026	Circuit Debugging	Enhance hands-on VLSI Skills
3.	31.01.2026	Tech Talk	Latest Trends In VLSI Design

Activities of 2024-25

S.No.	Date	Type of the Event	Title of the Event
1.	15.02.2025	Intra-department competition	Intra-department competition on Circuit Debugging Series-1
2.	21.04.2025	Magazine Release Event	Release of Magazine - VLSI Mag