



# <u>Sir Syed University Of Engineering and Technology</u>

**ELECTRICAL ENGINEERING DEPARTMENT** 

Quarterly Progress Report FYDP group				
Group Number:	Project Title:			
Reporting Number:	_ Report Period:			
Submission Date:	Date of last meeting with supervisor:			

FYDP Progress		<b>Prop</b> e (Ti from FYDP ap	
Progress of FYDP		Milestone	
(During this Reporting Period)	(to be filled by students with consent of supervisor)	Literature Review completion	
(Student Comments) (Use extra sheets		Problem Analysis/ Solution	
if required)		FYDP Sub-Task 1 (Please mention task)	
Tasks for Next Month (Student Comments) (Use extra sheets if required)	(to be filled by students with consent of supervisor)	FYDP Sub- Task 2 (Please mention task)	
		FYDP Sub- Task 3 (Please mention task)	
Total FYDP Progress %)	(to be filled by students/supervisor with consent of supervisor)	Final Hardware/ Software Model	
Comment on problems faced and solution	(to be filled by students)	FYDP Hardware/ Software Integration and troubleshooting	

Final Year Projects Committee. Department of Electrical Engineering
Sir Syed University of Engineering and Technology
University Road, Karachi-75300





## <u>Sir Syed University Of Engineering and Technology</u>

### **ELECTRICAL ENGINEERING DEPARTMENT**

		-	
presented			
by			
Supervisor			
Supervisor's	(to be filled by supervisor)		
Comments		FYDP Report Writing/	
about		Evaluation	
Student's		(Separate date for both)	
Performance			
A C 11:		D :	N. A
Are you familiar	with FYDP calendar? (Yes/No); Funded	Project: (Yes/	No)
Have you submit	tted the undertaking provided by FYDPC?	_ (Yes/No)	
Students Atten	<b>ding</b> : The meeting was held on (location :	) following members :	attended:
Students Atten	ing. The meeting was nead on (location)		ittenaca.
1 Namo	Doll# Signature.		
1. Name:			
2. Name:	Roll# Signature:	<del></del>	
3. Name:	Roll# Signature:		
4. Name:	Roll# Signature:		
5. Name:	Roll# Signature: _		
	COUNTERSIGNED		
	<u>GOOTT BROIGNED</u>		
Dated:	_		
		(Name)	
		Supervisor	
Notes:	tings of each EVDD group with the commendation	Il ha hald at least successive	
	tings of each FYDP group with the approved supervisor was of FYDP. First meeting will however be held immed		
	of announcing is within 0 days of announced of EVDD and /on		- r

- allocation of supervisor, i.e. within 8 days of approval of FYDP and/or supervisor allocation.
- 2. Atleast 3 student members (and the supervisor) should be present during the meeting to fulfill the quorum requirement.
- 3. Any student absent from three successive meetings would be entitled to marks deduction (10 marks). An application with supervisor comments should be submitted, mentioning the reason for being absent.

Final Year Projects Committee. Department of Electrical Engineering Sir Syed University of Engineering and Technology University Road, Karachi-75300





## **Sir Syed University Of Engineering and Technology**

### **ELECTRICAL ENGINEERING DEPARTMENT**

- 4. Every group should meet all the deadlines as mentioned in the FYDP calendar; in order to avoid marks deduction.
- 5. Any group not in contact with the supervisor for 2 months would be entitled to disciplinary action; including project cancellation.