

Basic Integrated Circuit Design
DFTG 2413
Semester and Year

Instructor —

Synonym —

Section Number —

Class Hours —

Office —

Office Hours —

Phone —

Email —

Please contact me if you wish to meet with me outside of regular office hours.

COURSE RATIONALE

The first course in the layout and design of integrated circuits including the use of Cadence Virtuoso CAD tools. Presents an overview of the IC design and manufacturing process is provided with an emphasis on CMOS technology.

COURSE DESCRIPTION

An introductory course in the layout and design of integrated circuits. This is the first of three courses designed to train students to become IC Layout Designers; however, this course may also be taken as a technical elective for other degree plans. An overview of the IC design and manufacturing process will be provided with an emphasis upon CMOS technology. Students shall be taught how to interpret logic gate symbols as well as the schematics of basic circuits. Instruction will include translating these devices and designs from the electrical into the physical helping students to construct layouts beginning with the simple and progressing to the complex. Students shall receive training in Cadence Virtuoso to compose their layouts. To insure proper layout construction both physically and electrically, DRC and LVS verification will be introduced.

PREREQUISITES

DFTG 1405 Technical Drafting

REQUIRED TEXTS/MATERIALS

Textbook

IC Layout Basics: A Practical Guide, by Christopher Saint and Judy Saint, McGraw-Hill Professional

STUDENT LEARNING OUTCOMES

Course-Level Student Learning Outcomes

Upon successful completion of the course, students will be able to:

- Recognize schematic symbols for basic CMOS devices
- Identify the components in the CMOS transistor cross-section
- Translate design schematics into layout architecture
- Select proper layers & coordinate placement of these layers in relationship to each other in accordance with design rules
- Fold transistors when their width exceeds maximum value or as appropriate to the design
- Share diffusion as appropriate
- Minimize transistor drain area
- Properly connect circuit nodes with metal lines of sufficient dimension to carry the current load
- Maximize contact and via cuts as appropriate
- Insure sufficient substrate and well ties (taps) exist
- Place text in the layout as required by the schematic
- Translate resistor values to appropriate layout dimensions
- Minimize the area required for a given circuit
- Efficiently use Cadence Virtuoso to construct layout
- Utilize design hierarchy to build cells containing subcells
- Recognize the components that are required to build a complete chip

Program-Level Student Learning Outcomes

At the completion of the AAS degree –Electronics Graphics Specialization - the student will be able to:

- E1. Utilize CAD software to plan and prepare technical graphics and documentation appropriate to the Electrical/Electronic Engineering industry.
- E2. Utilize CAD software to translate schematics, logic diagrams, and other technical graphics into the physical layout of Integrated Circuits and Printed Circuits.

- E3. Employ CAD-based verification tools and techniques to debug Integrated Circuit layouts.
- I1. Utilize CAD software to plan and prepare documents and technical graphics appropriate to a range of design, manufacturing, and construction industries.

At the completion of the AAS degree –Electronics Graphics Specialization - the student will be able to:

- E1. Utilize CAD software to plan and prepare technical graphics and documentation appropriate to the Electrical/Electronic Engineering industry.
- E2. Utilize CAD software to translate schematics, logic diagrams, and other technical graphics into the physical layout of Integrated Circuits and Printed Circuits.
- E3. Employ CAD-based verification tools and techniques to debug Integrated Circuit layouts.

SCANS COMPETENCIES

ARCHITECTURAL & ENGINEERING CAD SCANS FIVE COMPETENCIES

Courses	Resources				Interpersonal				Information				Systems		Technology					
	C1 Allocates Time	C2 Allocates Money	C3 Allocates Material	C4 Allocates Human Resources	C5 Participates as a Member of a Team	C6 Teaches Others	C7 Serves Clients/Customers	C8 Exercises Leadership	C9 Negotiates to Arrive at a Decision	C10 Works with Cultural Diversity	C11 Acquires & Evaluates Information	C12 Organizes & Maintains Information	C13 Interprets & Communicates Information	C14 Uses Computers to Process Information	C15 Understands Systems	C16 Monitors & Corrects Performance	C17 Improves & Designs Systems	C18 Selects Technology	C19 Applies Technology to Task	C20 Maintains & Troubleshoots Technology
ARCE 1452	✓		✓								✓	✓	✓	✓	✓			✓	✓	✓
DFTG 1405	✓				✓						✓	✓	✓	✓	✓			✓	✓	✓
DFTG 1413	✓					✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 1417	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 1433	✓	✓	✓		✓	✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 1445	✓		✓	✓	✓		✓	✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 1458	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 1475	✓		✓		✓		✓	✓			✓	✓	✓	✓	✓	✓	✓		✓	
DFTG 1491*	✓							✓			✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 1493	✓							✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 1494	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 1495	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2400	✓				✓						✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2402	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2405	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2412	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2419	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2421	✓					✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2427	✓					✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2428	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2430	✓						✓	✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2431	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2432	✓										✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2436	✓					✓					✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2440	✓			✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2470	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2471	✓										✓	✓	✓	✓	✓	✓	✓	✓	✓	
DFTG 2473	✓					✓					✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2474	✓										✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2475	✓					✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2476	✓					✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2477	✓		✓	✓	✓	✓		✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
MCHN 1419	✓			✓	✓					✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	
ENGLISH																				
MATH																				
Oral Communication																				
Arts & Humanities																				
Social Behavior & Science																				

* = Capstone

ARCHITECTURAL & ENGINEERING CAD SCANS COMPETENCIES

FOUNDATION SKILLS

Courses	Basic Skills					Thinking Skills					Personal Qualities					
	Reading	Writing	Arithmetic/Mathematics	Listening	Speaking	Creative Thinking	Decision Making	Problem Solving	Visualizing	Knowing How to Learn	Reasoning	Responsibility	Self-Esteem	Sociability	Self-Management	Integrity/Honesty
ARCE 1452	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1405	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1413	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1417	✓	✓	✓	✓			✓	✓	✓	✓	✓	✓			✓	✓
DFTG 1433	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1445	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1458	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1475	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1491*	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1493	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 1494	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓	✓
DFTG 1495	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓	✓
DFTG 2400	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓
DFTG 2402	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓	✓
DFTG 2405	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓	✓
DFTG 2412	✓	✓	✓			✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2419	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓		✓		✓	✓
DFTG 2421	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2427	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓
DFTG 2428	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓			✓	✓
DFTG 2430	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2431	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓			✓	✓
DFTG 2432	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓		✓	✓	✓	✓
DFTG 2436	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2440	✓	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
DFTG 2470	✓	✓	✓	✓	✓		✓	✓	✓	✓	✓				✓	✓
DFTG 2471	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓			✓	✓	✓
DFTG 2473	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓		✓	✓	✓
DFTG 2474	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓			✓	✓
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DFTG 2477	✓	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓		✓	✓
MCHN 1419	✓	✓	✓	✓		✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓
ENGLISH																
MATH																
Oral Communication																
Arts & Humanities																
Social Behavior & Science																

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COURSE EVALUATION/GRADING SCHEME

Grades will be determined as follows:

- Daily Work 50% of final grade
- Mid-Term Exam 25% of final grade
- Final Exam 25% of final grade

Daily Work shall consist of warm-ups, drawing assignments, questions and possible quizzes. Grades will reflect the quality of work performed within the assigned time period.

INSTRUCTIONAL METHODOLOGY

This course is taught in the classroom in a lecture/laboratory format. The lecture will generally introduce concepts and skills, which will then be developed and applied in the laboratory.

OPEN LABS

Hours for the open lab will coincide with the tutoring schedule which will be posted during the first week or two of the semester. **You may not be able to complete all the assignments in class**, so the open lab is one of your best opportunities to get help in finishing the lab exercises.

COURSE OUTLINE/CALENDAR

Class 1

Introduce Course

Class 2

Introduce Integrated Circuits and Design Rules

Class 3

Introduce PMOS schematic, cross-section and layout

Project: PMOS layout

PMOS questions

Class 4

Introduce NMOS schematic, cross-section and layout

Project: NMOS layout

NMOS questions

Video on Wafer Processing and questions

Class 5

Introduce Cadence Virtuoso

Class 6

Introduce Stick Diagrams; Inverter schematic and layout

Project: Inverter layout Inverter questions

Class 7

Introduce Folding and Sharing Transistors

Project: Folding Transistor layout

Folding and Sharing questions

Class 8

Introduce NOR schematic and layout

Project: NOR layout

NOR questions

Class 9

Continue NOR layout

Class 10

Introduce NAND schematic and layout

Project: NAND layout

NAND questions

Class 11

Continue NAND layout

Class 12

Introduce Transmission Gate schematic and layout

Project: Flip-Flop layout

Class 13

Continue Flip-Flop layout

Class 14

Review for Mid-Term Exam

Continue Flip-Flop layout

Class 15

Mid-Term Exam

Class 16

Introduce Latch schematic and layout

Project: Latch layout

Class 17

Continue Latch layout

Class 18

Introduce Capacitor schematic and layout

Project: Shift Register layout

Class 19

Continue Shift Register layout

Class 20

Introduce Guard Rings; Tri-State Buffer schematic and layout

Project: Tri-State Buffer layout

Class 21

Introduce Resistor schematic and layout; Input/Output Circuits

Projects: Input Protection, Pad and Input Buffer layouts

Class 22

Continue Input Protection, Pad and Input Buffer layouts

Class 23

Introduce Complex Gate schematic and layout

Project: Complex Gate layout

Class 24

Continue Complex Gate layout

Class 25

Introduce Final Project schematics and layout

Class 26

Continue Final Project

Class 27

Continue Final Project

Class 28

Continue Final Project

Class 29

Review for Final Exam

Continue Final Project

Class 30

Final Exam

COURSE/DEPARTMENTAL POLICIES

Attendance/Class Participation

Students accruing more than two (2) UNEXCUSED absences may be withdrawn from the course at the instructor's discretion. Students may make-up absences by attending other classes. It is the student's responsibility to inform instructor when circumstances prevent him/her from attending class. An instructor may lower a student's final grade for a course due to excessive absences. **Attendance at the Midterm and Final Portfolio Reviews are mandatory—students who do not attend these reviews may be dropped from the course.**

Cell Phones and Electronic Devices

To avoid disturbing or distracting others during class or open lab times, students are requested to conduct personal communications of any sort outside of the classrooms. For this reason, the use of cell phones and other PDA's by students is prohibited inside A&E CAD classrooms (this includes placing or receiving phone calls, text messages and emails). When conducting personal communication in the hallways, please control the volume of your voice so as not to disturb students in nearby classrooms.

Withdrawal Policy

Course withdrawals may occur at any time after the official reporting date of a semester and up to the established deadline for withdrawals in each semester. The established deadline is listed in the course schedule and on the Web.

It is the responsibility of each student to ensure that his or her name is removed from the roll if he or she decides to withdraw from the class. The instructor does, however, reserve the right to drop a student should he or she feel it is necessary. If a student decides to withdraw, he or she should also verify that the withdrawal is submitted before the Final Withdrawal Date. The student is also strongly encouraged to retain their copy of the withdrawal form for their records.

Initiation of Withdrawals:

Withdrawals from a course result in a grade of "W" and may be affected through action taken by either the student, the course instructor, or the instructor's immediate supervisor in the instructor's absence. Students who wish to withdraw from specific courses should initiate withdrawal procedures with the Campus Admissions and Records Office prior to the published deadline for withdrawals. Students who are not withdrawn as of the established deadline will receive a performance grade (A, B, C, D, or F). Students must present a picture I.D. to withdraw from the course.

Students who enroll for the third or subsequent time in a course taken since Fall 2002, may be charged a higher tuition rate, for that course.

State law permits students to withdraw from no more than six courses during their entire undergraduate career at Texas public colleges or universities. With certain exceptions, all course withdrawals automatically count towards this limit. Details regarding this policy can be found in the ACC college catalog.

Incompletes

The grade of "I" (for Incomplete) may be given by an instructor for a course in which a student was unable to complete all of the objectives for the passing grade. A grade of "I" cannot be carried beyond the established date in the following semester or session. The completion date is determined by the instructor, but may not be later than two weeks prior to the end of the semester. The Department Chair will approve a change from "I" to a performance grade (A, B, C, D, F) for the course prior to or at the deadline. Consideration should be given to course load and job and family obligations when carrying an "I" grade into a new semester for completion. Grades of "I" that are not resolved by the deadline will automatically be converted to a grade of "F." In extreme cases, permission may be granted to carry an "I" grade for longer than the following semester or session deadline; this must have the approval of the Dean.

Policy on Late or Missing Work

Instructors may impose a late penalty on work that is submitted after the assigned due date.

Statement on Scholastic Dishonesty

Representing the work of another person as your own work is considered scholastic dishonesty by the A&E CAD department. Academic work submitted by students shall be the result of their own thought, research or self-expression. For purposes of this rule, academic work is defined as, but not limited to tests and quizzes, whether taken electronically or on paper; projects, either individual or group; papers; classroom presentations; and homework. When students borrow ideas, wording or organization from another source, they shall reference that information in an appropriate manner.

Reproduction or transmission of A&E CAD Departmental Check Prints in any manner (including photography and scanning) is a violation of the A&E CAD department's Scholastic Dishonesty policy.

Sharing a drawing file of a class project with another student, or representing the file of another student as your own work, is a violation of the department's Scholastic Dishonesty policy.

Violation of this policy may result in, but is not limited to, the following: reduction of the grade on the project, a grade of zero being recorded for the project, the assignment being replaced with a different project with a reduced maximum grade, the reduction of the final course grade, and in the most extreme cases, withdrawal from the course. Students accused of scholastic dishonesty will be subject to the procedures outlined in the Policies and Procedures for Academic Dishonesty section of the ACC Student Handbook.

Student Rights and Responsibilities

Students at the college have the rights accorded by the U.S. Constitution to freedom of speech, peaceful assembly, petition, and association. These rights carry with them the responsibility to accord the same rights to others in the college community and not to interfere with or disrupt the educational process. Opportunity for students to examine and question pertinent data and assumptions of a given discipline, guided by the evidence of scholarly research, is appropriate in a learning environment. This concept is accompanied by an equally demanding concept of responsibility on the part of the student. As willing partners in learning, students must comply with college rules and procedures.

Statement on Students with Disabilities

Each ACC campus offers support services for students with documented disabilities.

Students with disabilities who need classroom, academic or other accommodations must request them through the Office for Students with Disabilities (OSD). Students are encouraged to request accommodations when they register for courses or at least three weeks before the start of the semester, otherwise the provision of accommodations may be delayed.

Students who have received approval for accommodations from OSD for this course must provide the instructor with the 'Notice of Approved Accommodations' from OSD before accommodations will be provided. Arrangements for academic accommodations can only be made after the instructor receives the 'Notice of Approved Accommodations' from the student.

Students with approved accommodations are encouraged to submit the 'Notice of Approved Accommodations' to the instructor at the beginning of the semester because a reasonable amount of time may be needed to prepare and arrange for the accommodations.

Additional information about the Office for Students with Disabilities is available at <http://www.austincc.edu/support/osd/>.

Safety Statement

Austin Community College is committed to providing a safe and healthy environment for study and work. You are expected to learn and comply with ACC environmental, health and safety procedures and agree to follow ACC safety policies. Additional information

on these can be found at <http://www.austincc.edu/ehs>. Because some health and safety circumstances are beyond our control, we ask that you become familiar with the Emergency Procedures poster and Campus Safety Plan map in each classroom. Additional information about emergency procedures and how to sign up for ACC Emergency Alerts to be notified in the event of a serious emergency can be found at <http://www.austincc.edu/emergency/>.

You are expected to conduct yourself professionally with respect and courtesy to all. Anyone who thoughtlessly or intentionally jeopardizes the health or safety of another individual will be immediately dismissed from the day's activity, may be withdrawn from the class, and/or barred from attending future activities.

Freedom of Expression

Each student is strongly encouraged to participate in class. In any classroom situation that includes discussion and critical thinking, there are bound to be many differing viewpoints. These differences enhance the learning experience and create an atmosphere where students and instructors alike will be encouraged to think and learn. On sensitive and volatile topics, students may sometimes disagree not only with each other but also with the instructor. It is expected that faculty and students will respect the views of others when expressed in classroom discussions.

All discussion or conversation in the classroom should be appropriate and respectful of others.

Students and Instructional Services

ACC strives to provide exemplary support to its students and offers a broad variety of opportunities and services. Information on these services and support systems is available at: <http://www.austincc.edu/s4/>

Links to many student services and other information can be found at:

<http://www.austincc.edu/current/>

For help setting up your ACCeID, ACC Gmail, or ACC Blackboard, contact ACC Helpdesk at helpdesk.austincc.edu or 223-HELP.

Ownership of Student Work

The Architectural & Engineering Computer Aided Design (A&E CAD) Department reserves the right to retain any and all student work (including but not limited to: original work, scans, photographs, and copies of student work) completed as A&E CAD course work for the purposes of documentation, accreditation, evidence of student performance, university transfer, marketing or any other purpose supporting the mission of the Department and Austin Community College.

Concealed Handgun Policy

The Austin Community College District concealed handgun policy ensures compliance with Section 411.2031 of the Texas Government Code (also known as the [Campus](#)

[Carry Law](#)), while maintaining ACC's commitment to provide a safe environment for its students, faculty, staff, and visitors.

Beginning August 1, 2017, individuals who are licensed to carry (LTC) may do so on campus premises except in locations and at activities prohibited by state or federal law, or the college's concealed handgun policy.

It is the responsibility of license holders to conceal their handguns at all times. Persons who see a handgun on campus are asked to contact the ACC Police Department by dialing 222 from a campus phone or 512-223-7999.