

CIP Technical Steering Committee Meeting

Date: 10th October 2023.

Roll Call

TSC members (Alphabetical order by company name)

Attendees (Please change to **Bold**, if you attend this meeting) (Key shortcut: Ctrl+b)

Company	Members
Bosch	Philipp Ahmann [at the airport, may not be able to join] Sietze van Buuren
Cybertrust	Hirotaika Motai (Representative) Hiraku Toyooka
Hitachi	Hidehiro Kawai (Representative) Takuo Koguchi
Linutronix	
Moxa	Jimmy Chen (Representative)
Plat'Home	Masato Minda (Representative)
Renesas	Chris Paterson (CIP Testing WG Chair) Kento Yoshida Kazuhiro Fujita (Representative) (Voting) Takehisa Katayama (Representative) (Voting)
Siemens	Jan Kiszka (Representative) (Kernel Team Chair) Wolfgang Mauerer (Representative) (Voting) Urs Gleim Stefan Schroeder
Toshiba	Dinesh Kumar Kazuhiro Hayashi (Voting Representative) (CIP Core / Software Update Chair) Venkata Pyla Nobuhiro Iwamatsu (Kernel Maintainer) Shivanand Kunijadar

	Yoshi Kobayashi (TSC Chair)
Denx	Pavel Machek (Kernel Maintainer)
	Ulrich Hecht (Kernel Maintainer)
Linux Foundation	Neal Caidin

Discussions

Security Working Group

Items need to be approved by TSC voting members

- **Motion-1** : M-COM device from Siemens is recommended by SWG to be used for CIP IEC final assessment
 - The device has been added in CIP reference hardware list and updated [CIP wiki page](#)
 - The details of available options is under discussion since long time and comparison can be referred at page #3 [here](#)
 - Second: Siemens
 - Approved without any objection
- **Motion-2** : Use Debian bookworm based isar-cip-core images with CIP-6.1 kernel for CIP IEC final assessment
 - SWG members have verified IEC layer tests on QEMU amd64 architecture
 - M-COM device can support bookworm based images, adding recipes in isar-cip-core progress
 - Second: Siemens
 - Approved without any objection

Status updates

- Next BV meeting to be rescheduled as in the last week BV members were on vacations
 - a. Current status of action items from BV checklist, more contribution is needed to complete the action items

SWG Members	Started	Redy4Review	Complete	Not started
Dinesh	9	6	0	3
Stefan	0	0	0	10
George	4	0	0	7
Kent	0	0	0	7
Not assigned	0	0	0	8
Total	13	6	0	35

- b. Draft Traceability matrix document under review in SWG

- https://gitlab.com/cip-project/cip-documents/-/blob/trace_req_design_testing/process/Requirements_traceability.md?ref_type=heads
- Investigation to use mermaid diagram in CIP IEC markdown format is in progress as after including mermaid diagram, pdf generation is not working as expected
 - a. <https://gitlab.com/cip-project/cip-documents/-/issues/5>
 - b. <https://gitlab.com/cip-project/cip-documents/-/issues/2>
- Minor issue fixes in markdown documents for rendering in pdf format
 - a. https://gitlab.com/cip-project/cip-documents/-/merge_requests/47
- New member from Renesas is expected to start joining SWG meetings from 23rd Oct as per the updates received from Kent
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Kernel Team Working Group

Items need to be approved by TSC voting members

- None

Status updates

- **CIP IRC weekly meeting**
 - Logs
 - [Sep 28](#)
 - [Oct 5](#)
- **CIP kernel releases**
 - 4.4
 - v4.4.302-cip80 on October 10 by Ulrich
 - => all on schedule
 - 4.19
 - v4.19.295-cip103 on September 29 by Iwamatsu-san
 - => all on schedule
 - 5.10
 - none
 - => all on schedule
 - 6.1
 - none
 - => all on schedule
- **LTS discussion**
 - Reminded Linaro, no reply -> no updates
- **Kernel config unification for testing**
 - With more x86 targets being built (e.g. via isar-cip-core), it would be beneficial to create a unified kernel config for all of them (super-set)
 - Would permit a single test image for x86 arch (less CI builds)
 - Could become useful for other archs as well
 - Does anyone see conflicts in this approach?

CIP Core Working Group

Items need to be approved by TSC voting members

- None

Past minutes

- [past meetings](#)

Status updates

- CIP Reproducible Builds support
 - (Resolved) [#70 armhf images with SWUpdate are not reproducible](#)
 - (Resolved) [#73 FAT filesystem images are not reproducible](#)
 - [ebg-sysparts: Add filesystem UUID to fat images \(b1bc5858\)](#)
 - [dosfstools: Add recipe to build it locally \(4c3a8504\)](#)
 - [distro: Set dosfstools as build dependency \(9ec5c2e0\)](#)
 - (Resolved) [#77 BBB rootfs partition is not reproducible due to UUID mismatch](#)
 - (WIP) [#58 Diffoscope tool could not verify disk images with partitions](#)
 - (WIP) [#74 ext4 filesystem images are not reproducible](#)
 - (WIP) [#80 .swu file is not reproducible](#)
- isar-cip-core
 - Add recipes to build dosfstools for RB
 - Update CIP kernel versions, and isar revision to the latest
 - FYI: [EMLinux 3.0](#) by Cybertrust uses isar-cip-core
- Reference H/W support
 - (WIP) Enable OpenBlocks IoT in isar-cip-core & CI
 - Considering consolidating multiple x86 targets into a generic one
 - (WIP) Support secure boot on Renesas RZ/G2M

CIP Testing Working Group

Items need to be approved by TSC voting members

- None

Status updates

- The usual LAVA maintenance
- Working on a version upgrade for our LAVA instance
 - We need to arrange a day to upgrade the server & workers in sync
 - Proposal:
 - Start with the server on the 10th Oct eve, followed by the labs
 - Would this downtime be okay for the kernel team?

Discussions

- None(?)

Software Update Working Group

Items need to be approved by TSC voting members

- None

Status updates

- (WIP) Support physical boards
 - Secure storage
 - MCOM
 - SWUpdate + Secure boot
 - BBB
 - Renesas RZ/G2M
- (WIP) Evaluating wfx + SWUpdate internally (Toshiba)

Other topics

- Open Source Summit Japan
 - CFP has already closed.
 - Who submitted for OSSJ?
 - (Action) Please let us know the title.
 - Jan: not related to CIP (Xenomai)
 - Motai-san: Submitted ALS (Automotive Linux Summit) but CIP kernel will be touched in this talk.
 - Toshiba: Software update topics, related to CIP WG's activities
 - Call for Booth demo
 - Plat'Home(minmin): I will prepare the demo for IoT. It's my usual demo, but I'm making little progress each time.
 - Toshiba: Planning to provide a demo about Software Updates WG topics using physical board(s)
 - Renesas: Planning to provide the same audio demo as the last conference, now using the 6.1 CIP kernel
 - AI(Neal): Create a spreadsheet for OSSJ
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- CRA is becoming more serious issue for open source community and users.
 - <https://digital-strategy.ec.europa.eu/en/library/cyber-resilience-act>
 - <https://debconf23.debconf.org/talks/112-eu-legislation-bof-cyber-resilience-act-product-liability-directive-and-csam-regulation/>
- (DONE) Email to cip-dev: "[cip-dev] projected EOL v4.4 SLTS"

Upcoming TSC meetings

- Regular TSC meeting
 - Date: 24th of October, 2023
 - Start time: 13h UTC. [Local time in your timezone](#)
 - NOTE: DST information
 - US DST start: The second Sunday of March
 - US DST end: the first Sunday of November
 - EU DST start: The last Sunday of March
 - EU DST end: The last Sunday of October
- F2F meeting schedule
 - Date: 4th of December 2023
 - Time: Expected TSC from 9am to 2pm, GB from 2:30pm to 5:00pm
 - Booth setup at conference venue during the GB meeting
 - Place: Renesas HQ office
 - Map: <https://goo.gl/maps/TwfpjjsbGXik7oa29>
 - Registration will be required at the timing of entrance.
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