

Michael Azorin

Security-focused Solidity EVM smart contract engineer with deep DeFi knowledge

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EXPERIENCE

Advanced DeFi User

2/2021 – Present

- EVM: Ethereum, Polygon, Optimism, Arbitrum, Fantom, Avalanche, more. Non-EVM: Solana, Tron, Terra.
- Evaluating protocols for value, security, tokenomics, governance, community, long-term sustainability.
- Yield farming, cross-chain arbitrage, lending protocols, stablecoin, locking, voting, bribing, NFTs.

Instruction Resident, *OxMacro Engineering Fellowship* (Remote)

8/2022 – Present

- Reviewing Fellows' work by performing smart contract security audits of completed Solidity projects.
- Providing project mentorship with an emphasis on security-focused smart contract design.

Smart Contract Security Fellowship, *OxMacro Engineering Fellowship* (Remote)

7/2022 – 8/2022

- Graduated in highest **Honors Tier** — developed consistently secure and efficient smart contract projects.
- [Project DAO](#): An ownerless DAO contract with the purpose of acquiring NFTs with treasury funds. Custom spec with requirements for membership, proposal, gasless voting through ECDSA.
- [Project LP](#): A Uniswap V2-style AMM core and router that allows liquidity provisioning and swaps through a constant curve formula. Enhanced readability and some improvements over Uniswap's core contract.
- [Project ICO](#): A multi-phase ERC20 token fundraiser which includes a whitelist-only private phase.
- [Project MerkleDrop](#): A token airdrop contract which allows token claims either through an immutable Merkle tree set at contract deployment, or EIP-712 compliant signatures signed by the contract owner.
- [Project Crowdfund](#): A fundraising contract which provides ERC721 contributor badge NFTs to contributors that invest past a certain threshold.

Hardware Design Automation Engineer, *Intel Corporation* (Portland, OR / Remote) 5/2016 – 12/2021

- Built hardware design automation and optimization, improving microprocessor quality, speed, and reliability through custom automation on top of industry standard silicon-level CAD tools.
- Excellent knowledge of computer architecture design, SoC development process.
- Supported tools, flows, methods for SoC structural design including automated place and route and high-speed random logic synthesis.
- Built and released tool to track changes across project repositories, easily showing differences for release and development branches between multiple projects to drive inter-department convergence.
- Enabled high-speed design system through cross-site collaboration with global development team.

SKILLS

Blockchain Development

- Solidity, Hardhat, EVM, web3.js
- ERC20, ERC721, EIP-712, AMMs, Merkle trees

Scripting & Automation

- Unix/Linux, Bash, Python, TCL, Perl, Ruby, JS
- Data Analysis, Design & Test Automation

Version Control

- Git, Gerrit Code Review, Subversion SVN
- Regression Testing, Code Coverage

Hardware Design

- Digital Circuits, Digital Logic
- Verilog, FPGA, Embedded Systems

EDUCATION

- MSEE Computer Architecture, BSEE Electrical Engineering. University of Southern California, 5/2016.