

Journal Papers Published

2022-23

S.N o.	Name of the Faculty	Title of the paper	Title of the Journal	Date	Issue No. and Vol No.	Page No.	SCI/Scopus / UGC	Online version
1.	M.Sowjanya, Dr.P.Vijayi Bhaskar	VLSI Implementation of 32 bit Vedic Multiplier usingUrdhva Tiryakbhyam Sutra in Cadence Software.	International Journal of Research and Analytical Reviews (IJRAR)	June-2023	E-ISSN 2348-1269	115-121		Click here
2.	J. Mrudula B. Suneetha and V. Jayasri	VLSI Design Implementation of 32 X 32 Bit Vedic Multiplier	Industrial Engineering Journal	June-2023	Volume : 52, Issue 6	565-568	UGC	Click here to download
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