Unit Wise Important Questions (CO)

UNIT-1

1) Explain all arithmetic, shift and logic micro operations?

2) Discuss the use of three state bus buffer for the desired functionality of a bus system.

3) Describe functional view of the computer with its architecture.

4) Construct the 4-bit binary adder and binary incrementer circuit and explain its operation.

5) Explain signed magnitude, signed complement, signed 2's complement with examples.

UNIT-2

1) Describe various types of memory reference and register reference instructions with suitable Example ?

2) Draw and explain the common bus system.

3) Explain the stored program organization.

4) Illustrate in detail about the phases of the instruction cycle.

5) Write brief notes on BSA and ISZ.

UNIT-3

1) Describe the physical address forming in relative addressing modes.

2) What is an interrupt? Discuss different types of interrupts.

3) List any five types of addressing modes with suitable examples.

4) What is a CISC and RISC? What are the characteristics of CISC and RISC.

5) What is subroutine call and Return?

6) Draw and explain the format of micro instruction.

7) Draw and explain micro program control unit and micro program sequencer.

8) Design of control unit and its operation.

UNIT-4

1) Make use of circuit diagram of associative memory and explain briefly with one word of Associative Memory?

2) Explain in detail about the Auxiliary memory.

3) Discuss about the booth multiplication algorithm with example.

4) Discuss about the multiplication algorithm with example.

5) What is a Mapping? Explain in detail about the Associative, direct memory mapping.

6) Discuss the importance of memory address maps.

7) Explain signed magnitude, signed complement, signed 2's complement with examples

UNIT-5

1) Explain about the programmed I/O and Interrupt initiated I/O data transfer with example.
2) What is the priority interrupt? Explain parallel priority interrupt.
3) Make the use of circuit diagram and explain about DMA controller.
4) How handshake process is performed in Asynchronous data transfer.
5) Explain daisy chain priority interrupt
6) Demonstrate about Input - Output Interface.