



SRI KRISHNA INSTITUTE OF TECHNOLOGY

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Department of EE-VLSI

Subject Name: Verilog HDL

Subject Code: BVL304

SEM: DIV: III A

Faculty: Dr. N C Patil

Module-1 Question Bank

SL No	Question	CO	Level	Marks
1	Explain the evolution of Computer aided digital design and the emergence of HDLs.	1	L2	5
2	Explain the typical VLSI IC design flow	1	L2	5
3	Explain the importance of HDL	1	L2	5
4	What is the trend in HDLs	1	L2	5
5	What are Types of Design methodologies	1	L2	5
6	Explain module and instances with an example	1	L2	5
7	Explain the different ways of instantiation of stimulus block with an example	1	L2	5
8	Explain design block with an example	1	L2	5

Faculty Signature