

BANGALORE INSTITUTE OF TECHNOLOGY
K R ROAD, V V PURAM, BANGALORE-04
DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

COURSE BEGINNING SURVEY-2024-25 (Odd)
Batch: 2023-2027 CBCS

Course Title : Digital Design and Computer Organization

Course Code :BCS302

Sem: 3rd

USN							
NAME							

Prerequisites:

1. Introduction to Electronics Communication (BESCK204C)
2. Introduction to Electrical Engineering (BESCK104B)
3. Applied Physics (BPHYS102)
4. Introduction to C Programming (BESCK104E)

1	Which of the following combinations of logic gates can decode binary 1101? a) One 4-input AND gate c) One 4-input AND gate, one OR gate	b) One 4-input AND gate, one inverter d) One 4-input NAND gate, one inverter
2	3 bits full adder contains _____ a) 3 combinational inputs c) 6 combinational inputs	b) 4 combinational inputs d) 8 combinational inputs
3	The logic circuits whose outputs at any instant of time depends only on the present input but also on the past outputs are called _____ a) Combinational circuits c) Sequential circuits	b) Latches d) Flip-flops
4	The memory is divided into large number of small parts called ? a) location c) rows	b) cells d) columns
5	Which of the following is an example of secondary Memory? a) CD-ROM c) ROM	b) RAM d) CPU
6	Which of the following disadvantages of cache memory? a) Cache memory is faster than main memory b) It consumes less access time	

	c) It stores data for temporary use d) Cache memory has limited capacity
7	RAM is non-volatile a) TRUE b) FALSE c) Can be true or false d) Cannot say
8	Memory is primarily of _____ types. a) 0 b) 1 c) 2 d) 3
9	_____ is a very high speed semiconductor memory which can speed up the CPU. a) Primary Memory b) Secondary Memory c) Cache Memory d) All of the above
10	Size of the _____ memory mainly depends on the size of the address bus. a) Main b) Virtual c) Secondary d) Cache

Faculty-Incharges

Course Co-ordinator

Module Co-ordinator

IQAC

Programme Co-ordinator