S.No	IEEE Funded Events
1.	IEEE NANOTECHNOLOGY COUNCIL SB CHAPTER received an amount of Rs. 82,180/-. for conducting technical events
2.	Signal Processing Society (SPS) chapter of IEEE has approved an amount of Rs 20545/- for conducting technical events 2023.
3.	Signal Processing Society (SPS) chapter of IEEE has approved an amount of US$ 300 for conducting technical events in May, 2021. Approval letter
4.	Circuits and Systems (CAS) chapter of IEEE has approved an amount of US$1200.for conducting a series of events in the month of December, 2020.
	Other Events
	Dr. M.Jaganmohana Rao received a grant of Rs.100000 from SERB for the conducting of a two day symposium on "5G Communication and Beyond" during February 2-3, 2024.
5.	Department has conducted an online Faculty Development Programme on "Internet of Things (IoT)" during 01-05, December 2020 with the funding of Rs 93,000/- from AICTE Training and Learning(ATAL) academy. Coordinator: Mrs.M.Laxmi, Associate Professor Approval letter
6.	AICTE sponsored Short Term Training Programme on "Advances in Wireless Technologies and Telecommunication" was conducted during August-September 2020. STTP1: August 03-08, 2020 (Learning); STTP2: August 17-22, 2020 (Understanding) STTP3: Aug.31 – Sep.5, 2020(Implementation) Coordinator: Dr. P.Vijai Bhaskar, Professor and Dean Academics Grants Received: Rs.5 lakhs by AICTE. Approval letter
7.	Received an amount of Rs. 42, 798/- from IETE as membership rebate in the year 2020
8.	AICTE sponsored Two-week FDP on Advances in Signal and Image Processing with Hands-on Training using MATLAB was organized from 18th to 30th November, 2019. Prof. B. Hari Kumar was the convener and Dr. C. V. Narsimhulu was the Coordinator for this FDP. This event was funded with a grant of Rs.4.98 lakhs by AICTE. A total of 63 participants from various engineering colleges and research organizations from the states of Telangana, Karnataka, Tamilnadu and Andhra Pradesh have participated. Sanction Order

9.	AICTE Sponsored Faculty Development Programme (FDP) on "Advanced VLSI System Design" was organised from 17th to 29th June, 2013. Dr C.V.Narasimhulu was the co-coordinator of the event. Dr. S. Udaya Kumar was the Coordinator