

**S.R.K.R. ENGINEERING COLLEGE (AUTONOMOUS)****BHIMAVARAM – 534204: W.G.Dt., A.P.****Department of Computer Science and Engineering****I B.Tech. I Semester****Assignment-2****SUBJECT : Computer Fundamentals and Digital Logic**

<b>S.N O</b>	<b>Questions</b>	<b>CO</b>	<b>KL</b>
	<b>UNIT-III</b>		
1.	Solve Boolean function $F(A,B,C,D,E) = \sum(0,2,4,7,8,10,12,16,18,20,23,24,25,26,27,28)$ using K-Map.	3	3
2.	Solve Boolean function $F(A, B, C, D) = m(0, 1, 2, 3, 4, 5) + d(10, 11, 12, 13, 14, 15)$ using K-Map.	3	3
	<b>UNIT-IV</b>		
3.	Design a combinational circuit for Decimal Adder	4	3
4.	Design a combinational circuit for Priority Encoder.	4	3
	<b>UNIT-V</b>		
5.	Draw the circuit diagram of SR, D, JK, T flip flops and explain its operation with the help of a truth table and excitation table?	5	3
6.	Explain the step of design procedure for sequential circuit.	5	3

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