

PiE5

PiE5 Operation Manual

Introduction

Quick system description

- Detector Systems
 - Vertex
 - SciFi
 - SciTiles
- Services
 - Power distribution (TDK + MPDC boxes)
 - DCDC crates
 - HV boards
 - Environmental sensors
 - FrontEnd Crates (FEC)
- Cooling
 - Liquid cooling (chiller) for FEC (electronics, Water/Glycol)
 - Liquid cooling (chiller) for Sci* (Silicon oil)
 - Helium system for Vertex
 - Liquid cooling (chiller) for Helium

General principles

- **During beam time** Services and DAQ should be on all time
- **During cosmics** DAQ is off when no operations are going
 - Turned on at the beginning of the day
 - Turned off at the end
 - This means that all subsystems follow
- Single systems:
 - **During beam time** They should be on all time
 - **During cosmics** They should follow the DAQ (see above)
 - There should be a responsible on call or on site for each subsystem (?). This should take a decision if to turn off for any reason
- Shift handover
 - Each subsystem should take care of handing over a debriefing for the next shift (including a shift Elog)
 - Suggestion: open an elog and keep writing during shift
- Coordination
 - Check if there are conflicts before doing something on systems

- Ask all interested systems if you can proceed and wait to receive an answer
- Tools:
 - Mattermost
 - Stay in the integration Zoom room

Shift operation

Awareness / Safety

This manual focuses on system integrity, and is not an alternative for PSI (Personal) Safety Rules.

Before doing any work on the system check what is happening. Things like: is the magnet on? Is anyone working on the cage? Which systems are running? Is anyone debugging a piece of code?

MIDAS Alarms are implemented. Do not ignore any alarm! Alarm instructions linked [here](#)

Shift guidelines

Shifters are in charge! No system intervention without consulting shift crew (this includes PIs).

Priorities in this order:

1. Check system (e.g. **custom pages** and **history plots**)
2. Check data quality
3. Do your personal tasks (debugging, analysis,...)

Coordination

Before performing anything that alters the state of some systems, we must check that no-one else is working on it to avoid conflicts.

- Check with run coordinator and/or shift crew
- On-site checks:
 - Go around a specific set of locations (TBD) and check nobody is working there
 - This set depends if PiE5 is accessible or not
 - First check if PiE5 is closed
- Online checks:

- Make a page where every service and subsystem report if they are on and running
- Check this page at the beginning of the shift or operation
 - If something is not what you expect, call responsible

Summer 2025 Cosmic run

- ☐ Shift schedule with some instructions: [signup sheet](#)
- ☐ Non-expert / MEG walkthrough checklist [Mu3e daily area checklist](#)
- ☐ Shift rules
 - ☐ During remote operation, there should one on-call PSI person
 - ☐ At the end of the last shift, the system should be put in a safe state, and a check(list) of the system state should be reported in the elog
 - ☐ Non-shift days still need a completed checklist

Safe state

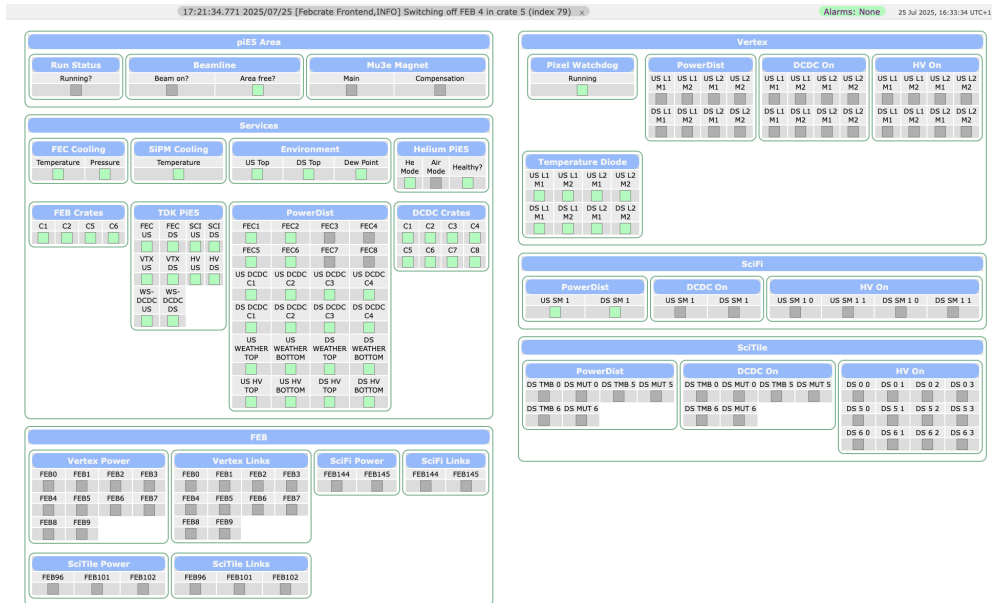
This might change, but currently:

- ☐ No Detector power
- ☐ FEBs off
- ☐ All SC (FEs) on
- ☐ All cooling running, set temperatures $\geq 15\text{C}$

System checklist

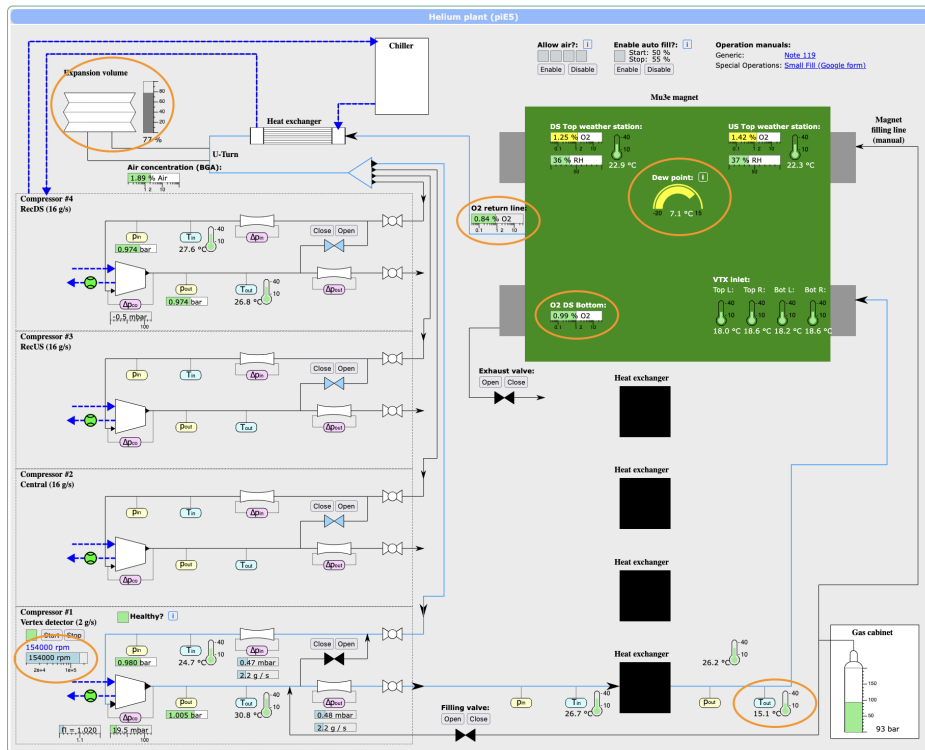
- ☐ Check and post the system status page

What you expected to see at the end of the last shift of the day (SciFi PowerDists can be on):



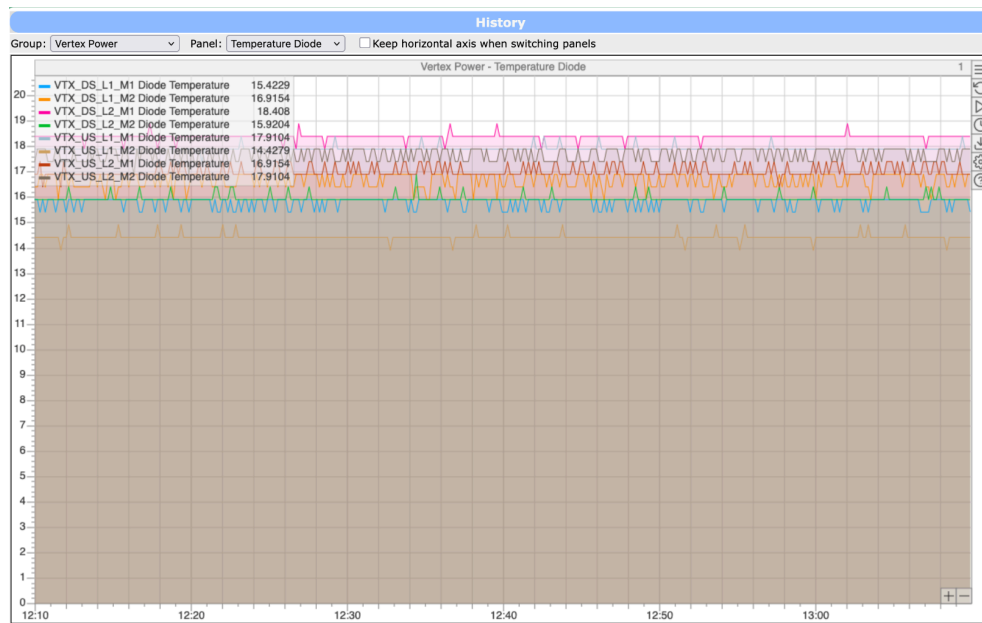
In addition:

- ☐ Helium: Compressor speed, O2 concentration DS Bottom, O2 concentration in return line, dew point, T after heat exchanger, expansion volume %
- Go Services/Helium piE5 page and check the values circled in **orange** (**!** Anything that turns **red** should be reported immediately):



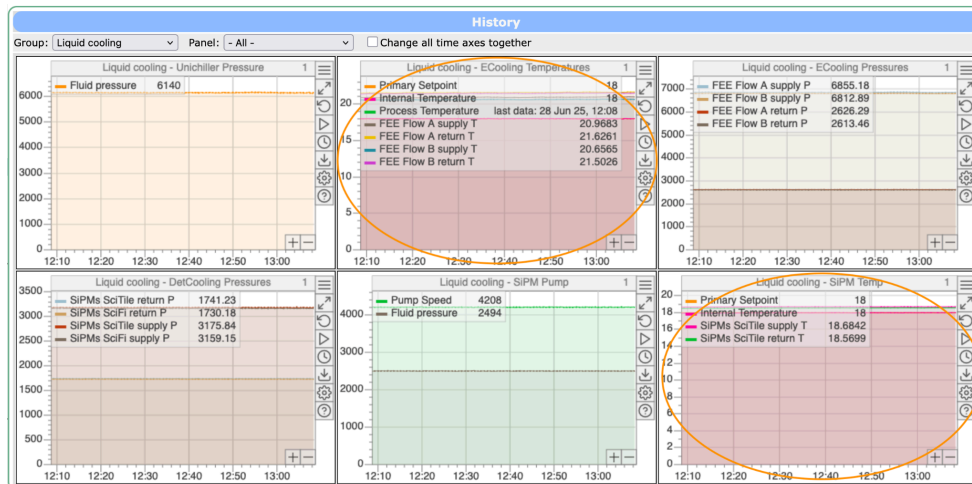
- ☐ Vertex: TDIODE max T

Go History/Vertex Power/Temperature Diode (⚠ not plural!) and record the highest diode temperature:

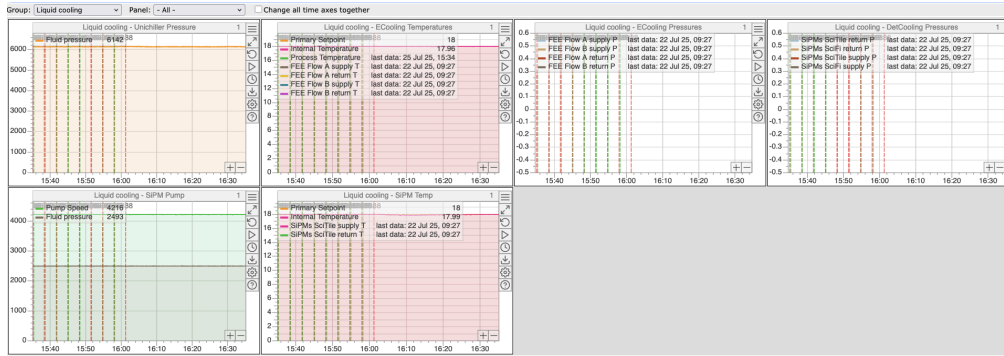


- ☐ E-Cooling and SiPM (Unistat) temperature, max FEB and FEB crate temperatures

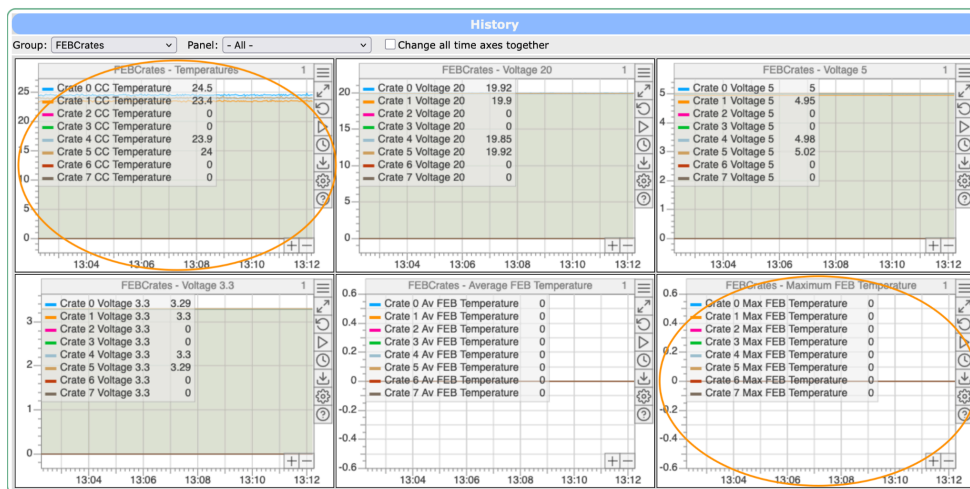
E- & SiPM-Cooling temps: Go History/Liquid cooling and check the 'Internal Temperature' circled in orange:



⚠ As of 22.07.2025, Chiller manifolds is temporarily disabled due to mscb334 issue. Hence, you will only see 'Primary Setpoint' and 'Internal Temperature' in those two plots:



Max FEB and FEB crate temps: Go History/FEBCrates and check Temperatures and Maximum FEB Temperature - you should see 0 for the former as all FEBs are off at the end of the last shift of the day:




⚠ you get 'no data available' on the FEB temp readings? Check if SW Downstream is on, also see <https://mattermost.gitlab.rlp.net/mu3e/pl/5z6ifyrojbggupy6hacpyuyv6h>

Services and their respective monitoring that should be running all time under normal operation

- Electronics chiller
- Detector cooling
- Helium system
 - Compressors
 - Chillers
- Interlock System
- Magnet
 - Power supplies
 - Compressors
 - Monitoring
-

Visual checks on site

Note: Walkthrough checklist for the No-beam run after the June 2025 beamtime:

 Mu3e daily area checklist

To enter the area you need instruction from Andreas Knecht (or deputy)

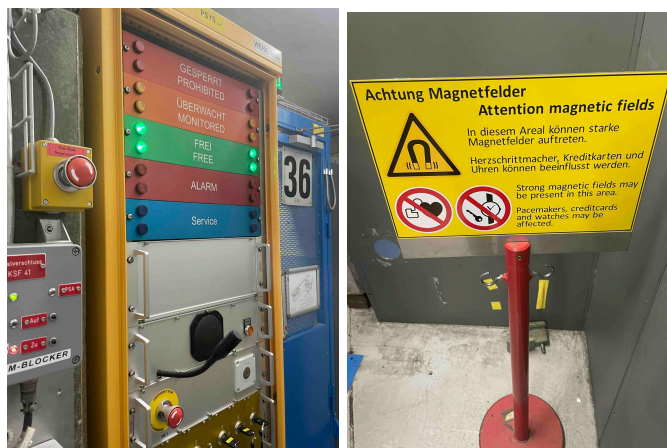
- Understand what you see
- Risks (e.g. Magnet on)
- Entrance procedures

Before startup after a period of interruption, or at least once a day the shift crew should do a walk through. Assess the state of all services as a non-expert = common sense approach. Ask questions if needed. Non exhaustive list

- Something leaking, making weird noises?
- Any device in an error state (Note that the Huber chillers have a maintenance warning which we ignore for the 2025 beamtime)
- Anything not connected which you think should be (e.g helium cooling tubes)
- Chiller running at sensible set points
- After a helium system startup the interlock system needs to be unlatched (reset) <https://elog.psi.ch/elog/Testbeams/3539> . If the interlock is engaged for any other reason first understand why. Reference [status](#) for 2025

Some PiE5 basics

Area state , and magnet warning. One can walk around the magnet with the field on, but no loose metal objects allowed.



- FEC chiller manifolds in nominal condition:



- Chiller for helium return running on platform 2



Operations on site (if needed)

Also have a look at

https://physi.uni-heidelberg.de/Forschung/he/mu3e/wiki/index.php/Operation_Procedures

Opening magnet flanges

2 people: one performing one checking. Open carefully. Make sure the flanged are hooked

Closing magnet flanges

3 people: one performing, two checking. Check that the power cables do not get squeezed on the top of the flanges (1 person) and the fibres do not get squeezed on the bottom of the flanges (1 person).

Service checks (online)

The principle is that services will be on all time.

Services (random)

- Magnet status
- Power distribution
- Helium
- Chillers
- FEBs
- Environment

Re-turning on

If something went off by any accident. List same as above but with turning on procedures rather than checks

- Magnet status
- Power distribution
- Helium
- Chillers
- FEBs
- Environment
-

Liquid cooling (FEC)

- Ideally, one should go & visually observe that valves are running in operations mode
- If valves are in bypass mode or chiller is not running, you are limited to running 1-2 FEBs per side & DCDC current draw at most 5-6 A
- ~~— Observe the values on the history page under Liquid Cooling~~
- Temperatures should ideally be stable at 15C

Turning on

Note: The circulation should not be toggled regularly.

step 1: Set the valves in DS + US LOOP configuration.

-> See attached picture for valve positioning.

step 2: Make sure the "Unichiller Temperature" Midas Alarm is active

Enable the "Unichiller running" warning

step 3: Make sure Liquid Cooling Frontend is running

step 4: Turn on temperature control

Go to the Liquid Cooling FECs Midas equipment

Check if the primary setpoint is set in the range 14-18 C

Start circulation by setting Temperature Control to 1.

step 5: Watch the system stabilize (pressure and temperature) in the History. If something is off, visually inspect the system again (Unichiller controller and manifold)

Turn Off

- Turn the chiller off Temperature Control to 0
- Watch the circulation stop in the history pages (pressure goes to 1 bar , flow goes down)
- Disable the Unichiller running warning in the midas alarm system

Operation

Temperature alarm has to be running

If the PID gets unstable (like in the top picture), or the alarm triggers:

- Turn the chiller off Temperature Control to 0
- Set system in bypass mode (See valve positions)

Monitoring

- It is observed that, if the valves are in bypass mode, the Unichiller Pressure runs at 4500
Note: This mode means there is no cooling water to the electronics - hence no cooling!
- While if the valves are in operations mode, the Unichiller Pressure runs above 6000
- Monitoring plots in the history system:

Temperatures:

Setpoint: Target outlet temperature of the chiller

Internal Temperature: Sensed by the chiller, outlet fluid temperature.

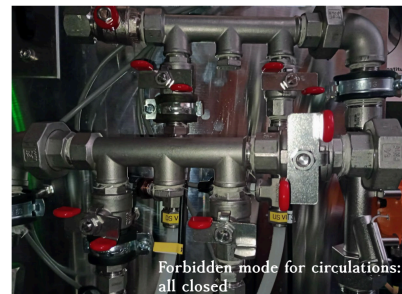
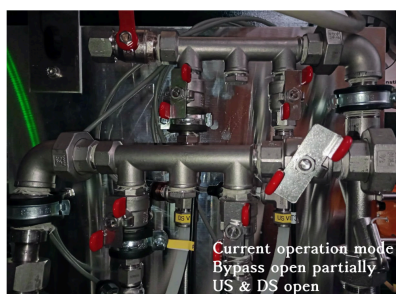
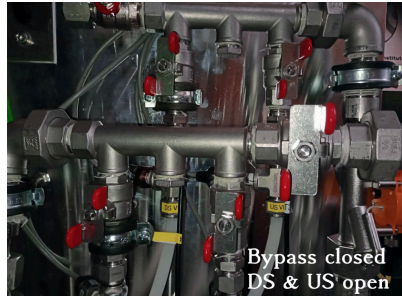
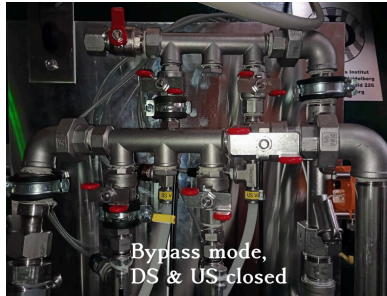
Flow A / B inlet & outlet Temperatures: Similar to setpoint Temperature, < 2°C difference between outlet and inlet, following the Internal temperature reading from the chiller

Flow:

To be calibrated. We see about 20l/min on the display

Pressures:

To be calibrated. Outlet pressures should both be around 6.5bar absolute; Inlet Pressures TBD



Running Helium System

Refer manual in Internal Note:

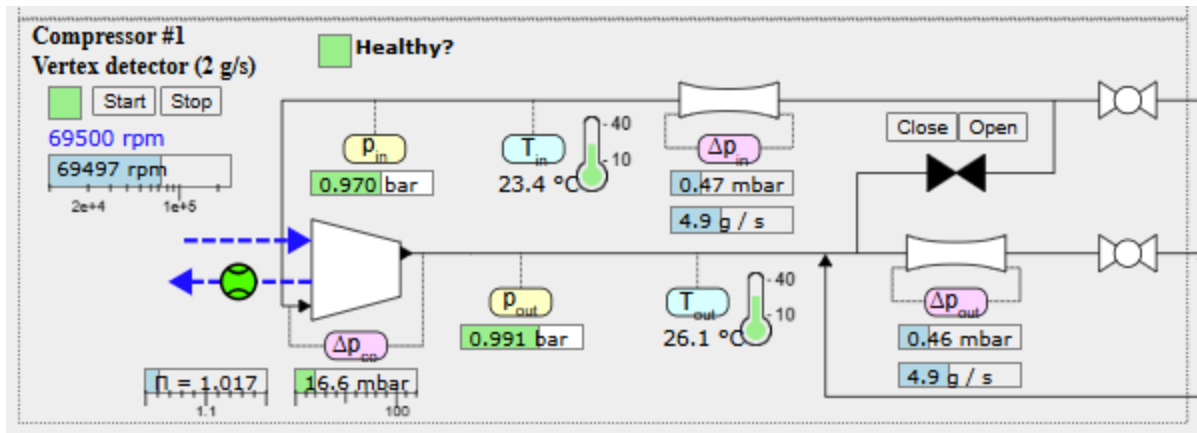
<https://www.physi.uni-heidelberg.de/Forschung/he/mu3e/restricted/notes/Mu3e-Note-0119-HePlantPiE5.pdf>

Running Helium System in Air Mode FW comment: we are running

Refer manual in Internal Note: <https://www.physi.uni-heidelberg.de/Forschung/he/mu3e/restricted/notes/Mu3e-Note-0119-HePlantPiE5.pdf>

Turn on

1. Ensure that Helium PiE5 FE is running and that all Allow Air? Indicators are dark green and not grey
2. Visually ensure that He lines are connected & not blocked
3. Set the demand compressor speed value to 20000 rpm
4. Start the compressor
5. Increase the compressor speed in steps of 10000 + 5000 to 35000 rpm
6. Check that the bypass is closed (black color, not blue). If unsure, you can hover your mouse over the bypass icon to read the status. If bypass is not closed, then press the Close button.
7. Increase the compressor speed in steps of 10000 to 60000 rpm.
8. Ensure that Healthy? Icon turns to green



Turn off

1. Make sure that no one needs cooling in the first place and that Pixels is off
2. Reduce the speed to 35000 rpm
3. After a few seconds, reduce the speed to 20000 rpm
4. Stop the compressor

Small refill of Helium System

1. The expansion volume level approaches 20%
2. Please perform a so called "small refill"
3. The procedure is described in a google form, also the some documentation is directly stored:
 - a. <https://forms.gle/WDXufxyse1PEVEDD8>
4. Please create also an ELOG

Turn on systems (only when necessary)

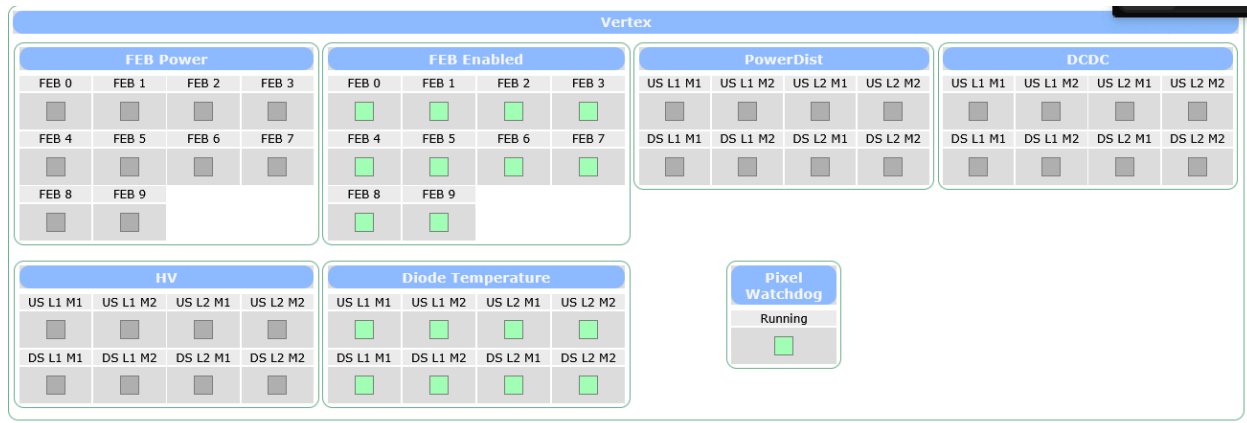
Turning on a system means turning on the Power Distribution, DCDC and HV channels specific to that system.

3 subsystems ran in parallel. They can be turned on all or just some. Must be clarified for each shift.

Turning on can be done only one by one.

Before starting

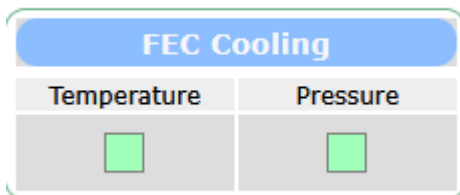
Go to the page Services/System Status and make sure the your equipment is in the status you expect it. For instance, for Vertex:



This means that the Vertex is off (Power Dist, DCDC and HV all gray), the temperatures are OK, Watchdog running, but the FEBs are off. All FEBs are enabled.

Frontend boards

You can turn on FEBs from the DAQ/FEB Crates page. For running a FEB, make sure it is enabled in the ODB. **MAKE SURE THE COOLING IS WORKING BEFORE!** This can be



verified in the System Status page:

This is how the page looks when all FEBs are on:

Crate 1 MSCB464.psi.ch 1001 20V 17.77 V 3.3V 3.29 V 5V 4.99 V T 36 C Disable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 FEB 144 0°C OFF	Slot 12 FEB 145 0°C ON	Slot 13 FEB 4 0°C ON	Slot 14 FEB 0 0°C ON	Normal Image
Crate 2 MSCB465.psi.ch 1002 20V 17.57 V 3.3V 3.30 V 5V 4.95 V T 24 C Disable	Slot 0 FEB 1 0°C ON	Slot 1 FEB 2 0°C ON	Slot 2 FEB 3 0°C ON	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image
Crate 3 0 Disconnected 20V 0.00 V 3.3V 0.00 V 5V 0.00 V T 0 C Enable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image
Crate 4 0 Disconnected 20V 0.00 V 3.3V 0.00 V 5V 0.00 V T 0 C Enable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image
Crate 5 MSCB472.psi.ch 1005 20V 17.09 V 3.3V 3.30 V 5V 4.98 V T 35 C Disable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 FEB 147 0°C ON	Slot 12 FEB 8 0°C ON	Slot 13 FEB 7 0°C ON	Slot 14 FEB 6 0°C ON	Normal Image
Crate 6 MSCB473.psi.ch 1006 20V 16.78 V 3.3V 3.29 V 5V 5.02 V T 25 C Disable	Slot 0 FEB 5 0°C ON	Slot 1 FEB 9 0°C ON	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 FEB 96 0°C ON	Slot 5 FEB 97 0°C ON	Slot 6 FEB 98 0°C ON	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image
Crate 7 0 Disconnected 20V 0.00 V 3.3V 0.00 V 5V 0.00 V T 0 C Enable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image
Crate 8 0 Disconnected 20V 0.00 V 3.3V 0.00 V 5V 0.00 V T 0 C Enable	Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image

Vertex FEBs

The vertex FEBs are the ones labelled from 0 to 4 (US) and 5 to 9 (DS). If you want to use a portion of them, make sure you disable them before running SW Central. To enable/disable a FEB, go to /Equipment/LinksCentral/Settings, set LinkMask to 3 (enabled) or 0 (disabled) to the value at the corresponding index. The tables in Services/System Status will help you.

To turn on from scratch:

- Turn on all FEBs (Feb 0 to Feb 9)
- You can tell if the buttons are working as you should see messages about turning on FEBs
 - If you don't see the messages, reload the page or restart FebCrate Frontend
- And you should see messages like these in the Messages page (x10):

```
[Febcrate Frontend,INFO] Switching on FEB 2 in crate 1 (index 17)
[Febcrate Frontend,INFO] Switching on FEB 1 in crate 1 (index 16)
[Febcrate Frontend,INFO] Switching on FEB 0 in crate 1 (index 15)
[Febcrate Frontend,INFO] Switching on FEB 14 in crate 0 (index 14)
[Febcrate Frontend,INFO] Switching on FEB 13 in crate 0 (index 13)
```

- → **Wait one minute** until all links are stabilized!!!
- Start SW Central from the Programs page
 - Wait for receiving the message “Done” in the “switch” message section
 - Make sure there are no errors in the Message page regarding FEB connections
 - To cross-check everything worked fine, check the page DAQ/FEBs, it should look

Central

0	1/14		0xeb90633d VP
1	2/0		0x6d7566c5 VP
2	2/1		0x6d7566c5 VP
3	2/2		0x6d7566c5 VP
4	1/13		0x6d7566c5 VP
5	6/0		0xdff54824 VP
6	5/14		0xdff54824 VP
7	5/13		0xdff54824 VP
8	5/12		0xdff54824 VP
9	6/1		0xdff54824 VP
10	0/-1		U

like this:

- If one of the FEBs on the top left has red dots:
 - Stop SW Central
 - power-cycle the FEB(s) from the DAQ/FEB crates page as above
 - Wait a bit
 - Start SW Central

Known issues:

- You receive a message like this:

```
11:03:07.673 2025/03/24 [SW central,ERROR] [switch_fe.cpp:222:frontend_init(),ERROR] No switching board present or driver not loaded
11:03:07.665 2025/03/24 [SW central,ERROR] [switch_fe.cpp:632:init_mudaq(),ERROR] could not open device
```

- Go to switch0 ~/online/common/kerneldriver and run `sudo ./load_mudaq.sh`
- If a FEB malfunctions and causes all chips to lose configuration, avoid power-cycling the FEBs with the Vertex detector still on. Power down vertex then power-cycle the FEBs.

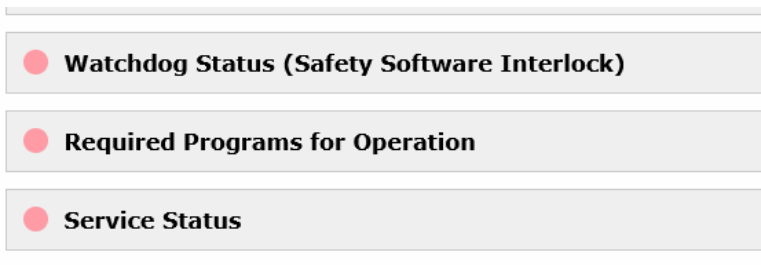
Vertex

Glossary and general guidelines:

- Turn-on power partition (PP): turn on LV and HV, adjust HV to operational
- Adjust VDD power partition: adjust LV trim to hit target VDD
- Turn-off power partition: turn off HV and LV
- Power-up a chip: bring the chip to full power mode
 - This can be done to all chips in a power partition
- Power-down a chip: bring the chip to the minimum power mode
 - This can be done to all chips in a power partition
- Most buttons in the custom page start a sequencer script
 - They get gray (disabled) while the sequencer is running
 - They get blue again (enabled) when it is over
 - Wait every time you press a button

Check status

From the page Pixel/Pixel Control check the 3 dots on top left, they should be green:



If any is red, click on the bar to see details. Typical reasons:

- Watchdog is red
 - TDK: likely interlocked
 - Toggle interlock in the area
 - Go to Status, click TDK PiE5 and set on the pixel TDK

#	Name	Output On	Mode	Demand	Voltage	Current limit	Current
0	FEC US	Yes	CV	20.00 V	19.99 V	7.00 A	0.23 A
1	FEC DS	Yes	CV	20.00 V	19.99 V	11.00 A	0.25 A
2	TDK13	No	OFF	20.00 V	0.00 V	0.50 A	0.00 A
3	TDK14	No	OFF	20.00 V	0.00 V	0.50 A	0.00 A
4	TDK15	No	OFF	20.00 V	0.00 V	0.50 A	0.00 A
5	TDK16	No	OFF	20.00 V	0.00 V	0.50 A	0.00 A
6	SCI US	Yes	CV	20.00 V	19.99 V	2.00 A	0.00 A
7	SCI DS	No	OFF	20.00 V	0.00 V	5.00 A	0.00 A
8	VERTEX US	Yes	CV	20.00 V	19.99 V	7.00 A	0.00 A
9	VERTEX DS	Yes	CV	20.00 V	19.99 V	7.00 A	0.00 A
10	HV US	Yes	CV	20.00 V	20.00 V	3.00 A	1.41 A
11	HV DS	Yes	CV	12.00 V	12.00 V	3.00 A	2.43 A
12	TDK23	No	OFF	5.00 V	0.00 V	0.50 A	0.00 A
13	TDK24	No	OFF	5.00 V	0.00 V	0.50 A	0.00 A
14	SC US	Yes	CV	12.00 V	12.00 V	1.00 A	0.30 A
15	SC DS	Yes	CV	12.00 V	12.00 V	1.00 A	0.47 A

The mode should be CV

- Required programs red
 - Some programs not running

- Open the bar, check which program and start it from the Program page
 - Note: SW Central depends on FEBs, see next point
- Service Status red:
 - DCDC Crates off
 - Use the button “Set Crates on”. Press it multiple times if it does not work.
 - FEB Power off
 - SW Central program might be stuck. Stop and start it from the Programs page.
 - If the FEBs are off:
 - Turn them on from the DAQ/FEB Crates page:

Crate	Model	Status	Slot 1	Slot 2	Slot 3	Slot 4	Slot 5	Slot 6	Slot 7	Slot 8	Slot 9	Slot 10	Slot 11	Slot 12	Slot 13	Slot 14	Image
Crate 1	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
Crate 2	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
Crate 3	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
Crate 4	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
Crate 5	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
Crate 6	MSC8464 pos.ch	Connected	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image

this is how it

should appear. Make sure you also see messages on the top bar showing they are being turned on.

- Wait 30-60 seconds and start SW Central
- Check the DAQ/FEBs page, and make sure all dots for the FEBs




FEB	Model	Status	Slot 1	Slot 2	Slot 3	Slot 4	Slot 5	Slot 6	Slot 7	Slot 8	Slot 9	Slot 10	Slot 11	Slot 12	Slot 13	Slot 14	Image
0	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
1	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
2	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
3	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
4	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
5	0x2f9035a5	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
6	0x2f9035a5	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
7	0x2f9035a5	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
8	0x2f9035a5	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
9	0x2f9035a5	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image
10	0xc72c914b	VP	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	0°C	Normal Image

0-9 are blue:

- If some is red:
 - Restart SW Central (and wait up to a minute, display will also not update until it starts again)
 - If multiple restarts doesn't work, try to power-cycle the FEB

Check Helium






















From the System Status page, check the Helium table:

Helium PiE5		
He Mode	Air Mode	Healthy?
		

Turn on

Make sure the status dots on top right are green

From the Pixel/Pixel Control page, in the power tab, select the power partitions you want to turn on and click “Turn On Selected PPs”:

Power Control													
Turn On Selected PPs				Turn Off Selected PPs				Adjust Vdd for Selected PPs					
Power Status													
Power Partition	Sel.	Resistivity	Input ch. on	Pwr Good	Adjust	LV curr	HV on	HV volt	HV curr	VDD sense	VDD target	Temp sense	Temp. Intlk
US-L1-Top	<input checked="" type="checkbox"/>	80			-9.00 %	0.000		0.000	0.540	0.396	1.850	17.413	
US-L1-Btm	<input checked="" type="checkbox"/>	80			-9.00 %	0.025		0.000	0.600	0.284	1.850	13.433	
US-L2-Top	<input checked="" type="checkbox"/>	380			-9.00 %	0.000		0.000	0.550	0.373	1.850	15.920	
US-L2-Btm	<input checked="" type="checkbox"/>	80			-9.00 %	0.025		0.000	0.560	0.377	1.850	17.413	
DS-L1-Top	<input type="checkbox"/>	80			-9.00 %	0.150		0.000	0.550	0.341	1.850	15.423	
DS-L1-Btm	<input type="checkbox"/>	80			-9.00 %	0.000		0.000	0.530	0.217	1.850	15.920	
DS-L2-Top	<input type="checkbox"/>	380			-9.00 %	0.075		0.000	0.550	0.334	1.850	17.910	
DS-L2-Btm	<input type="checkbox"/>	80			-9.00 %	0.050		0.000	0.560	0.380	1.850	15.423	

Here will we turn on US PPs

[If Temp Intlk is red contact experts]

Now the buttons should become gray (disabled) until all selected power partitions are on.

Known issues

- The Watchdog gets red and the Sequencer gets paused, an error should appear on the

11:48:33.203 2025/06/01 [Sequencer.TALK] Sequencer is paused. X

Turn Off All Power Immediately

Open Manual Open Operation Form

Sequencer: **Paused** Run: Stopped

Watchdog Status (Safety Software Interlock)

Required Programs for Operation

Service Status

Expert **Power** Manual Config Calibration Tuning Slow Control

Power Control

Turn On Selected PPs Turn Off Selected PPs Adjust Vdd for Selected PPs

Power Partition	Sel.	Resistivity	Input ch. on	Pwr Good	Adjust	Power Status									
						LV cur	HV on	HV volt	HV cur	VDD sense	VDD target	Temp sense	Temp. Intik		
US-L1-Top	✓	80	✓	✓	-9.00 %	1.475	50.000	1.130	1.720	1.850	29.851				
US-L1-Btm	✓	80	✓	✓	-9.00 %	0.025	0.000	0.600	0.281	1.850	13.433				
US-L2-Top	✓	380	✓	✓	-9.00 %	0.000	0.000	0.550	0.372	1.850	16.418				
US-L2-Btm	✓	80	✓	✓	-9.00 %	0.025	0.000	0.560	0.377	1.850	17.413				
DS-L1-			✓	✓	-9.00 %										

top bar, buttons get blue

- Call an expert (LV, TS, J)

Adjust Voltage

Click “Adjust Vdd for Selected PPs”

At the end, in the geometry view on top right, you should find Power and Sense boxes all green:

US Layer 1 Top

Open Select Power+Sense LVDS

LV HV T Enabled: 26/36 Working: 0/26

US Layer 1 Bottom

Open Select Power+Sense LVDS

LV HV T Enabled: 23/36 Working: 0/23

US Layer 2 Top

Open Select Power+Sense LVDS

LV HV T Enabled: 30/45 Working: 0/30

US Layer 2 Bottom

Open Select Power+Sense LVDS

LV HV T Enabled: 41/45 Working: 1/41

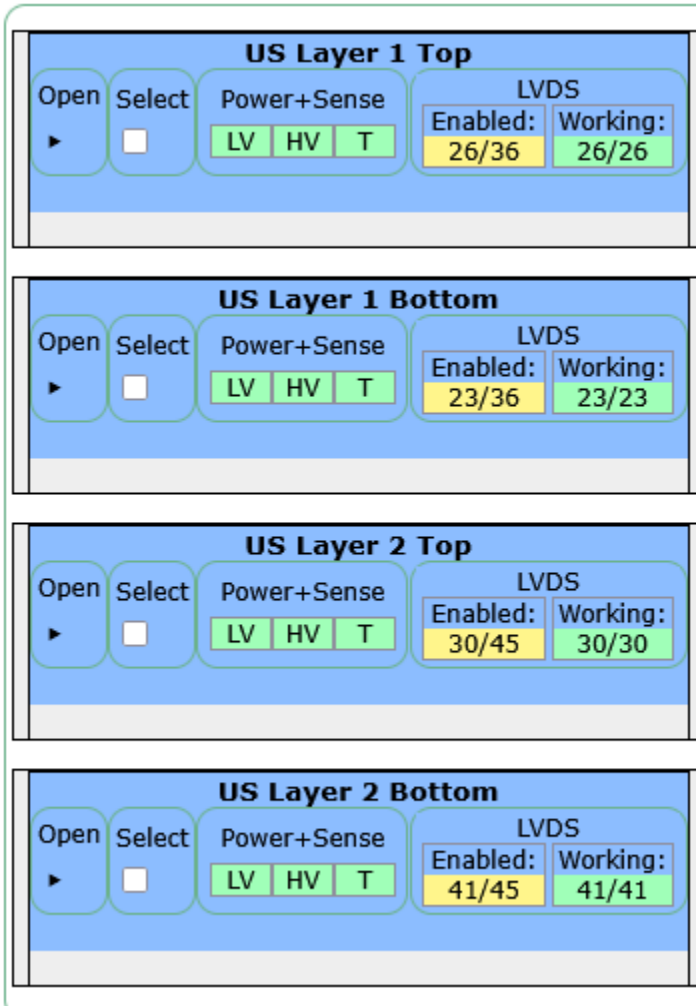
DO NOT CLICK “Set HV for selected PPs” yet!

Power up

We do it all PPs together

- In the geometry view on the top right, click “Select all active sensors”
- In the Manual Config tab, click “Configure Min -> SC”
- In the Manual Config tab, click “Configure SC -> Full”
 - If the LV box gets blue on the top of the power partition (pic above), the VDD went down
 - Go to the Power page
 - Select the power partition
 - Click “Adjust VDD...”

At this stage you should see something like this:



All Power+Sense green, all enabled LVDS links working

Set HV voltage

Go back to the power tab and click “Set HV for selected PPs”

Noise Scan Procedure with PySequencer

1. Prepare the Scan

Power Control														
Turn On Selected PPs			Turn Off Selected PPs			Adjust Vdd for Selected PPs			Set HV for Selected PPs					
Power Status														
Power Partition	Sel.	Resistivity	Input ch. on	Pwr Good	Adjust	LV curr	HV on	HV volt	HV curr	HV target	VDD sense	VDD target	Temp sense	Temp. Intlk
US-L1-Top	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	12.00 %	5.275	<input type="checkbox"/>	60.000	1.760	60	1.954	1.950	63.184	<input type="checkbox"/>
US-L1-Btm	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	1.50 %	4.325	<input type="checkbox"/>	60.000	2.450	60	1.990	1.950	50.249	<input type="checkbox"/>
US-L2-Top	<input checked="" type="checkbox"/>	380	<input type="checkbox"/>	<input type="checkbox"/>	1.50 %	6.225	<input type="checkbox"/>	10.000	66.130	10	1.958	1.950	105.473	<input type="checkbox"/>
US-L2-Btm	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	-1.50 %	7.000	<input type="checkbox"/>	0.000	0.000	40	1.970	1.950	85.572	<input type="checkbox"/>
DS-L1-Top	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	7.50 %	5.400	<input type="checkbox"/>	60.000	19.930	60	1.932	1.950	83.085	<input type="checkbox"/>
DS-L1-Btm	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	3.00 %	3.675	<input type="checkbox"/>	60.000	0.010	60	1.946	1.950	71.144	<input type="checkbox"/>
DS-L2-Top	<input checked="" type="checkbox"/>	380	<input type="checkbox"/>	<input type="checkbox"/>	-3.00 %	5.800	<input type="checkbox"/>	10.000	0.000	10	1.922	1.900	105.473	<input type="checkbox"/>
DS-L2-Btm	<input checked="" type="checkbox"/>	80	<input type="checkbox"/>	<input type="checkbox"/>	-1.50 %	7.725	<input type="checkbox"/>	60.000	11.010	60	2.032	1.950	88.557	<input type="checkbox"/>

-Dont write TDACs

-If Vertex is started properly and all power partitions should look like this:

-Start manually a run.

-Then open the tmux session on the backend to monitor the PySequencer.

- Open the file `vtx_noise_scan.py` (e.g. in VSCode) and edit the parameters according to the table below:

VPDAC_value

TDAC_value

start_iteration

	5	0	20000
	5	1	21000
	5	2	22000
	5	3	23000
	5	4	24000
	5	5	25000
	5	6	26000
x	5	7	27000

It's recommended to change and save the parameters directly in VSCode to avoid mistakes.

2. Start the PySequencer

1. Go to `odb/Equipment/PySequencer/State` → Check that the `Filename` is set to `vtx_noise_scan.py`.
2. Go to `odb/Equipment/PySequencer/Command` → Click “Load new file”.
3. Go to `odb/Equipment/PySequencer/Param/Value` → Verify that the parameters are correctly loaded from your script.
4. In `odb/Equipment/PySequencer/Command`, click “Start script” to begin the scan.

3. Troubleshooting Tips

- In the tmux session, watch the initial output:
- If the parameters are not initialized properly, click “Start script” again. (This is a known MIDAS bug.)
- If many chips show noisy pixels immediately:

- Click “Stop”, then click “Start script” again. This usually resolves the issue.

The script takes approximately 5–7 hours to complete.

4. Organize Output Files

Once the scan is finished:

1. Navigate to: /home/mu3e/online/online/userfiles
2. Create a new folder named: vpdac_5_tdac_<TDAC_value> (Replace <TDAC_value> with the actual TDAC value used in your scan)
3. Copy the hitmap files from the correct iteration range:

```
find . -maxdepth 2 -wholename  
"./tdac_files_runs/hitmap_chip_*_iteration_*.bin" \  
| awk -F'[_./]' '{for (i = 1; i <= NF; i++) {if ($i == "iteration" && (i+1) <= NF  
&& $((i+1)) ~ /^[0-9]+$/) {if ($((i+1)) >= 20000 && $((i+1)) <= 21000)  
print}}}' \  
| xargs -l{} cp {} vpdac_5_tdac_<TDAC_value>/
```

Update the iteration range based on the scan settings.

5. Copy Final TDAC Masks

1. Inside your folder vpdac_5_tdac_<TDAC_value>, create a subfolder:

```
mkdir vpdac_5_tdac_<TDAC_value>/final_tdacs
```

2. Copy the final TDAC mask files into this folder:

```
cp tdac_files/* vpdac_5_tdac_<TDAC_value>/final_tdacs/
```

Again, make sure to replace <TDAC_value> accordingly.

Write TDACs

We do it all PPs at a time for now

- Make sure the active sensors are still selected
 - otherwise on the top right click “Select all active sensors”

- From the Manual Config tab click “Write TDACs”
- The
- If you click on the LVDS box you should see something like this:

			Locked	Ready	DPA	Alignments	Phase	Out of Phase	Disp. Err	8b10b Err	Hits	Active	Inverted	Matrix
Ladder 1 (FEB 0)	Chip 1 gID=1	Link 0	-1	1	1	52	1	0	0	0	1650	yes	no	C
		Link 1	-1	1	1	46	2	0	0	0	1211	yes	no	B
		Link 2	-1	1	1	12	1	0	0	0	3174	yes	yes	A
	Chip 2 gID=2	Link 3	-1	1	1	37	0	0	0	0	192	yes	yes	B
		Link 4	-1	1	1	46	0	0	0	0	282	yes	yes	A
		Link 5	-1	1	1	40	0	0	0	0	276	yes	yes	C
	Chip 3 gID=3	Link 6	-1	1	1	40	2	0	0	0	7055	yes	yes	A
		Link 7	-1	1	1	10	2	0	0	0	135	yes	yes	C
		Link 8	-1	1	1	52	2	0	0	0	1560	yes	yes	B
Ladder 2 (FEB 0)	Chip 1 gID=33	Link 9	-1	1	1	49	2	0	0	0	1341	yes	yes	C
		Link 10	-1	1	1	37	2	0	0	0	1088	yes	yes	B
		Link 11	-1	1	1	40	2	0	0	0	2507	yes	yes	A
	Chip 2 gID=34	Link 12	-1	1	1	23	0	0	0	0	1163	yes	yes	B
		Link 13	-1	1	1	55	0	0	0	0	2417	yes	yes	A
		Link 14	-1	1	1	38	0	0	0	0	1730	yes	yes	C
	Chip 3 gID=35	Link 15	-1	1	1	6	1	0	0	0	1433	yes	yes	C
		Link 16	-1	1	1	54	1	0	0	0	6950	yes	yes	A
		Link 17	-1	1	1	24	1	0	0	0	86	yes	yes	B
Ladder 3 (FEB 0)	Chip 1 gID=65	Link 18	-1	1	1	30	3	0	0	0	149	yes	no	B
		Link 19	n.a.	n.a.	n.a.	n.a.	n.a.	n.a.	n.a.	n.a.	n.a.	no	no	-
		Link 20	-1	1	1	50	3	0	0	0	139	yes	yes	A
	Chip 2 gID=66	Link 21	-1	1	1	52	3	0	1	1	3544	yes	yes	B
		Link 22	-1	1	1	10	3	0	0	0	4948	yes	yes	A
		Link 23	-1	1	1	57	3	65536	0	0	9710	yes	no	C
	Chip 3 gID=67	Link 24	-1	1	1	56	3	65536	0	0	185	yes	yes	A
		Link 25	-1	1	1	14	3	0	0	0	123	yes	yes	C
		Link 26	-1	1	1	20	3	0	0	0	234	yes	yes	B

with a certain hit rate due to noise. (Out of phase should be 0 as well)

At this point, if you close all power partitions on

SciFi

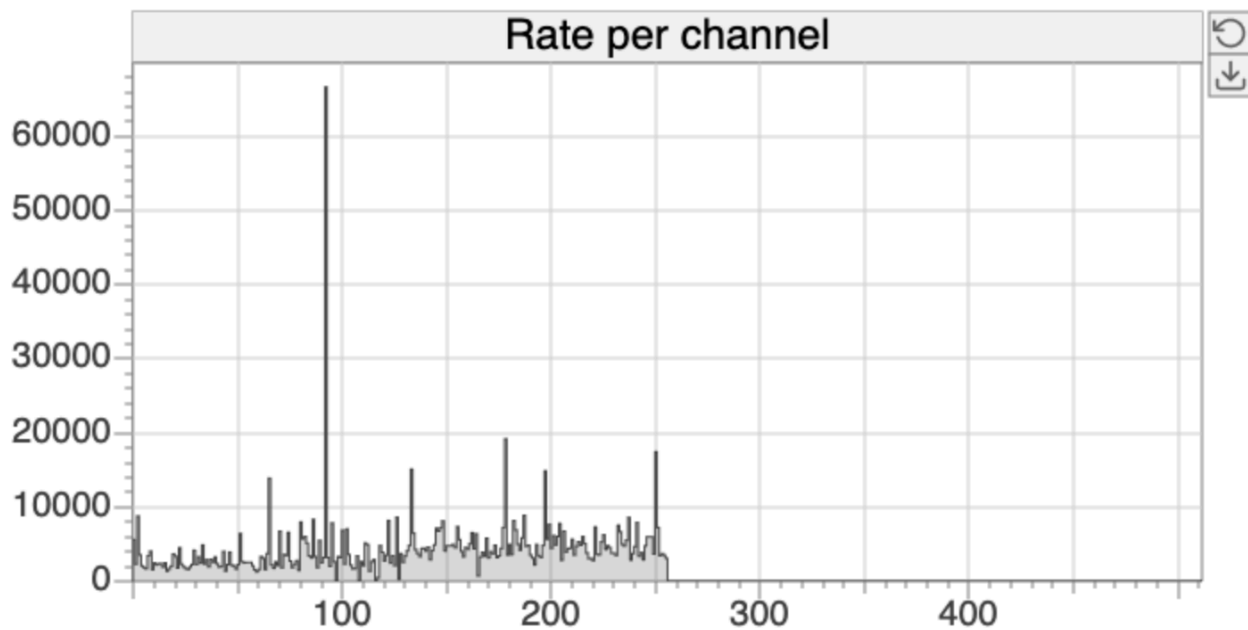
Basic startup sequence:

1. LV on
2. HV on
3. Configure
4. Toggle all resets

See also <https://elog.psi.ch/elog/Testbeams/4958> and the SciFi monitoring custom page

Monitoring during Cosmic Run Jul 25

- In the “Scifi-Monitor” custom page, check the LV and HV current
 - LV should be in the region of 6.5 ± 0.1 A
 - HV should be 6-10 uA



- Under DQM Plots check the plot “rate per channel”, this should look something like the one above, i.e. somewhat flat
 - Some outliers are ok
 - Channels 128-255 have generally a bit a higher rate than 0-127
- Check the efficiency of sorter 0, it should be in the high 90ies

Configure	Configure PRBS	Configure PLL	Configure Short	Configure Short 0pe5	Configure Short 1pe5	Configure Short 2pe5	Configure AllOff
TestPulsesTDC no ▾	Reset Datapath no ▾	Reset ASICs no ▾	Reset Counters no ▾	Reset LVDS no ▾			
Lapse Boundary low 10640	Lapse Boundary high 22127	Lapse delay 1000	Sorter delay 0	Sorter0:0.995 In0:0.998 In1:0.993 Over.:0		Sorter1:0.932 In0:0.714 In1:0.956 Over.:0	

- If not, try pressing configure and the resets (will jump back to no immediately) and restart a run
- If this does not work, try turning off HV, then LV and then restarting LV then LV

SciFi Monitor and Control							
Steps	Settings						
	Configure	Configure PRBS	Configure PLL	Configure Short	Configure Short 0pe5	Configure Short 1pe5	Configure Short 2pe5
	TestPulsesTDC no	Reset Datapath no	Reset ASICs no	Reset Counters no	Reset LVDS no		
	Lapse Boundary low 10640	Lapse Boundary high 22127	Lapse delay 1000	Sorter delay 0	Sorter0:0.989 In0:0.974 In1:0.996 Over:0	Sorter1:0.759 In0:0.749 In1:0.794 Over:14	
	Expert Settings ▼						
	Mask Editor ▼						
1.	Start a new shift report	Create shift report	Help?				
2.	Shifter: (empty) Run number: 6617 Last run start: Wed Jul 23 11:56:04 2025 Last run stop: Wed Jul 23 11:55:50 2025						
3.	Turn on FEBs / SWB ▼						
4.	LV Settings ▼						
5.	HV Settings ▼						
6.	IV Scan ▼						
7. (optional)	PLL Test ▼						
8. (optional)	Rate Scan ▼						
9.	Rate Tuning ▼						
10.	Sorter Tuning ▼						
	<input type="checkbox"/> Sorter tuning done?	Start Sorter Scan	Start Sorter Tuning	Done			
11.	DQM Plots ▼						

- If it's still low, click start sorter tuning

Normal operation:

Go to the SciFi/Scifi-Monitor page

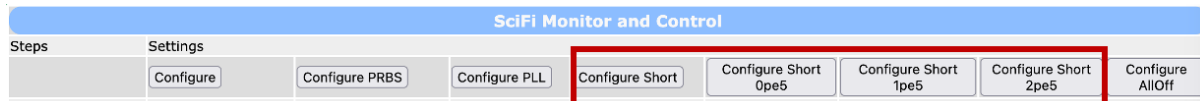
3.	Turn on FEBs / SWB ▼				
	<input type="checkbox"/> FEBs on? SWB on?	Crate1 on/off <input checked="" type="checkbox"/>	Crate5 on/off <input checked="" type="checkbox"/>	FEB145 on/off <input type="checkbox"/>	FEB147 on/off <input type="checkbox"/> SW Fibre on/off

- check that the Crates are on (green light)
- Start the FEBs by ticking these two boxes
- Click on SW Fibre on/off to be directed to the Programs page, there start the SW Fibre if it is not yet running

LV

4.	LV Settings ▼			
LV (on/off) SCI_US_SM_1 <input type="checkbox"/> LV on/okay?	no	0.00	0.049999997	
LV (on/off) SCI_DS_SM_1 <input type="checkbox"/> LV on/okay?	no	0.00	0	

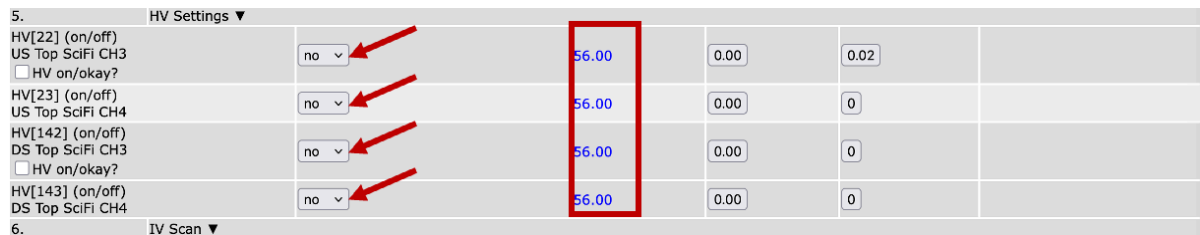
- turn on the HV by setting the dropdown selectors to "yes"
 - before configuring the current is somewhat random, but usually around 8 A and slowly dropping



- Press “Configure Short” with the desired threshold
 - the current should drop to ~6.5 A

HV

- Never turn on HV when not configured in short mode



- In the red box is where we set the HV value
 - normal operation point is 56 V
 - this can only be set per HV board, i.e. per side
- set the dropdown selectors (red arrows above) to yes for the channels you want to turn on
 - the expected current (last line) changes with irradiation (and if there is beam), currently (after June 25 TB) it is around 10 uA for noise runs

PLL Test

- turn on LV, make sure HV is off
- configure short
 - the LV current should go to ~6.5 A
- configure PLL
 - the LV current should go to ~6 A
- now choose channels you want to look at by clicking on them and setting mask to “y”
->click **Configure**
- start a run
- you should see under PLL Test a peak at 200 k, sometimes there are also some other peaks, but as long as the 200 k one is there it is locked
- **ch 128-159 and 192-223 will not lock, should be masked**
- if not locked
 - the vnvcdelay0, vncnt0, vncnt0 are for channels 0-31, vnvcdelay1... for ch 32-63 etc
 - change mostly vnvcdelay to get it to lock (usually values between 10 and 30)
 - first try to go to 0 and then back to what it was
 - if you can't get it to lock with vnvcdelay, try vncnt and vncnt

IV Scan

- turn on LV
- in HV settings set the channel you wanna test to yes
- in IV Scan press start scan
- download the scans (csv and png) on top right of plot and post them here:
<https://elog.psi.ch/elog/Testbeams/4795>

Turn Off

- In the Scifi-Monitor custom page, under 5. HV, turn off the HV (set the dropdown selector to “no”, you can leave the set value at 56 V
- Under 4. LV set the dropdown selectors to “no”



3.	Turn on FEBs / SWB ▼				
<input type="checkbox"/> FEBs on?	<input type="checkbox"/>	Crate1 on/off	Crate5 on/off	FEB145 on/off	FEB147 on/off
<input type="checkbox"/> SWB on?	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>
4.	LV Settings ▼				

- Under 3 uncheck the two boxes on the right to turn off the FEBs

General Notes

when there are problems, i.e. data looks weird.

- try reset datapath, reset asics and reset counters at top of scifi page
- under Programs restart "SW Fibre"

SciTiles

Documented on the tile custom page: [cmd=custom&page=tile/Tile-Instructions](#)

MuSe

Status

Transition

ODB

Messages

Chat

Log

Alarms

Programs

Buffers

History

MSCB

Sequencer

Event Dump

Config

Help

MuSeDisplay

Services

DAQ

Pixel

SciFi

Title

DQM

Param

ALARM: What to do?

OperationManual

RunDB

DAQ help

RunDBEdit

Magnet T

18.06.55.539 2025/06/26 [mhtpd1.INFO] Alarm "Helium pES HeBottle" reset

Alarms: No

Normal Shift Operation

For details on these steps please refer to the sections below.

1. Check LV and HV are powered on in the Tile DS Hardware/Power page, if not follow the power up procedure.

2. Check the slow control information on the Tile DS Hardware/Power page, especially the temperatures.

3. Once a beam run is started, check the htrate map for any anomalies. For the expected behavior and also what to do if something is off, refer to the sections below.

4. Check the DQM pages for any discrepancies or issues.

5. If any issues are encountered, and could be resolved using the explanations provided below. Document them, including run numbers, in the ELOG.

6. If issues persist, please contact a Tile expert.

Tile DS Power requirements

Devices/Channels which need to be turned on

• TDK: SCI DS

• PowerDist: DS WEATHER TOP and BOTTOM, DS DCDC C2 and C3

• DCDC: Cables: C6 and C7

• FEBs: FEB96, FEB101, FEB102

Tile DS Hardware/Power Page

• This Page is for Powering up and Monitoring the Slow Control of the Tile DS station

• For all communication on this page, switch_le_downstream (SW Downstream in Programs) is required to run.

Color Legend

• Gray: Disabled/Not important

• Green: Turned on/in a good state

• Yellow: Too low or too high value, yet not critical

• Red: Value too high, please look at the respective description and contact an expert

Power Up Procedure

1. To provide Power to the Modules through the PowerDist/FEBs and DCDCcates, Look at Module Power Power Tile DS

2. Check for a current of roughly 0.7 to 1.0 A in the Module Power TMB Power (LV)

3. To establish communication to the tile modules, start by clicking the Init Checkbox and wait until the lamps below turn green

4. To power ASICs, check the boxes of the modules you wanna power in the "MuTRIG Power" column and then click on "Power all selected"

5. You should see the MuTRIG current jump to 3-4 A.

6. To power on the HV (important for IV Scans, Threshold Tuning, and data taking), set the Voltage in Module Power to 56V and click "Set HV", then "Power Tile HV"

7. After Powering, check if the Voltages in the High Voltage Monitoring show the desired Voltage. A voltage slightly lower is usually fine/expected.

Power Down Procedure

The Power Down Procedure is the same as the Power Up Procedure, just in reverse order:

1. Power of the HV by pressing the "Switch off Tile HV" button.

2. Check the Voltages in the High Voltage Monitoring, they should be 0V.

3. Click the Init Checkbox in the Module Control Table.

4. The MuTRIG Power consumption should jump close to 0A.

5. Power off LV completely by pressing the "Switch off Tile DS" button.

Configuring the MuTRIGs

1. To configure the MuTRIGs, using the current ODB settings, press the "Configure all selected MuTRIGs" button in the Module Control Table.

2. After doing that you should see the MuTRIG current jump to 8-10 A.

3. Often you will see the counters running wild after configuring. What typically helps is to reset them with the "Reset DPath" button

Tile Operation Scripts Page

• This Page is used for loading operational settings for different scenarios and to perform scans of the Tile Detector, such as IV, TTH and ETH.

IV Scan

Operating systems

Vertex

While running check:

- The 3 dots on top left bars are green:

Watchdog Status (Safety Software Interlock)

Required Programs for Operation

Service Status

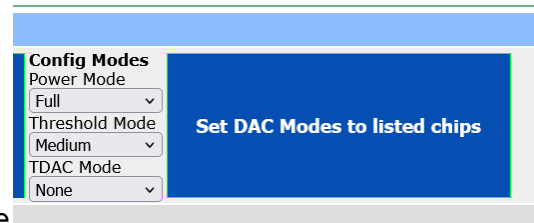
- If Service Status is red: click on bar, it will open. Then you see a breakdown of the issues:
 - FEB Power not OK (correlated with LVDS links bad)
 - Stop SW Frontend
 - Start SW Frontend
 - Wait
- If Watchdog is red
 - If for a short time and nothing happens, OK
 - If it is really bad, it will turn off some PPs
 - The PP LV box will show gray LV and HV
 - Check on messages->Vertex for a red watchdog message
 - Sometimes the issue goes away and you do not see it in the watchdog anymore (the dot gets green)
 - If it is there (the dot is red), click on the bar and check which issue is red

```

11:10:40.849 2025/06/04 [Pixel watchdog.INFO] Untriggered for power partition VTX_DS_L2_M1 on condition Min_VDD_voltage
11:10:40.941 2025/06/04 [Pixel watchdog.INFO] [Pixel watchdog.active-response-on:6] [Pixel watchdog.INFO] Turning off power partition VTX_DS_L2_M1 because of condition Min_VDD_voltage (LV channel 69 and M0 channel 128)
11:10:35.720 2025/06/04 [Pixel watchdog.INFO] Triggered for power partition VTX_DS_L2_M1 on condition Min_VDD_voltage
11:10:23.650 2025/06/04 [shhtpd.INFO] Run 000 stopped

```

- Check Power and LVDS boxes
 - Power all green
 - If LV Blue: underpower
 - Click
 - LVDS working links green
 - If not: open the power partition and spot the bad chips (with orange/red dots)
 - Click on them
 - Go to the Manual config tab
 - Click configure
 - [Ask expert for this, if this is the case we have to update TDACs]



Check config mode

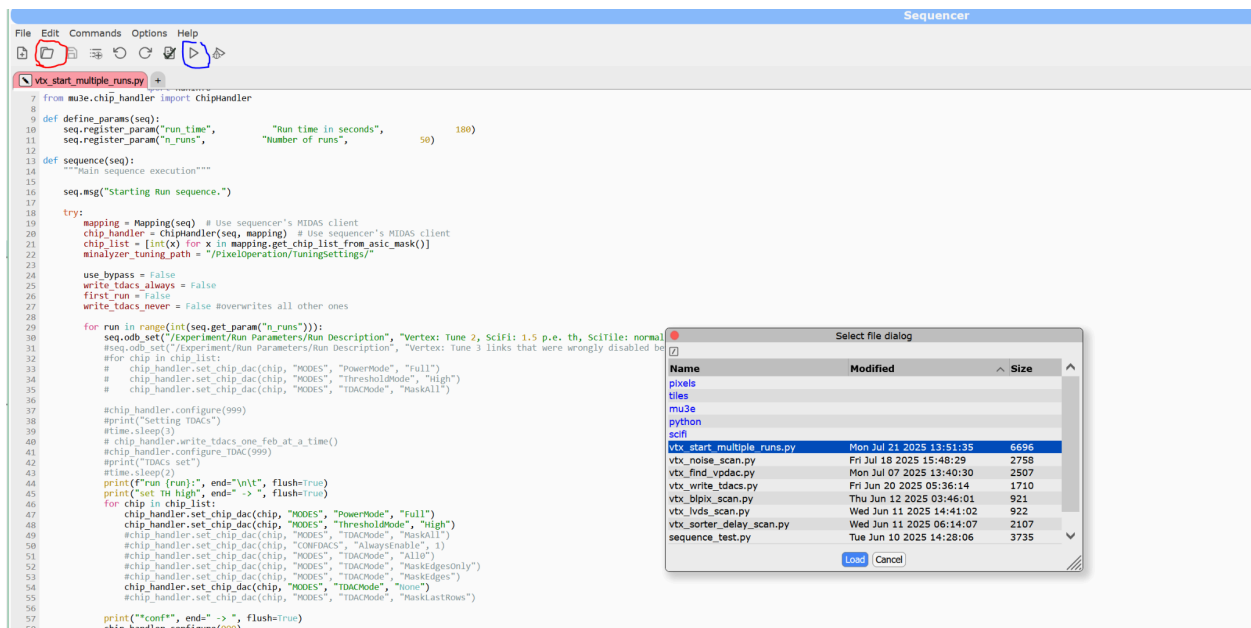
- Set Power to full
- Set Threshold to Medium
- Set TDAC to None
- Click Set DACs Modes to listed chips
- Click configure
- If not good, click reset PLL
- If not good, click reset Biasblocks
- Very worst:
 - Click Configure Full->Min
 - Click Configure Min->SC
 - Click Configure SC->Full
- If nothing works call experts

Data taking

Starting runs

We use the pySequencer for now

Open the pySequencer page, load “vtx_start_multiple_runs.py” (highlighted in red below) and start (highlighted in blue below)



The page will ask you for the total number of runs and their length. Typical value of a length is 180 (3 min) or 300 (5 min). The total number depends on how long you wish to take data for in total.

Stopping runs

To stop the pySequencer and all runs

- Go to the pySequencer page and press stop
 - The button highlighted in blue above becomes a square while the pySequencer is running
- Go to the Sequencer page and press stop
 - There is a similar button
- Stop the run from the State or Transition page

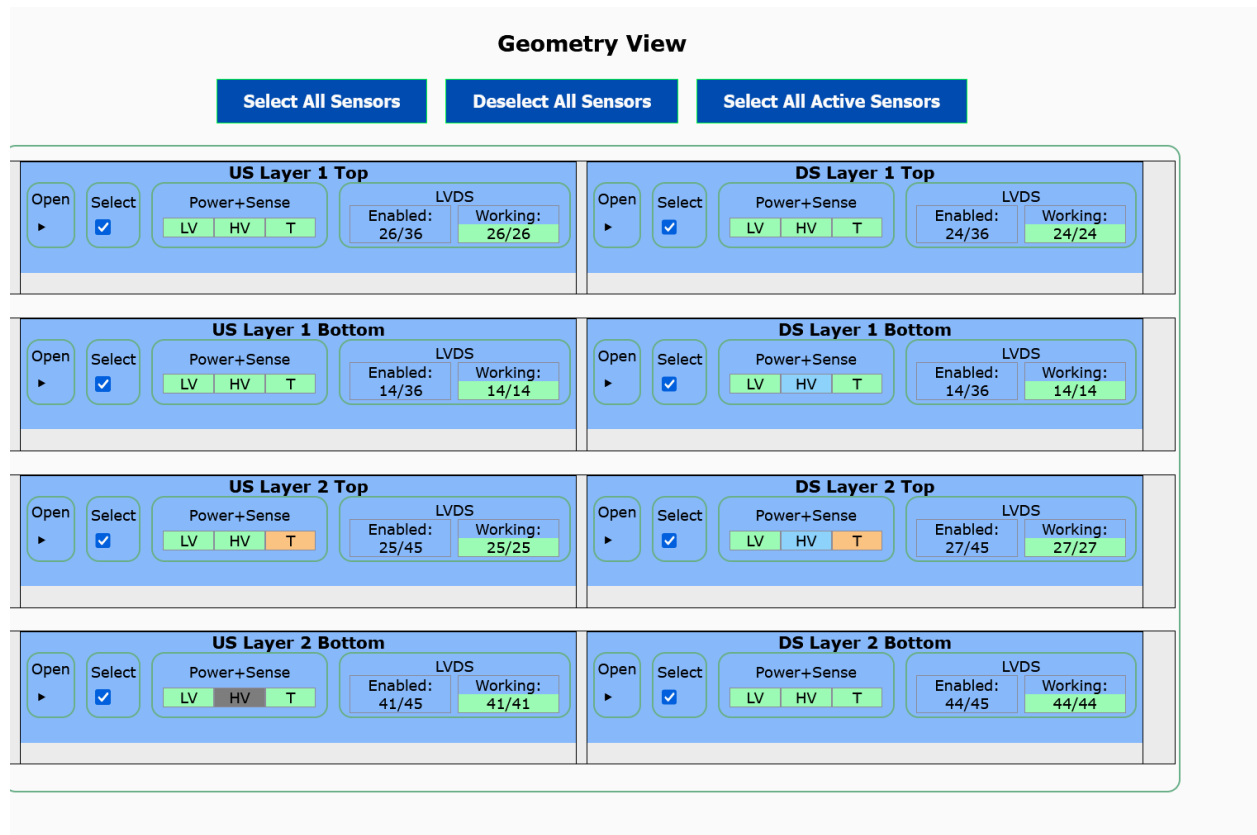
Before run

Checks for DAQ

Generic check?

Checks for Vertex

Check LVDS link stability: the geometry view should look like this:



Checks for Scifi

Add

Checks for SciTiles

Add

During run

Checks for DAQ

DQM? History?

Checks for Vertex

During cosmic run, just check for link stability same as above

Checks for Scifi

Add

Checks for SciTiles

Add

After run

Checks for DAQ

Generic check?

Checks for Vertex

Data quality checks?

Checks for Scifi

Add

Checks for SciTiles

Add

Turn off systems (only when necessary)

3 subsystems in parallel. They can be turned on all or just some. Must be clarified for each shift.

Vertex

Power down

We do it one PP at a time for now

- In the geometry view on the top right, open the power partition
- Select all chips with at least one green dot
 - Or yellow/orange/red if the link is not running
 - It should not be gray (disabled)
- In the Manual Config tab, click “Configure Full-> Min”

Turn off

From the Pixel/Pixel Control page, in the power tab, select the power partitions you want to turn off and click “Turn Off Selected PPs”:

SciFi

- In the Scifi-Monitor custom page, under 5. HV, turn off the HV (set the dropdown selector to “no”, you can leave the set value at 56 V)
- Under 4. LV set the dropdown selectors to “no”

3.	Turn on FEBs / SWB ▼					
<input type="checkbox"/> FEBs on?	<input type="checkbox"/>	Crate1 on/off	Crate5 on/off	FEB145 on/off	FEB147 on/off	SW Fibre on/off
<input type="checkbox"/> SWB on?		<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

- Under 3. LV set the dropdown selectors to “no”
- Under 3. uncheck the two boxes on the right to turn off the FEBs

SciTiles

Add here

Extra notes

One day Vertex had to do this: US Layer 1 Ladder 2:

- Ladder 3 Min to SC
- Ladder 1+2 Min to SC
- Ladder 3 SC to Full
- Ladder 1+2 SC to Full

Still the case?

👁️ Staging setup

Staging setup Operation Manual

Add here relevant notes when you find out peculiarities in how things work

Turn off everything before touching anything on cage!!

Form

A compact set of instructions as a Google form can be found [here](#). This should be filled every time the Vertex operation is started.

Visual Check Before Turning Things On

To ensure it is truly safe before powering the detector, operators need to make a quick round to make sure that:

- No personnel is doing maintenance/intervention near the detector that poses a risk to themselves if power is applied
- Visually, the helium intake/outtake connections are connected
- Chillers are running and at nominal temperature (~15C)
- Nothing looks off around the magnet & helium area
- If TDKs are at Fault Mode, then reset the PLC interlock

Turn on Helium

In the notes:

https://www.physi.uni-heidelberg.de/Forschung/he/mu3e/restricted/notes/Mu3e-Note-0107-Helium_Staging_Setup_Manual.pdf

Check programs and alarms

When nothing is on, the programs page should look like this:

Programs				
Program	Running on host	Alarm class	Autorestart	Commands
mhttpd	mu3ebe	-	No	
Logger	mu3ebe	Program Alarm	No	Stop Logger
Febcrate Frontend	Not running	-	No	Start Febcrate Frontend
CR Frontend	mu3ebe	Program Alarm	No	Stop CR Frontend
Powerrack Frontend	mu3ebe	-	No	Stop Powerrack Frontend
DCDC Frontend	Not running	-	No	Start DCDC Frontend
mserver	mu3ebe	-	No	Stop mserver
SW Central	Not running	-	No	Start SW Central
Sequencer	mu3ebe	-	No	Stop Sequencer
ana	mu3ebe	-	No	Stop ana
HV Frontend	Not running	-	No	Start HV Frontend
ROSWBCentral	Not running	-	No	Start ROSWBCentral
msysmon	Not running	-	No	Start msysmon
Liquid cooling Frontend	mu3ebe	-	No	Stop Liquid cooling Frontend
SW Fibre	Not running	-	No	Start SW Fibre
Environment Frontend	Not running	-	No	Start Environment Frontend
dhcpd_fe	mu3egw.mu3e	-	No	Stop dhcpd_fe
Pixel Watchdog	mu3ebe	Program Alarm	No	Stop Pixel Watchdog
Helium Plant piE5 Frontend	Not running	-	No	Start Helium Plant piE5 Frontend
Lazy_merlin	Not running	-	No	Start Lazy_merlin
Helium Staging Frontend	mu3ebe	Alarm	No	Stop Helium Staging Frontend

The programs in green in the picture above should be running all the time independently on the current state of services/detectors (especially those with Alarm conditions).

Note: “Helium Staging Frontend” is valid for the staging setup, it should be moved to “Helium Plant piE5 Frontend” in piE5.

Also, from the Alarm page, check that no Program Alarm is disabled and that all states are OK. If a state is OK but the alarm is ringing, reset the alarm.

Turn on services

Before turning on the FEC TDK, make sure the electronics chiller is on in the area by going to the physical chiller

- If the light is off, Click “Run/Stop”
- If the light is on, it means it is already running (check with others if it was meant to be so)

Make sure the Powerrack frontend is running

Note: sometimes it crashes or gets stopped. You should realise it as some select boxes are set to no but its color box is still green (or vice versa). Restart the frontend in this case.

Go to Services/Staging Power, it should look like this:

Power Control/Debug page for Non Experts

Note: Ensure the Helium system status is Healthy before powering on the Pixel TDKs. Also, check if the Electronics Chiller is running before powering on the FECrates TDKs.

Power Controls:

Helium Status

System healthy ■

Shortcuts

Turn On US

Turn On DS

Turn Off US

Turn Off DS

TDK Power Control

Name	Output on	Power good
Pixel US	no	■
Pixel DS	no	■
FE Crates US	no	■
FE Crates DS	no	■

DCDC Crates Power Control

Name	Crate Input	Power good
US WEATHER TOP	no	■
US DCDC C1	no	■
US DCDC C2	no	■
US WEATHER BOTTOM	no	■
US DCDC C3	no	■
US DCDC C4	no	■
DS WEATHER TOP	no	■
DS DCDC C5	no	■
DS DCDC C6	no	■
DS WEATHER BOTTOM	no	■
DS DCDC C7	no	■
DS DCDC C8	no	■

HV Crates Power Control

Name	Crate Input	Power good
US HV TOP	no	■
US HV BOTTOM	no	■
DS HV TOP	no	■
DS HV BOTTOM	no	■

FEC Crates Power Control

Name	Output on	Power good
US FEC1 CH1	no	■
US FEC1 CH2	no	■
US FEC2 CH1	no	■
US FEC2 CH2	no	■
DS FEC3 CH1	no	■
DS FEC3 CH2	no	■
DS FEC5 CH1	no	■
DS FEC5 CH2	no	■
DS FEC6 CH1	no	■
DS FEC6 CH2	no	■

If the Helium Status is red something went wrong with turning on the Helium system. Contact experts.

Note: If the TDKs cannot be turned on because of an intervening interlock and if the He interlock had triggered before (e.g. because of having turned off the He), the toggle the two switches for resetting the interlock.

Click turn on US. This is then how the page should look like:

Power Control/Debug page for Non Experts

Note: Ensure the Helium system status is Healthy before powering on the Pixel TDKs. Also, check if the Electronics Chiller is running before powering on the FECrates TDKs.

Power Controls:

Helium Status

System healthy ■

Shortcuts

Turn On US

Turn On DS

Turn Off US

Turn Off DS

TDK Power Control

Name	Output on	Power good
Pixel US	yes	■
Pixel DS	no	■
FE Crates US	yes	■
FE Crates DS	no	■

DCDC Crates Power Control

Name	Crate Input	Power good
US WEATHER TOP	yes	■
US DCDC C1	yes	■
US DCDC C2	yes	■
US WEATHER BOTTOM	yes	■
US DCDC C3	yes	■
US DCDC C4	yes	■
DS WEATHER TOP	no	■
DS DCDC C5	no	■
DS DCDC C6	no	■
DS WEATHER BOTTOM	no	■
DS DCDC C7	no	■
DS DCDC C8	no	■

HV Crates Power Control

Name	Crate Input	Power good
US HV TOP	yes	■
US HV BOTTOM	yes	■
DS HV TOP	yes	■
DS HV BOTTOM	yes	■

FEC Crates Power Control

Name	Output on	Power good
US FEC1 CH1	yes	■
US FEC1 CH2	no	■
US FEC2 CH1	yes	■
US FEC2 CH2	no	■
DS FEC3 CH1	no	■
DS FEC3 CH2	no	■
DS FEC5 CH1	no	■
DS FEC5 CH2	no	■
DS FEC6 CH1	no	■
DS FEC6 CH2	no	■

Known issue: the Pixel US has output on to yes but the box is red: likely Helium interlock. Put output on to no and reset the interlock switches (manually on the power rack, requires presence)

If not all expected boxes are green, press the button again

Notes:















- There are 2 channels per FEC but only one needs to be on (bug in the power distribution board, two connections recognized as short, only one required for Vertex operation)
 - For US can be 0 and 2 on the Power Distribution page
 - These are US FEC1 CH1 and US FEC2 CH1 in the page above

Pixel watchdog

Start Pixel watchdog from the programs page (it should be already on)

Note on watchdog operation:

- The watchdog checks a series of conditions which can be seen in the table in the Pixel Control page (when expanding its section)

Watchdog Status		
Condition	Status	Description
Max_LV_current		Turn off PP if LV current too high
Max_VDD_voltage		Turn off PP if VDD Mupix too high
Min_VDD_voltage		Turn off PP if VDD Mupix too low while running (wait 2 sec)
Max_HV_current		Turn off PP if HV current too high
Max_Temperature		Turn off PP if temperature too high
Warn_Temperature		Warn user if temperature high
Min_Temperature		Turn off PP if temperature too low
TDK		Turn off PP if TDK supply off while running
PwrDistPixel		Turn off PP if Pixel Pwr Dist not good while running
PwrDistCrates		Turn off PP if crate slow control off while running
He		Turn off detector if Helium system not healthy
He_mode		Turn off detector if air allowed in Helium but 2 sides enabled (expert mode)
Programs		Turn off detector if required safety programs not running
Programs_debug		Turn off detector if required safety programs not running. Debug = after 20 s

Click the table for more details

-
- When one of this condition is triggered, the box gets red and a message appears in the Message page under Vertex as an info
 - This does not necessarily means that the watchdog intervenes, as the power partition(s) might be already off or there might be a timer
- When the watchdog intervenes and turns off a power partition, a message appears in the same page as error (red), indicating which power partitions are turned off and the condition (i.e. reason)

DCDCCrates Frontend

- **[Optional: do not touch unless you know what you are doing]** If you want to run it with certain crates, and not the ones shown above
 - Stop DCDC Frontend before turning on/off crates
 - Make sure that the channels for those crates are on
 - Go to on Services/Staging Power and under the DCDC Crates Power Control turn on the crates you want
 - Turn on also the weather channels
 - these depends if the crates are US/DS and Top/Bottom
 - set disable to the non-used crates
 - In the ODB/Equipment/DCDCCrates/Settings/ set the values of "Enabled" and "Crate on" to true or false depending which crates you want to use
- Start DCDC Frontend on Programs page

HV frontend

- Start HV Frontend on programs page

Extra note: all HV crates must be turned on, as the Frontend does not allow to disable crates (US Top, US bottom, DS top, DS bottom)

Note 2: as for now, it might trigger the pixel watchdog on a DS power partition. No problem for now **(to be fixed before we connect DS)**

Turning on FEBs (US only so far)

- Start FebCrate Frontend
- Go to the DAQ/Feb Crates page from left panel
- The Crates you turned on should be green
 - If they are yellow, try to refresh the page or restart FebCrate Frontend
- The slots with a FEB number underneath have the FEB connected, you can turn them on by clicking on the 'OFF' button (yes, it's a button)
 - Turn on all US (Feb 0 to Feb 4)
 - You can tell if the buttons are working as you should see messages about turning on FEBs
 - If you don't see the messages, reload the page or restart FebCrate Frontend
- It should look like this at the end:

Crate 1 MSCB392.psi.ch 1001 20V 18.78 V 3.3V 3.29 V 5V 5.00 V T 24 C		Slot 0 0°C OFF	Slot 1 0°C OFF	Slot 2 0°C OFF	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 FEB 4 0°C ON	Slot 14 FEB 0 0°C ON	Normal Image
Crate 2 MSCB392.psi.ch 1002 20V 18.43 V 3.3V 3.30 V 5V 4.95 V T 22 C		Slot 0 FEB 1 0°C ON	Slot 1 FEB 2 0°C ON	Slot 2 FEB 3 0°C ON	Slot 3 0°C OFF	Slot 4 0°C OFF	Slot 5 0°C OFF	Slot 6 0°C OFF	Slot 7 0°C OFF	Slot 8 0°C OFF	Slot 9 0°C OFF	Slot 10 0°C OFF	Slot 11 0°C OFF	Slot 12 0°C OFF	Slot 13 0°C OFF	Slot 14 0°C OFF	Normal Image

- And you should see these messages in the Messages page:

```
[Febcrate Frontend,INFO] Switching on FEB 2 in crate 1 (index 17)
[Febcrate Frontend,INFO] Switching on FEB 1 in crate 1 (index 16)
[Febcrate Frontend,INFO] Switching on FEB 0 in crate 1 (index 15)
[Febcrate Frontend,INFO] Switching on FEB 14 in crate 0 (index 14)
[Febcrate Frontend,INFO] Switching on FEB 13 in crate 0 (index 13)
```

- → **Wait one minute** until all links are stabilized!!!

- **[this point should be already done]** In ODB /Equipment/LinksCentral/Settings/ Make sure:
 - LinkMask have 3 **on the FEBs that are on**

- For US only these are FEBs 0-4
- For LinkFEB have the values same as the index (for the FEBs that are on?)
- FEBType = 1 for pixel (FEBs 0-9)
- LVDSLinkMask to
 - 0x7FFFFFFF for FEBs 2,4,7,9
 - 0xF FFFF FFFF for the others
- LVDSNumLinksMax
 - 27 for FEBs 2,4,7,9
 - 36 for the others
- NumASICsMax to
 - 9 for FEBs 2,4,7,9
 - 12 for the others
- Start SW Central from the Programs page
 - Wait for receiving the message “Done” in the “switch” message section
 - Make sure there are no errors in the Message page regarding FEB connections
 - To cross-check everything worked fine, check the page DAQ/FEBs, it should look like this:

Central	Recurt US	Recurt DS	Fibres
0 0/14	48 0/14	96 0/14	144 0/14
1 0/14	49 0/14	97 0/14	145 0/14
2 1/1	50 0/14	98 0/14	146 0/14
3 0/14	51 0/14	99 0/14	147 0/14
4 0/13	52 0/14	100 0/14	148 0/14
5 5/0	53 0/14	101 0/14	149 0/14
6 4/14	54 0/14	102 0/14	150 0/14
7 4/13	55 0/14	103 0/14	151 0/14
8 4/12	56 0/14	104 0/14	152 0/14
9 5/1	57 0/14	105 0/14	153 0/14
10 -1/-1	58 0/14	106 0/14	154 0/14
11 -1/-1	59 0/14	107 0/14	155 0/14
12 -1/-1	60 0/14	108 0/14	
13 -1/-1	61 0/14	109 0/14	
14 -1/-1	62 0/14	110 0/14	
15 -1/-1	63 0/14	111 0/14	
16 -1/-1	64 0/14	112 0/14	
17 -1/-1	65 0/14	113 0/14	
18 -1/-1	66 0/14	114 0/14	
19 -1/-1	67 0/14	115 0/14	
20 -1/-1	68 0/14	116 0/14	
21 -1/-1	69 0/14	117 0/14	
22 -1/-1	70 0/14	118 0/14	
23 -1/-1	71 0/14	119 0/14	
24 -1/-1	72 0/14	120 0/14	
25 -1/-1	73 0/14	121 0/14	
26 -1/-1	74 0/14	122 0/14	
27 -1/-1	75 0/14	123 0/14	
28 -1/-1	76 0/14	124 0/14	
29 -1/-1	77 0/14	125 0/14	
30	78	126	

0	Crate: 0 - Slot: 14	VertexPixel
L1 US 1-4		
Arria 39 C	1.1V 1.10 V	
Max 48 C	1.8V 1.80 V	
SI1 41 C	2.5V 1.71 V	
SI2 39 C	3.3V 3.34 V	
Arria ext 44 C	20V 18.62 V	
DCDC 44 C	FF 2.96 V	
Firefly 1 43 C		
T History	V History	
Arria firmware: 0x00000000	RX1 748 muW	
Max10 firmware: 0x00000000	RX2 13 muW	
	RX3 1075 muW	
	RX4 1200 muW	
	RX History	

- If one of the FEBs on the top left has red dots:
 - Stop SW Central
 - power-cycle the FEB(s) from the DAQ/FEB crates page as above
 - Wait a bit
 - Start SW Central

Known issues:

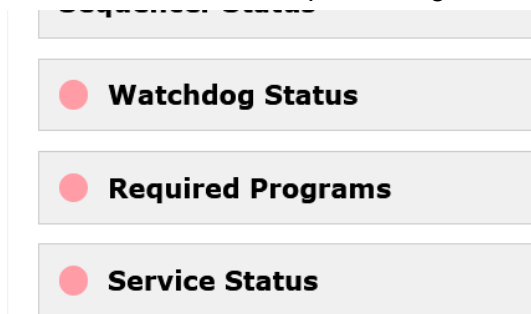
- You receive a message like this:

```
11:03:07.673 2025/03/24 [SW Central,ERROR] [switch_fe.cpp:222:frontend_init(),ERROR] No switching board present or driver not loaded
11:03:07.665 2025/03/24 [SW Central,ERROR] [switch_fe.cpp:632:init_mudaq(),ERROR] Could not open device
```

- Go to switch0 ~/online/common/kerneldriver and run `sudo ./load_mudaq.sh`
- If a FEB malfunctions and causes all chips to lose configuration, avoid power-cycling the FEBs with the Vertex detector still on. Power down vertex then power-cycle the FEBs.

Pixel Control via Page

- Go to Pixel/Pixel Control
 - At the moment this has been fully tested only with firefox, it seems to work on chrome, not on chromium
 - Not compactified yet, adjust the zoom in case the elements are overlapping
 - ~50% on laptop
 - ~60% on large screen
- Check that the three dots on top left are green:



- Click on the bar to expand the details
- Known issues:
 - If Service Status is red, it is generally the case that crates are off: there is a

Quick Actions

Set Crates on

button on the right to set them on:
does not work)

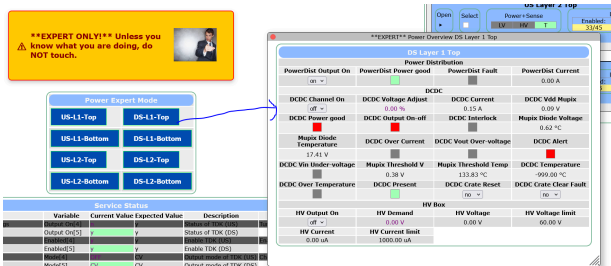
(click it more times if it

- If the watchdog is red, expand the details and check
 - Known case: if running only US and for some reason the watchdog restarted, the DS power partitions have to be disabled:
 - Go to the expert tab
 - Scroll to the end

- Disable the DS power partition from the table in the bottom center:

Watchdog Enabled Power Partitions	
Power Partition	Enabled
US Layer 1 Top	Enabled ▾
US Layer 1 Bottom	Enabled ▾
US Layer 2 Top	Enabled ▾
US Layer 2 Bottom	Enabled ▾
DS Layer 1 Top	Disabled ▾
DS Layer 1 Bottom	Disabled ▾
DS Layer 2 Top	Disabled ▾
DS Layer 2 Bottom	Disabled ▾

- If on the power tab you see some NaN values
 - try to stop and restart DCDC Frontend
 - Maybe multiple times
- If an interlock is red on the power tab
 - Go to the expert tab
 - Click on the power partition tab
 - Try to turn “DCDC Crate Reset” and “DCDC Crate Clear Fault” multiple times



- Turn on a power partition:
 - In the power tab, select the power partition(s) you want to turn on and click “Turn On Selected PPs”

Expert

Power

Manual Config

Calibration

Tuning

Slow Control

Power Control

Turn On Selected PPs

Turn Off Selected PPs

Adjust Vdd for Selected PPs

Power Status

Power Partition Sel.	Resistivity	Input ch.	on Pwr	Good	Adjust	LV curr	HV on	HV volt	HV curr	VDD sense	VDD target	Temp sense	Temp.	Intlk
US-L1-Top	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	-9.00 % 0.000	<div></div>	<div></div>	0.000 0.000	0.001	1.850	18.905	<div></div>	<div></div>	<div></div>
US-L1-Btm	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	-9.00 % 0.025	<div></div>	<div></div>	0.000 0.000	0.001	1.850	15.423	<div></div>	<div></div>	<div></div>
US-L2-Top	<input type="checkbox"/> 380	<div></div>	<div></div>	<div></div>	-9.00 % 0.000	<div></div>	<div></div>	0.000 0.000	0.000	1.850	17.910	<div></div>	<div></div>	<div></div>
US-L2-Btm	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	-9.00 % 0.025	<div></div>	<div></div>	0.000 0.000	0.000	1.850	18.905	<div></div>	<div></div>	<div></div>
DS-L1-Top	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	0.00 % 0.100	<div></div>	<div></div>	0.000 0.000	0.001	1.900	0.000	<div></div>	<div></div>	<div></div>
DS-L1-Btm	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	0.00 % 0.000	<div></div>	<div></div>	0.000 0.000	0.000	1.900	0.000	<div></div>	<div></div>	<div></div>
DS-L2-Top	<input type="checkbox"/> 380	<div></div>	<div></div>	<div></div>	0.00 % 0.050	<div></div>	<div></div>	0.000 0.000	0.000	1.900	0.000	<div></div>	<div></div>	<div></div>
DS-L2-Btm	<input type="checkbox"/> 80	<div></div>	<div></div>	<div></div>	0.00 % 0.000	<div></div>	<div></div>	0.000 0.010	0.000	1.900	0.000	<div></div>	<div></div>	<div></div>

-
- The buttons will turn grey, indicating that the sequencer script is running

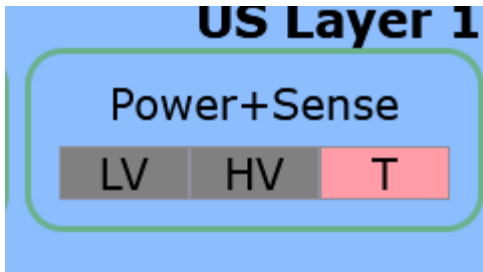
- Do not worry about messages from the Watchdog saying “Triggered” and then “Untriggered”
 - Watchdog is faster than DCDC read-back, but in this case it won’t intervene before 2 seconds
- Wait until the buttons become blue (enabled) again
 - There should be a message “Sequencer is finished”
 - This might take a bit, still doing things slow
- This is how it should look like at the end:

Power Partition	Sel. Resistivity	Input ch. on	Pwr Good	Adjust	LV curr	HV on	HV volt	HV curr	VDD sense	VDD target	Temp sense	Temp. Intlk
US-L1-Top	80	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	-9.00 %	1.375	<input checked="" type="checkbox"/>	50.000	0.760	1.653	1.850	29.851	<input checked="" type="checkbox"/>
US-L1-Btm	80	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	-8.00 %	1.575	<input checked="" type="checkbox"/>	50.000	1.090	1.768	1.850	38.806	<input checked="" type="checkbox"/>
US-L2-Top	380	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	-9.00 %	1.550	<input checked="" type="checkbox"/>	10.000	31.280	1.777	1.850	36.318	<input checked="" type="checkbox"/>
US-L2-Btm	80	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	-9.00 %	1.425	<input checked="" type="checkbox"/>	50.000	1.800	1.774	1.850	40.299	<input checked="" type="checkbox"/>
DS-L1-Top	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.100	<input type="checkbox"/>	0.000	0.000	0.001	1.900	0.000	<input type="checkbox"/>
DS-L1-Btm	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.000	<input type="checkbox"/>	0.000	0.000	0.000	1.900	0.000	<input type="checkbox"/>
DS-L2-Top	380	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.050	<input type="checkbox"/>	0.000	0.000	0.000	1.900	0.000	<input type="checkbox"/>
DS-L2-Btm	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.000	<input type="checkbox"/>	0.000	0.010	0.000	1.900	0.000	<input type="checkbox"/>

- By default the DCDC output is set to minimum. To reach the target VDD, select power partitions and click “Adjust Vdd for Selected PPs”
 - This aims to set the adjust such that the VDD sense (11th column) reaches the VDD target (12th column)
 - ~~This can be done one power partition at a time for now (currently a sequencer error will terminate the script, once it is finished with a power partition, don’t worry about it.)~~
 - It should look like this at the end:

Power Partition	Sel. Resistivity	Input ch. on	Pwr Good	Adjust	LV curr	HV on	HV volt	HV curr	VDD sense	VDD target	Temp sense	Temp. Intlk
US-L1-Top	80	<input type="checkbox"/>	<input checked="" type="checkbox"/>	3.00 %	2.050	<input checked="" type="checkbox"/>	50.000	0.930	1.859	1.850	38.308	<input checked="" type="checkbox"/>
US-L1-Btm	80	<input type="checkbox"/>	<input checked="" type="checkbox"/>	-4.50 %	1.850	<input checked="" type="checkbox"/>	50.000	1.260	1.855	1.850	42.786	<input checked="" type="checkbox"/>
US-L2-Top	380	<input type="checkbox"/>	<input checked="" type="checkbox"/>	-6.00 %	1.725	<input checked="" type="checkbox"/>	10.000	34.480	1.835	1.850	39.303	<input checked="" type="checkbox"/>
US-L2-Btm	80	<input type="checkbox"/>	<input checked="" type="checkbox"/>	-6.00 %	1.525	<input checked="" type="checkbox"/>	50.000	2.900	1.835	1.850	43.781	<input checked="" type="checkbox"/>
DS-L1-Top	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.100	<input type="checkbox"/>	0.000	0.000	0.001	1.900	0.000	<input type="checkbox"/>
DS-L1-Btm	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.000	<input type="checkbox"/>	0.000	0.000	0.000	1.900	0.000	<input type="checkbox"/>
DS-L2-Top	380	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.050	<input type="checkbox"/>	0.000	0.000	0.000	1.900	0.000	<input type="checkbox"/>
DS-L2-Btm	80	<input type="checkbox"/>	<input type="checkbox"/>	0.00 %	0.000	<input type="checkbox"/>	0.000	0.010	0.000	1.900	0.000	<input type="checkbox"/>

- To monitor power all time, click any “Power+Sense” box on the top right and you have a smaller and more user-friendly monitoring dialog (recommended)



- Click this:

Overview Power Partition US Layer 1 Top

US Layer 1 Top			
Input channel curr	LV curr	Input power	Power at sense
0.140	2.025	2.800	7.901
HV volt	HV curr	VDD sense	Temp sense
50.000	0.940	1.858	38.806
Temp. Intik			
<input type="checkbox"/>			

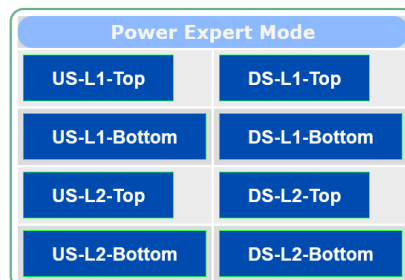
- To get this:

placed anywhere in the window

which can be

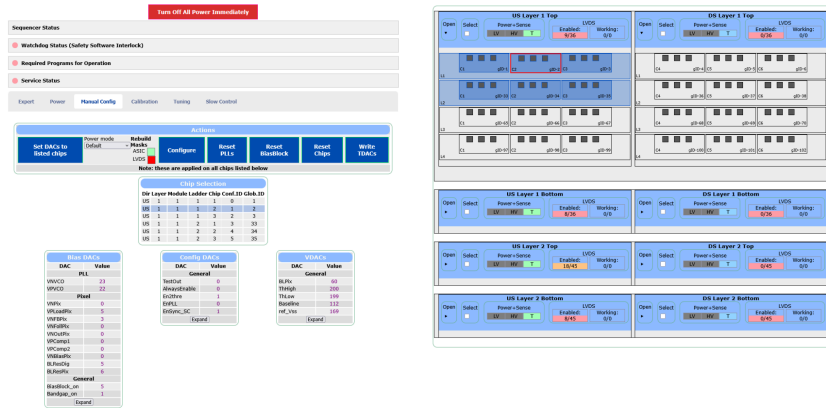
- **[CAUTION] Expert mode (to be used in case user mode does not work and/or something is off, call an expert before):**

- Go to the Expert tab, you should see the power partition buttons



-
- Click on the power partition you want to turn on, a pop-up window should open

- Whatever is blue is “listed” in the Manual Config page



- [Above: example where we selected ladder 1 and 2 from Layer 1 US]
- The list of selected chips appears in the table “Chip Selection”
- To manually change DACs (only one chip at a time for now)
 - Select the chip from the row on the “Chip Selection” page
 - The row becomes blue, while the chip on the right gets red borders (see pic above)
 - The DACs appear underneath the table in 3 more table, one for each DAC regime
 - The most relevant DACs appear first
 - Click expand to see them all
- **Note:** the DACs remain at the same values they were left the last time they were changed. If you think they were left in a usable/optimised state, you do not need to change them
- To change DACs to standard configurations
 - Click the button “Set DACs to listed chips” with the Power mode chosen on the select box on the right. Options:
 - Default: full power, very high threshold
 - Slow-control only: clock and LVDS, no line driver no comparator
 - Minimum power: all circuits off
- Operations on listed chips:
 - Configure: configure chips to the settings shown as above
 - Can be used if LVDS links are lost
 - Reset PLLs: disable and re-enable PLLs (clocking on chip)
 - Can be used if LVDS links are lost and configure did not help
 - Reset BiasBlock: turn off and on bias blocks on chips
 - Can be used if LVDS links are lost and configure and reset PLLs did not help
 - Reset chips: send a reset signal
 - To **all** chips, not only the listed ones [to be fixed?]
 - Write TDACs: write TDACs, i.e. single pixel masks and/or fine threshold
 - **CAREFUL:** Write TDACs does not work on multiple FEBs at the same time. Write TDACs one ladder at a time.

- The “Extra care” section is for debugging only
- More info about LVDS links can be found by clicking on the LVDS box on the top of each power partition in the Geometry View
- **Recommended procedure with 6 ladders US only** (mode 1 in the google form)
 - Select ONE ladder at a time and click Configure
 - Wait until all red boxes get green
 - Click Reset PLLs if not green
 - Click Reset BiasBlock if still not green
 - Click Write TDACs
 - Ladders used:
 - Layer 1 Ladder 2 (top side)
 - Layer 1 Ladder 7 (bottom side) .
 - Layer 2 Ladder 2 (top side)
 - Layer 2 Ladder 3 (top side)
 - Layer 2 Ladder 7 (bottom side)
 - Layer 2 Ladder 8 (bottom side)
 - If you want to change ladders: **make sure only 6 are running**
 - Select the ladder you want to turn off and click “Configure Full->Min”
 - The Buttons will get disabled and enabled multiple times
 - Do not leave the page
 - Wait until the buttons do not change anymore for few seconds
 - Disable the links for this ladder (they should not be working, i.e. lot of red)
 - From the LVDS dialog, “Active” should be yes, click it and it becomes no
 - If there were any links previously disabled, note them down in case
 - The row should become gray
 - Deselect the ladder
 - Select the next one
 - Enable the links for this ladder
 - From the LVDS dialog, “Active” should be no, click it and it becomes yes
 - The row should become colorful
 - Click “Rebuild ASIC and Link Masks” at the bottom of the dialog
 - Set the Power mode to “Minimum Power” and click “Configure”
 - Set the Power mode to “Slow-control only” and click “Configure”
 - Links should become green
 - Set the Power mode to “Default” and click “Configure”
 - If 1 or 2 links inside a chip are not running:
 - Verify with experts they are broken
 - Disable the links eventually
 - Check the Link Matrix (last row in the LVDS dialog table, yellow)
 - There are 3 per chip, they should be A,B and C
 - If one link shows ND, the inversion is wrong

- Inverted should be either yes or no, but it is wrong
 - Click it to change its value
 - Click “Rebuild ASIC and Link Masks” at the bottom of the dialog
 - You should see the Matrix type now
- Decrease thresholds if needed
- **To monitor data links:**
 - The 3 dots on each chip correspond to the link status
 - Gray: masked (bugfix: you have to refresh the page if you mask them externally)
 - Green: good
 - Orange: fairly high 8b10b error rate
 - Yellow: low 8b10b error rate
 - Green: zero 8b10b error rate
 - Click on the LVDS box on the top of the power partition to see more details
 - If some links gets lost (corresponding box gets red)
 - Click the entire chip
 - Click “Configure”
 - If it does not work, click “Reset PLLs”
 - If it does not work, click “Reset Biasblock”
- **Known issues:**
 - Some FEBs are down
 - You see error messages from switch FE
 - Turn off the Vertex
 - Stop SW Central and ROSWBCentral
 - Power cycle the misbehaving FEBs from the DAQ/FEB Crates page
 - Start SW Central and ROSWBCentral
 - Turn on Vertex
 - SW Central gets stuck
 - Keeps sending messages like this (in the switch bank):

```

17:14:21.617 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:14:21.685 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:14:21.799 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:14:21.799 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:14:21.799 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:14:21.792 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:14:24.423 2025/05/09 [SW Central:1400] Frontend board masking changed
17:14:24.581 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:14:24.575 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:14:24.569 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:14:24.561 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:14:24.557 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:14:47.379 2025/05/09 [SW Central:1400] Frontend board masking changed
17:14:47.368 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:14:47.437 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:14:47.432 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:14:47.431 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:14:47.430 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:14:47.426 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:14:48.061 2025/05/09 [SW Central:1400] Frontend board masking changed
17:14:48.054 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:14:48.063 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:14:48.060 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:14:48.060 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:14:48.059 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:14:48.058 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:15:32.711 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:15:32.699 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:15:32.697 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:15:32.697 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:15:32.695 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:15:32.675 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:15:44.142 2025/05/09 [SW Central:1400] Frontend board masking changed
17:15:44.140 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:15:44.197 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:15:44.192 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:15:44.191 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:15:44.179 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:15:44.173 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:15:38.076 2025/05/09 [SW Central:1400] Frontend board masking changed
17:15:38.069 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor
17:15:38.034 2025/05/09 [SW Central:1400] To Fiber 4 is mapped to link 6 "Central:1.2 US 8-10" --> 16x0.4
17:15:38.030 2025/05/09 [SW Central:1400] To Fiber 3 is mapped to link 3 "Central:1.2 US 4-7" --> 16x0.3
17:15:38.025 2025/05/09 [SW Central:1400] To Fiber 2 is mapped to link 2 "Central:1.2 US 1-3" --> 16x0.2
17:15:38.019 2025/05/09 [SW Central:1400] To Fiber 1 is mapped to link 1 "Central:1.1 US 5-8" --> 16x0.1
17:15:38.009 2025/05/09 [SW Central:1400] To Fiber 0 is mapped to link 0 "Central:1.1 US 1-4" --> 16x0.0
17:15:38.152 2025/05/09 [SW Central:1400] Frontend board masking changed
17:15:38.149 2025/05/09 [SW Central:1400] Found 3 FEBs, remapping 0 semiconductor

```

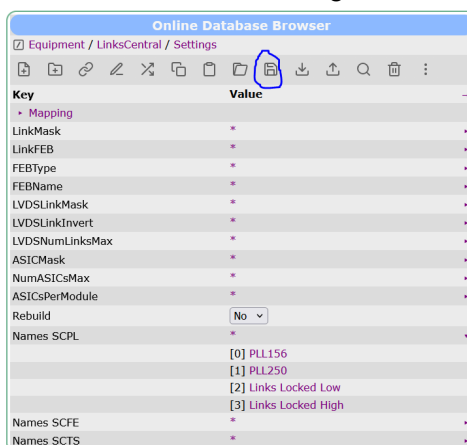
- Stop and start it
-

Tuning

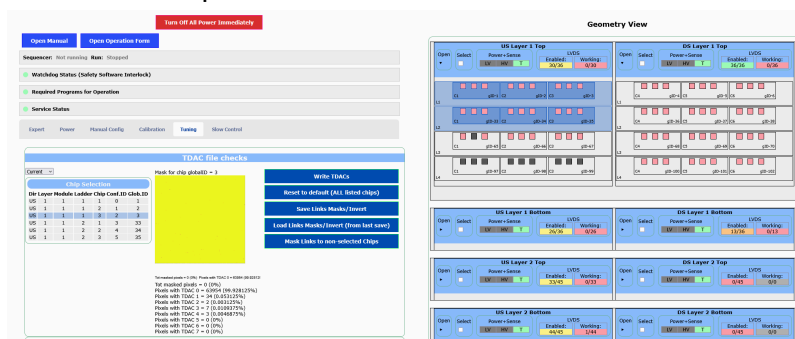
(temporary procedure, US only)

NOTE: in this procedure, if you are focusing on few ladders only, you can also turn on their power partition only

- To save the FULL LVDS links mask (the one used for operating the US detector)
 - This should be already done, no need to redo it, but just in case..
 - Go to ODB -> /Equipment/LinksCentral/Settings



- Click on this button:
 - Save ODB folder as VTX_link_settings.json (OK to overwrite it)
- To reload the FULL LVDS links mask (the one used for operating the US detector)
 - Same page as above but the button on the left
 - Same filename
 - This will trigger a long loop on SW Central (see known issue above)
 - Stop and Start it
- To inspect TDAC files:
 - Go to the Tuning tab
 - Select chips from the geometry view
 - Click on the chip in the list



- Bad mask if:
 - 100% of pixels have TDAC 0 (no hits)
 - Vertical and horizontal structures appear (8b10b errors picked up)
- To reset a mask file
 - On mu3ebe go to
/home/mu3e/online/online/userfiles/sequencer/mu3e/operation/cosmic_run_24

- python3 reset_tdac_file.py XXX
 - XXX is the chip global ID (the one on top of the mask)
- Power up one ladder
 - Select the ladder
 - Click “Configure Min→SC”
 - Click “Configure SC→Full”
 - Click Write TDACs
 - If some links are bad:
 - Reset PLL
 - Reset Biasblock
 - Re-power
 - Configure Full→Min
 - Configure Min→SC
 - Configure SC→Full
- Mask all other links
 - While selected, go to Tuning tab and click “Mask links to non-selected chips”
 - Click Configure
- For the first time, or when you redo some masking, make sure the DACs are OK
 - Put power mode to default
 - Click “Reset DACs to listed chips”
 - Put the threshold down to sensitive values
 - 125 ThHigh and 124 ThLow for 80 Ohccm
 - 140 ThHigh and 139 ThLow for 380 Ohmcm
 - Click Write TDACs
- Make sure you see some hits in the LVDS box
 - Otherwise lower the threshold
- Start one run
 - Go to tuning tab
 - Check tuning control:

The screenshot shows the 'Tuning Control' window. It contains two main sections: 'Minalyzer Parameters' and 'Script Parameters'. The 'Minalyzer Parameters' section has a header 'Change these for manual usage, otherwise the Script will take care'. It includes a table with parameters and their values: Algorithm (MaxHits), Max hits (20), N-sigma (5), Generate Mask (yes), Tune threshold (yes), Skip if errors (yes), Max Link Errors (10), and Link Errors Found n. The 'Script Parameters' section has a dropdown for Algorithm. To the right of these sections are two buttons: 'Start Tuning' and 'Start just one Run'.

Minalyzer Parameters		
Change these for manual usage, otherwise the Script will take care		
Algorithm	MaxHits	Algorithm to be used (Max number of hits or peak)
Max hits	20	Max number of hits (MaxHits algorithm)
N-sigma	5	Max sigma deviation (peak algorithm)
Generate Mask	yes	Generate mask at the end of the run
Tune threshold	yes	if yes increase threshold, if no mask
Skip if errors	yes	if yes, do not produce maskfile if errors detected
Max Link Errors	10	Max errors in case skip if errors is yes
Link Errors Found n		Output to see if the mask production has been skipped

Script Parameters

Algorithm

Start Tuning

Start just one Run

- - Algorithm: MaxHits
 - Generate Mask: yes
 - Skip if errors: yes
 - Max Link errors: 10 (can be modified in case)
- Select algorithm brute force (the only one)

Tuning Control			
Minalyzer Parameters		Script Parameters	
Change these for manual usage, otherwise the Script will take care			
Algorithm	MaxHits	Algorithm to be used (Max number of hits or peak)	
Max hits	20	Max number of hits (MaxHits algorithm)	
N-sigma	5	Max sigma deviation (peak algorithm)	
Generate Mask	yes	Generate mask at the end of the run	
Tune threshold	yes	if yes increase threshold, if no mask	
Skip if errors	yes	if yes, do not produce maskfile if errors detected	
Max Link Errors	10	Max errors in case skip if errors is yes	
Link Errors Found	n	Output to see if the mask production has been skipped	
Algorithm	brute_force		
start_threshold	140		
stop_threshold	120		
step_threshold_1	1		
step_threshold_2	1		
transition_threshold_1	125		
wait_time	20		
max_hits	20		
tune	y		
start_VPDAC	20		

-
- Wait_time and Max hits can be tuned, but let's keep both at 20 for now
- Click "Start just one run"
 - Check than link errors found is n at the end of the run
 - If y, reset PLL or something, then retry
- Write TDACs
- Move thresholds down
 - From Manual configuration tab
 - Make sure ThLow is ThHigh -1

Collect data

Before starting roswb_fe go to /Equipment/ROSWBCentral/Settings and

- mask_n_generic masks the FEBs bit-wise (e.g. mask = 8 =0b1000 unmask the 4th FEB)

Make sure "ROSWBCentral" and "ana" are running.

Go to the status page and start a run.

Monitor via the DQM page

Turn off Vertex

With this procedure, only the Vertex detector itself is off. For the moment, to be used when you leave the detector unsupervised for a bit, but you do not want to turn off all services (typical case: lunch break)

- In the power tab, select the power partition(s) you want to turn on and click "Turn Off Selected PPs"
- Wait until the buttons become blue (enabled) again
 - There should be a message "Sequencer is finished"
 - This might take a bit, still doing things slow

Turn off Services

THE VERTEX MUST BE OFF BEFORE THIS

This turns off all services (FEBs, crates, power distribution,...). For the moment, this should be done at the end of the day. In the long run, services should be able to run overnight.

- Stop SW Central and ROSWBCentral
- Turn off FEBs on the DAQ/Feb Crates page
- Stop FebCrate Frontend, DCDCCFrontend, HV Frontend

- In any order
 - Wait until the status is red on each one at a time
- Turn off all channels on the Services/Staging Power page
 - Click Turn off US until all boxes are red
- Turn off Helium system (**how? -> note107 linked below**)
- Turn off chillers (press Stop on the chillers themselves)

Turn off Helium

In the notes:

https://www.physi.uni-heidelberg.de/Forschung/he/mu3e/restricted/notes/Mu3e-Note-0107-Helium_Staging_Setup_Manual.pdf

Update microcontrollers on DCDC crates

- The code and output is in backend in
/home/mu3e/midas/mscb/embedded/projects/mu3e/PDCC
- Run
 - 'msc -d mscbXXX.psi.ch'
 - with XXX the mscb number
 - see ODB under /Equipment/DCDCCrates/Settings/MSCB Device, compare the right index
- Run
 - 'addr Y'
 - with Y the mscb address
 - See ODB under /Equipment/DCDCCrates/Settings/MSCB Address compare the right index
- Run
 - 'upload "/home/mu3e/midas/mscb/embedded/projects/mu3e/PDCC/pdcc.hex"'

Similar with HV controllers

Known issues

Switch0 freezes

SW Central and ROSWBCentral do not respond anymore, can't ssh into mu3esw0

Reset Switch0 physically from the hut downstairs

Run start fails

Messages				
19:17:16.229	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1487:Run Start, ERROR]	Run number mismatch on Feb 4, aborting run start. Run Number on FEB:22011 should be 1112
19:17:16.227	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1483:Run Start, ERROR]	Feb 4 has not received the run start signal or was not in IDLE when receiving it, aborting run start
19:17:16.225	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1479:Run Start, ERROR]	Feb 4 has denied a run start due to 0x000055fb, aborting run start
19:17:16.224	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1487:Run Start, ERROR]	Run number mismatch on Feb 3, aborting run start. Run Number on FEB:22011 should be 1112
19:17:16.222	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1483:Run Start, ERROR]	Feb 3 has not received the run start signal or was not in IDLE when receiving it, aborting run start
19:17:16.220	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1479:Run Start, ERROR]	Feb 3 has denied a run start due to 0x000055fb, aborting run start
19:17:16.219	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1487:Run Start, ERROR]	Run number mismatch on Feb 2, aborting run start. Run Number on FEB:22011 should be 1112
19:17:16.217	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1483:Run Start, ERROR]	Feb 2 has not received the run start signal or was not in IDLE when receiving it, aborting run start
19:17:16.215	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1479:Run Start, ERROR]	Feb 2 has denied a run start due to 0x000055fb, aborting run start
19:17:16.214	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1487:Run Start, ERROR]	Run number mismatch on Feb 1, aborting run start. Run Number on FEB:22011 should be 1112
19:17:16.212	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1483:Run Start, ERROR]	Feb 1 has not received the run start signal or was not in IDLE when receiving it, aborting run start
19:17:16.210	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1479:Run Start, ERROR]	Feb 1 has denied a run start due to 0x000055fb, aborting run start
19:17:16.209	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1487:Run Start, ERROR]	Run number mismatch on Feb 0, aborting run start. Run Number on FEB:22011 should be 1112
19:17:16.207	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1483:Run Start, ERROR]	Feb 0 has not received the run start signal or was not in IDLE when receiving it, aborting run start
19:17:16.206	2025/05/09	[SW Central, ERROR]	[switch_fe.cpp:1479:Run Start, ERROR]	Feb 0 has denied a run start due to 0x000055fb, aborting run start

In addition to re-powering FEBs and restarting the clock and reset FE, If your run fails to start and there is a "waiting for more switching boards" message from the clock/reset FE, go to ODB/Equipment/Clock reset/Settings/ and make sure the Switching Board Mask is only on for the switching boards you are using (0 for the vertex pixel)

Notes for PiE5 operation

Modes

We are now (week 21) in hybrid mode. Missing:

- DS operation
- He cooling
- FEC cooling
- DAQ

No shifts, everybody together running from ~9am till the end of the day.

We should go soon to cosmic mode, after fixing the points above. This means still debugging (HW, FW, SW, anything)

This should last one week...? Before beam anyway.

We should have a draft of shifts, called slots, but the shifter should know that data taking is not priority. The slots can be 2 per day, 8-14 and 14-20 or something similar.

Note: there will be beam tuning, so PiE5 might be closed.

Goal is to have a live understanding of the detector (with DQM).

Then we go to startup mode.

First beams, stuff will be confusing.

Then we go to Nominal data-taking mode.

For both startup and nominal we need shifts.

Awareness

Before doing any work on the system check what is happening. Make a checklist. Things like: is the magnet on? Is anyone working on the cage? Is anything on? Is anyone debugging a piece of code?

Shifters are in charge!

Alarms are being implemented. Do not ignore any alarm!

Documentation

Try and maintain the same scheme.

Shift guidelines

Shifters are in charge!

Priorities:

1. Check system
2. Check data
3. Do more stuff (debugging, analysis,...)