Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

LABORATORY COURSE FILE CONTENTS

Check list Course Outcomes Attainment

S. No.	Contents	Available (Y/N/NA)	Date of Submission	Signature of HOD
1.	Authenticated Syllabus Copy	Y		
2.	Individual Time Table	Y		
3.	Students' Name List (Approved Copy)	Y		
4.	Course Plan, PO, PSO, COs, CO-PO Mapping, COA Plan, Session Plan and Periodic Monitoring	Y		
5.	Rubrics for Assessment of Laboratory Experiments	Y		
6.	**Lab Manual / Lab Learning Materials** a) List of Experiments (Cycle I & Cycle II) b) Detailed Procedure for Experiments & Field Applications c) Viva-Voce Questions d) Smart Lab Experiments if any	NA		
7.	Dissemination of Syllabus and Course Plan to the Students	Y	31.08.2021	
8.	**Continuous Assessment** A. Laboratory Observation B. Laboratory Records C. Evaluation Sheet with Rubrics D. Slow Learners List and Remedial Measures			
9.	Course End Survey (Indirect Assessment) & Consolidation			
10.	**End Term Examination** A. Question Paper B. Sample Answer Scripts (Best, Average, Poor) if available C. Evaluation Sheet with Rubrics D. Slow Learners List and Remedial Measures.			
11.	Content Beyond the Syllabus (Proof)			
12.	Innovative Teaching Tools Used			
13.	Consolidated Mark Statement			

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

14.	CO Attainment (Continuous Assessment + End Term)			
15.	Gap Analysis & Remedial Measures			
16.	CO - PO Attainment			
17.	Class Record (Faculty Logbook)			

Signature of HOD/ Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course** : **Electric Circuits Lab** — **L: 0**
8. **Program** : **B.Tech** — **T: 0**
9. **Target** : **60%** — **P: 3**, **C: 2**

EEE42201	**Electric Circuits Lab**	L	T	P	C
Version 1.0		0	0	3	2
Pre-requisites/Exposure	Electric Circuits, Electrical and Electronics Technology				
Co-requisites	--				

Syllabus Copy

Course Objectives

1. To introduce with different circuit parameters, their behaviour and graphical representation that brings out with different circuit related theory and topology.
2. To introduce with different software related simulation methods.
3. To bring with idea how hardware and software combination and verification can be done.

Course Content

List of Experiments:

1. Verification of Kirchhoff's current law and voltage law using hardware and PSpice.

2. Verification of reciprocity theorem using hardware and PSpice.

3. Verification of Milliman's theorem using hardware and PSpice.

4. Verification of circuits containing dependent sources (VDVS, VDCS, CDCS, CDVS) using PSpice.

5. Determination of average value, rms value, form factor, peak factor of sinusoidal wave and square wave using hardware and verification using MATLAB.

6. Calculation and Verification of Z, Y, ABCD Parameters of a two-port network.

7. Verification of hybrid parameters of a two-port network using PSpice.

8. Verification of series resonance using hardware and MATLAB.

9. Verification of parallel resonance using hardware and MATLAB.

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra		**Course Code: EEE42201**
7. Course	**: Electric Circuits Lab**	**L: 0**
8. Program	**: B.Tech**	**T: 0**
9. Target	**: 60%**	**P: 3**
		C: 2

10. Observe transient response of first order R-L circuit using PSpice and determine time constant of the circuit.

11. Observe transient response of first order R-C circuit using PSpice and determine time constant of the circuit.

12. Observe transient response of second order R-L-C circuit using PSpice with (a) underdamped, (b) over-damped and (c) critically damped responses.

Text Books:

1. W.H. Hayt, J. E. Kemerlay& S.M. Durbin, "Engineering Circuit Analysis", Eighth Edition, McGraw Hill, 2012
2. Van Valkenburg, "Network Analysis", Prentice-Hall India, 2001.

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Faculty Individual Time Table

ADAMAS UNIVERSITY, KOLKATA
SCHOOL OF ENGINEERING AND TECHNOLOGY
DEPARTMENT OF ELECTRICAL ENGINEERING
Programme: B.Tech
Course Code & Course: EEE42201 & Electric Circuits Lab Faculty Coordinator: Dr Rituparna Mitra

Day & Time	10.30 - 11.20	11.20 - 12.10	12.10 - 01.00	01.00 - 01.50	01.50 - 02.40	02.40 - 03.30	03.30 - 04.20	04.20 - 05.10
Monday		-		LUNCH				
Tuesday		-	-					
Wednesday	-							
Thursday		-			-			-
Friday	-	-	-		-			

Signature of HOD

Date:

Signature of Class Coordinator

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

Students Name List

Roll Number	Registration Number	Name of the Student
UG/02/BTEE/2020/001	AU-2020-0004481	Saptarshi Bhattacharjee
UG/02/BTEE/2020/002	AU/2020/0004560	Arkajyoti Das

Signature of HOD/Dean

Date:

Signature of Class Coordinator

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

COURSE PLAN

Target	60% (marks)
Level-1	50% (population)
Level-2	60% (population)
Level-3	70% (population)

1. Method of Evaluation

UG	**PG**
Continuous Assessment (50%)	Continuous Assessment (50%)
End Semester Examination (50%)	End Semester Examination (50%)

*Keep as per Program (UG/PG)

2. Passing Criteria

Scale	**PG**	**UG**
Out of 10 Point Scale	CGPA – "5.00" Min. Individual Course Grade – "C" Passing Minimum – 40	CGPA – "5.00" Min. Individual Course Grade – "C" Passing Minimum – 35

*Keep as per Program (UG/PG)

3. Pedagogy

- Direct Instruction
- Kinesthetic Learning
- Flipped Classroom
- Differentiated Instruction
- Expeditionary Learning
- Inquiry Based Learning
- Game Based Learning
- Personalized Learning

4. Topics introduced for the first time in the program through this course

- (New Experiments Introduced & Content Beyond Syllabus)

5. References:

Text Books	Web resources	Journals	Reference books
2	0	0	0

Signature of HOD/Dean — **Signature of Faculty**

Date: — **Date:**

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra		**Course Code: EEE42201**
7. Course	**: Electric Circuits Lab**	**L: 0**
8. Program	**: B.Tech**	**T: 0**
9. Target	**: 60%**	**P: 3**
		C: 2

GUIDELINES TO STUDY THE SUBJECT

Instructions to Students:

1. Go through the 'Syllabus' in the LMS in order to find out the Reading List.
2. Get your schedule and try to pace your studies as close to the timeline as possible.
3. Get your on-line SmartLab videos section. Make sure you use them during this course.
4. check your LMS regularly
5. go through study material
6. check mails and announcements on blackboard
7. keep updated with the posts, assignments and examinations which shall be conducted on the blackboard
8. Be regular, so that you do not suffer in any way
9. C**ell Phones and other Electronic Communication Devices:** Cell phones and other electronic communication devices (such as Blackberries/Laptops) are not permitted in classes during Tests or the Mid/Final Examination. Such devices MUST be turned off in the class room.
10. **E-Mail and online learning tool:** Each student in the class should have an e-mail id and a pass word to access the LMS system regularly. Regularly, important information – Date of conducting class tests, guest lectures, via online learning tool. The best way to arrange meetings with us or ask specific questions is by email and prior appointment. All the assignments preferably should be uploaded on online learning tool. Various research papers/reference material will be mailed/uploaded on online learning platform time to time.
11. **Attendance:** Students are required to have minimum attendance of 75% in each subject. Students with less than said percentage shall NOT be allowed to appear in the end semester examination.

This much should be enough to get you organized and on your way to having a great semester! If you need us for anything, send your feedback through e-mail rituparna.mitra@adamasuniversity.ac.in Please use an appropriate subject line to indicate your message details.

There will no doubt be many more activities in the coming weeks. So, to keep up to date with all the latest developments, please keep visiting this website regularly.

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

RELATED OUTCOMES

1. **The expected outcomes of the Program are:**

PO1	**Engineering Knowledge:** Apply the knowledge of mathematics, science, engineering Fundamentals, and an engineering specialization to the solution of complex engineering problems.
PO2	**Problem Analysis:** Identify, formulate, review research literature, and analyse complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
PO3	**Design/ Development of Solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
PO4	**Conduct Investigations of Complex Problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
PO5	**Modern Tool Usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.
PO6	**The Engineer and Society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
PO7	**Environment and Sustainability:** Understand the impact of professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
PO8	**Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
PO9	**Individual and Team Work**: Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
PO10	**Communication:** Communicate effectively in complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.
PO11	**Project Management and Finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and multidisciplinary environments.
PO12	**Life-Long Learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

2. The expected outcomes of the Specific Program are: (up to 3)

PSO1	Engage in analysis and design of various power system components and their applications in the field of Electrical Engineering.
PSO2	Apply the domain knowledge of Electrical Engineering to solve problems for development of society, and/ or pursue higher education and research.
PSO3	Engage in lifelong learning and adapt to changing professional and societal needs.

3. **The expected outcomes of the Course are: (minimum 4 and maximum 6)**

CO1	Understand different network theorems required to analyze complex problems.
CO2	Explain differences between various dependent sources.
CO3	Know the significance of average value, rms value, form factor, peak factor of sinusoidal wave and square wave using hardware and verification using MATLAB.
CO4	Learn about Z, Y, ABCD Parameters of a two-port network.
CO5	Obtain the knowledge about R-L-C circuit response in Series parallel combination.

4. Co-Relationship Matrix

Indicate the relationships by 1- Slight (Low) 2- Moderate (Medium) 3-Substantial (High)

Program Outcomes / Course Outcomes	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12	PSO 1	PSO 2	PSO 3
CO1	3	3	1	-	3	-	-	-	-	-	-	3	-	3	-
CO2	3	3	-	-	3	-	-	-	-	-	-	-	3	3	-
CO3	-	3	3	-	3	-	-	-	-	-	-	-	3	3	-
CO4	-	-	3	3	3	-	-	-	-	-	-	-	-	3	-
CO5	-	2	3	-	3	-	-	-	-	-	-	-	1	3	-

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

Average	**3**	**3**	**3**	**3**	**3**	-	-	-	-	-	-	**3**	**3**	**3**	-

5. **Course Outcomes Assessment Plan (COA):**

Course Outcomes	**Continuous Assessment* (50 Marks)**		**End Term Exam (50 Marks)**	**Total (100 Marks)**
	Cycle I	**Cycle II**		
CO1	10	NA	10	20
CO2	10	NA	12	22
CO3	NA	10	08	18
CO4	NA	10	11	21
CO5	NA	10	09	19
Total	20	30	50	100

* Internal Assessment – Continuous Assessment

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

OVERVIEW OF COURSE PLAN OF COURSE COVERAGE

Course Activities:

S. No.	Description	Planned			Actual			Remarks
		From	To	No. of Session	From	TO	No. of Session	
1.	Cycle I Experiments	07/09/2021	04/01/2022					
2.	Cycle II Experiments	18/01/2022	22/02/2022					

Total No. of Instructional periods available for the course: ____ Sessions

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** **L: 0**
8. **Program : B.Tech** **T: 0**
9. **Target : 60%** **P: 3**

C: 2

SESSION PLAN

Session Plan				Actual Delivery			
Exp.	Date	Topics to be Covered	CO Mapped	Exp.	Date	Topics Covered	CO Achieved
1	07/09/2021	Verification of Kirchhoff's current law and voltage law	CO1				
2	14/09/2021	Verification of reciprocity theorem	CO4				
3	21/09/2021	Verification of Milliman's theorem	CO1				
4	28/09/2021	Verification of circuits containing dependent sources	CO5				
5	05/10/2021	Determination of average value, rms value, form factor, peak factor of sinusoidal wave and square wave	CO1				
6	26/10/2021	Verification of Z, Y, ABCD Parameters of a two-port network	CO2				
7	02/11/2021	Verification of hybrid parameters of a two-port network	CO3				
8	09/11/2021	Verification of series resonance	CO3				
9	16/11/2021	Verification of parallel resonance	CO3				

Cycle-I

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** **L: 0**
8. **Program : B.Tech** **T: 0**
9. **Target : 60%** **P: 3**

C: 2

Remarks: **Signature of Faculty** **Date:**

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** **L: 0**
8. **Program : B.Tech** **T: 0**
9. **Target : 60%** **P: 3**

C: 2

SESSION PLAN
Cycle-II

Session Plan				Actual Delivery			
Exp.	Date	Topics to be Covered	CO Mapped	Exp.	Date	Topics Covered	CO Achieved
1	18/01/2022	Observe transient response of first order R-L circuit	CO2				
2	25/01/2022	Observe transient response of first order R-C circuit	CO2				
3	08/02/2022	Observe transient response of second order R-L-C circuit	CO4				

Remarks:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

PERIODIC MONITORING

Attainment of the Course (Learning) Outcomes:

Components	Attainment level	Action Plan	Remarks
Cycle I Continuous Assessment	CO1:	Quiz/Test on 21.12.2021	
	CO2:		
	CO3:	NA	
	CO4:		
	CO5:		
Cycle II Continuous Assessment	CO1:	NA	
	CO2:		
	CO3:	Quiz/Test on 22.02.2022	
	CO4:		
	CO5:		
End Semester	CO1:	Will held on 07.03.2022	
	CO2:		
	CO3:		
	CO4:		
	CO5:		
Any Other	CO1:	NA	
	CO2:		
	CO3:		
	CO4:		
	CO5:		

Signature of HOD/ Dean

Date

Signature of Faculty

Date

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Continuous Evaluation Sheet

Roll Number	Registration Number	Name of the Student	Continuous Assessment*								
			Cycle I (25)					Cycle II (25)			
			Ex 1	Ex 2	Ex 3	Ex 4	Ex 5	Ex 6	Ex 7	etc.	Total (50)

*Depends on Number of Experiments Divide the Total Marks and Prepare Rubrics for Evaluating Experiments

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Planning for Remedial Classes

Sl. No.	Name of Student	Roll No.	Reg. No.	Mid Sem Marks	Remedial Classes Held							Retest on the basis of Remedial Classes	End Sem Marks	Improvement (Y/N)
					Date									
					Venue									
					Time									
1.														
2.														

Signature of HOD/ Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**, **C: 2**

COURSE END SURVEY

INDIRECT ASSESSMENT

Sample format for Indirect Assessment of Course outcomes:

NAME:
ROLL NO.:
REG. NO..:
COURSE:
PROGRAM:

Please rate the following aspects of course outcomes of

Use the scale 1-5 (Poor – Excellent) *

Course Outcomes	Statement	1	2	3	4	5
CO1						
CO2						
CO3						
CO4						
CO5						

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

INDIRECT ASSESSMENT CONSOLIDATION

ADAMAS UNIVERSITY, KOLKATA SCHOOL OF DEPARTMENT OF CO Indirect Assessment		
Programme: **Academic Year:2020-21** **Batch: 2020-22**		
Course Code & Name:		
Course Outcome	**Students Feed Back (5)**	**Attainment (100)**
CO1		
CO2		
CO3		
CO4		
CO5		
etc.		
Signature of HOD/Dean **Date:**		**Signature of Faculty** **Date:**

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Evaluation Sheet (End Semester)

Roll Number	Registration Number	Name of the Student	Marks (50)

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Planning for Remedial Classes – End Semester

Sl. No.	Name of Student	Roll No.	Reg. No.	End Sem Marks	Remedial Classes Held							Retest on the basis of Remedial Classes	Supple Exam Marks	Improvement (Y/N)
					Date									
					Venue									
					Time									
1.														
2.														

Signature of HOD/ Dean

Date

Signature of Faculty

Date

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

Consolidated Mark Statement

Roll Number	Registration Number	Name of the Student	Marks			
			Continuous Assessment (50)		End Semester (50)	Total (100)
			Cycle I (25)	Cycle II (25)		

Signature of Dean/HOD

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. **Name of the Faculty: Dr Rituparna Mitra** — **Course Code: EEE42201**
7. **Course : Electric Circuits Lab** — **L: 0**
8. **Program : B.Tech** — **T: 0**
9. **Target : 60%** — **P: 3**

C: 2

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

CO ATTAINMENT – GAP ANALYSIS & REMEDIAL MEASURES

ADAMAS UNIVERSITY, KOLKATA
SCHOOL OF
DEPARTMENT OF
CO ATTAINMENT - GAP ANALYSIS & REMEDIAL MEASURES

Batch :	2020-22		Academic Year: 2020-21
Course Code & Name		Name of the Coordinator	Year & Semester
			I & I

CO	Direct Assessment	Indirect Assessment	CO Attainment	Target	CO Attainment Gaps	Action for Bridge the Gap	Target Modification
CO1							
CO2							
CO3							
CO4							
CO5							

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

CO-PO ATTAINMENT

ADAMAS UNIVERSITY, KOLKATA
SCHOOL OF
DEPARTMENT OF
CO-PO ATTAINMENT

Programme :	Year & Sem: I & I	Academic Year: 2020-21	Batch:2020-22

Course Code	Course Name	CO-PO	PO 1	PO 2	PO 3	PO4	PO5	PO6	PO 7	PO8	PO 9	PO 10	PO 11	PO 12	PSO 1	PSO 2	PSO 3
		Relationship															
		Mapping Value															
		Attainment															

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra — Course Code: EEE42201
7. Course : Electric Circuits Lab — L: 0
8. Program : B.Tech — T: 0
9. Target : 60% — P: 3
C: 2

PO ATTAINMENT OF THE COURSE

Signature of HOD/Dean

Date:

Signature of Faculty

Date:

Year: II

Semester: I

6. Name of the Faculty: Dr Rituparna Mitra		**Course Code: EEE42201**
7. Course	**: Electric Circuits Lab**	**L: 0**
8. Program	**: B.Tech**	**T: 0**
9. Target	**: 60%**	**P: 3**
		C: 2

INSTRUCTIONS FOR FACULTY

Instructions for Faculty

- Faculty should keep track of the students with low attendance and counsel them regularly.
- Course coordinator will arrange to communicate the short attendance (as per University policy) cases to the students and their parents monthly.
- Experiment covered in each lab should be recorded in the table of RECORD OF CLASS TEACHING (Suggested Format).
- Internal assessment marks should be communicated to the students twice in a semester.
- The file will be audited by respective Academic Monitoring and Review Committee (AMRC) members for theory as well as for lab as per AMRC schedule.
- The faculty is required to maintain these files for a period of at least three years.
- This register should be handed over to the head of department, whenever the faculty member goes on long leave or leaves the Colleges/University.
- For labs, continuous evaluation format (break-up given in the guidelines for result preparation in the same file) should be followed.
- Department should monitor the actual execution of the components of continuous lab evaluation regularly.
- Instructor should maintain record of experiments conducted by the students in the lab weekly.
- Instructor should promote students for self-study and to make concept diary, due weightage in the internal should be given under faculty assessment for the same.
- Course outcome assessment: To assess the fulfilment of course outcomes two different approaches have been decided. Degree of fulfillment of course outcomes will be assessed in different ways through direct assessment and indirect assessment. In Direct Assessment, it is measured through quizzes, tests, assignment, Mid-term and/or End-term examinations. It is suggested that each examination is designed in such a way that it can address one or two outcomes (depending upon the course completion). Indirect assessment is done through the student survey which needs to be designed by the faculty (sample format is given below) and it shall be conducted towards the end of course completion. The evaluation of the achievement of the Course Outcomes shall be done by analyzing the inputs received through Direct and Indirect Assessments and then corrective actions suggested for further improvement.
- **Submission Targets of Course Contents:**
  - **S. No. 1 to 7 : Before Starting the Course**
  - **S. No. 8 & 9 : After Mid Semester Examination**
  - **S. No. 10 to 13 : Immediately After End Semester Examination**
  - **S. No. 14 to 17 : After Declaration of Result of the Course**