



TO WHOMSOEVER IT MAY CONCERN

This is to certify that Samiddho Chatterjee (21BEC1133), has successfully completed his VLSI Design Internship Program from 22/08/2023 to 30/10/2023

During his internship program with us, he worked on the below mentioned project AHB2APB bridge design using Verilog HDL.

We wish him all the best in his future endeavours.

For Maven Silicon Softech Pvt Ltd.

Sweet madas (BANGALORE)

Sweety Dharamdasani

Head - Learning & Development

Place: Bengaluru Date:23/11/2023





CERTIFICATE OF COMPLETION



This is to certify that Samiddho Chatterjee (21BEC1133)

has successfully completed VLSI Design Methodologies

online course on 30th Oct 2023



MSUID: MS/VIT-DM+DI/SEP023