

OPEN POSITIONS:

PHYSICAL DESIGN ENGINEER (STAFF/SENIOR/FRESHER)

Hiring Headcount: 04

1. Fresher Physical Design Engineer

Responsibilities include, but are not limited to:

- Perform gate level netlist to GDS design independently including and not limit to floor planning, place & route, clock tree synthesis, timing sign off and physical verification.
- Perform design IP implementation, IR drop analysis, DFT, STA and foundry merge.
- Work with manager to achieve assigned tape out target.

Job requirements:

- Final-year students who are about to graduate or recent graduates in the following majors: Electronics & Telecommunications, Computer Engineering, Semiconductor Engineering or related fields.
- Solid knowledge of CMOS, Physical design, IC design flow.
- GPA of 7.5/10 or higher.
- Good English proficiency.
- Eager to learn, good logical thinking skills.
- A good team player.

2. Physical Design Engineer (Staff/Senior)

Responsibilities include, but are not limited to:

- Responsible for implementing RTL-to-GDSII process flow for block-level or full-chip SoCs.
- Communicate closely with the front-end design team to ensure the physical feasibility of the design modules.
- Analyze the backend design process for timing and congestion and optimize PPA from an implementation perspective.
- Responsible for various sign-off tasks, such as STA/IR/EM/PV, etc.
- Manage and track the backend status of team members, identify risks, provide effective solutions, and drive risk elimination.

Job requirements:

- Bachelor's degree or above, majoring in electronic engineering related to microelectronics, IC, electronic information, etc.
- From 3+ years of experience in digital backend related work; Experience in advanced process nodes below 7nm is preferred.

- Have complete experience in chip tapeout and be able to solve complex problems.
- Strong programming skills in TCL, Python, Perl, shell, etc.
- Good communication and smooth English reading and writing skills.
- Strong leadership skills are a plus.

3. Working Location

Hai Chau Ward, Da Nang City.

4. Benefits

- Competitive Salary & Attractive Benefits
- 14th Month Salary Bonus
- Full Social Insurance on Gross Salary
- International Working Environment
- Clear Career Roadmap in Semiconductors
- Global Career Opportunities

5. Application Method

Interested candidates are requested to submit your CV and university academic transcript (academic transcript applied for Fresher positions) to email address: stella_kieu@alchip.com.

Contact phone number: 0936 222 370 (call/zalo).

Shortlisted candidates will be contacted and invited for interviews via email/phone.

Join us and take your career to the next level!